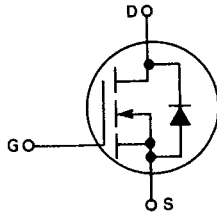
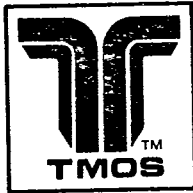


**IRF530**  
**IRF531**  
**IRF532**  
**IRF533**

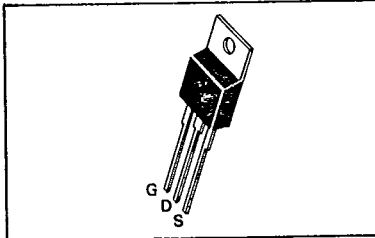
**N-CHANNEL ENHANCEMENT-MODE SILICON GATE  
 TMOS POWER FIELD EFFECT TRANSISTOR**

These TMOS Power FETs are designed for low voltage, high speed power switching applications such as switching regulators, converters, solenoid and relay drivers.

- Silicon Gate for Fast Switching Speeds
- Rugged — SOA is Power Dissipation Limited
- Source-to-Drain Diode Characterized for Use With Inductive Loads



Part Number	V <sub>DS</sub>	r <sub>DS(on)</sub>	I <sub>D</sub>
IRF530	100 V	0.18 Ω	14 A
IRF531	60 V	0.18 Ω	14 A
IRF532	100 V	0.25 Ω	12 A
IRF533	60 V	0.25 Ω	12 A



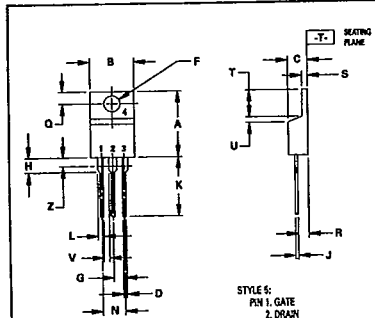
**MAXIMUM RATINGS**

Rating	Symbol	IRF				Unit
		530	531	532	533	
Drain-Source Voltage	V <sub>DSS</sub>	100	60	100	60	Vdc
Drain-Gate Voltage (R <sub>GS</sub> = 1.0 MΩ)	V <sub>DGR</sub>	100	60	100	60	Vdc
Gate-Source Voltage	V <sub>GS</sub>	± 20				Vdc
Continuous Drain Current T <sub>C</sub> = 25°C	I <sub>D</sub>	14	14	12	12	Adc
Continuous Drain Current T <sub>C</sub> = 100°C	I <sub>D</sub>	9.0	9.0	8.0	8.0	Adc
Drain Current — Pulsed	I <sub>DM</sub>	56	56	48	48	Adc
Total Power Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	75 0.6				Watts W/°C
Operating and Storage Temperature Range	T <sub>J,Tstg</sub>	-55 to 150				°C

**THERMAL CHARACTERISTICS**

Thermal Resistance Junction to Case	R <sub>θJC</sub>	1.67	°C/W
Junction to Ambient	R <sub>θJA</sub>	62.5	°C/W
Maximum Lead Temp. for Soldering Purposes, 1/8" from case for 5 seconds	T <sub>L</sub>	300	°C

See the MTM12N10 Designer's Data Sheet for a complete set of design curves for this product.



- NOTES:  
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.  
 2. CONTROLLING DIMENSION INCH.  
 3. DIM Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	14.48	15.75	0.570	0.620
B	3.66	13.78	0.280	0.465
C	4.67	4.82	0.180	0.190
D	0.64	0.98	0.025	0.039
F	3.61	3.71	0.142	0.147
G	2.42	2.68	0.096	0.106
H	2.80	3.83	0.110	0.151
J	0.26	0.55	0.014	0.022
K	13.75	14.27	0.500	0.562
L	1.15	1.29	0.045	0.051
N	4.83	5.33	0.190	0.210
O	2.54	3.04	0.100	0.120
R	2.04	2.29	0.080	0.110
S	1.15	1.29	0.045	0.051
T	5.97	6.47	0.235	0.255
U	0.00	1.27	0.000	0.050
V	1.15	—	0.045	—
Z	—	2.04	—	0.080

**CASE 221A-04  
 TO-220AB**

ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)

14E D

Characteristic	Symbol	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>					
Drain-Source Breakdown Voltage (V <sub>GS</sub> = 0, I <sub>D</sub> = 250 μA)	V <sub>(BR)DSS</sub>	100 60	—	—	V <sub>dc</sub>
Zero Gate Voltage Drain Current (V <sub>GS</sub> = 0 V, V <sub>DS</sub> = Rated V <sub>DSS</sub> ) (V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 0.8 Rated V <sub>DSS</sub> , T <sub>C</sub> = 125°C)	I <sub>DSS</sub>	—	—	0.25 1.0	mAdc
Forward Gate-Body Leakage Current (V <sub>GS</sub> = 20 V, V <sub>DS</sub> = 0)	I <sub>GSSF</sub>	—	—	100	nAdc
Reverse Gate-Body Leakage Current (V <sub>GS</sub> = -20 V, V <sub>DS</sub> = 0)	I <sub>GSSR</sub>	—	—	-100	nAdc

<b>ON CHARACTERISTICS*</b>					
Gate Threshold Voltage (V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA)	V <sub>GS(th)</sub>	2.0	—	4.0	V <sub>dc</sub>
On-State Drain Current (V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 10 V)	I <sub>D(on)</sub>	14 12	—	—	Adc
Static Drain-Source On-Resistance (V <sub>GS</sub> = 10 V, I <sub>D</sub> = 8.0 A)	r <sub>DS(on)</sub>	—	—	0.18 0.25	Ohm
Forward Transconductance (V <sub>DS</sub> = 15 V, I <sub>D</sub> = 8.0 A)	g <sub>FS</sub>	4.0	—	—	mhos

<b>DYNAMIC CHARACTERISTICS</b>					
Input Capacitance	(V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0, f = 1.0 MHz)	C <sub>iss</sub>	—	800	pF
Output Capacitance		C <sub>oss</sub>	—	500	
Reverse Transfer Capacitance		C <sub>rss</sub>	—	150	

<b>SWITCHING CHARACTERISTICS* (T<sub>J</sub> = 100°C)</b>					
Turn-On Delay Time	V <sub>DD</sub> = 36 V, I <sub>D</sub> = 8.0 A Z <sub>O</sub> = 15 Ω	t <sub>d(on)</sub>	—	30	ns
Rise Time		t <sub>r</sub>	—	75	
Turn-Off Delay Time		t <sub>d(off)</sub>	—	40	
Fall Time		t <sub>f</sub>	—	45	

<b>SOURCE DRAIN DIODE CHARACTERISTICS*</b>			
Characteristic	Symbol	Typ	Unit
Forward On-Voltage	V <sub>SD</sub>	2.3	V <sub>dc</sub>
Forward Turn-On Time	t <sub>on</sub>	Limited by stray inductance	
Reverse Recovery Time	t <sub>rr</sub>	360	ns

<b>INTERNAL PACKAGE INDUCTANCE (TO-220)</b>					
Symbol	Min	Typ	Max	Unit	
Internal Drain Inductance (Measured from the contact screw on tab to center of die) (Measured from the drain lead 0.25" from package to center of die)	L <sub>d</sub>	—	3.5 4.5	—	nH
Internal Source Inductance (Measured from the source lead 0.25" from package to source bond pad.)	L <sub>s</sub>	—	7.5	—	

\*Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0 %.

FIGURE 1 — SWITCHING TEST CIRCUIT

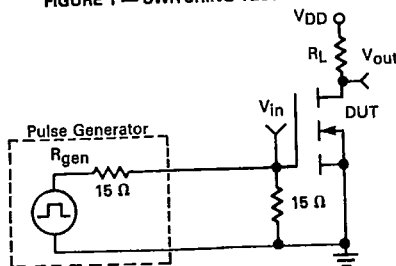


FIGURE 2 — SWITCHING WAVEFORMS

