

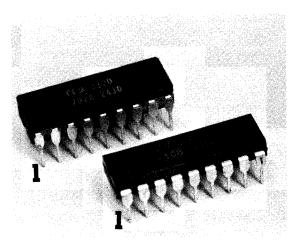
CEM 3320

CURTIS ELECTROMUSIC SPECIALTIES

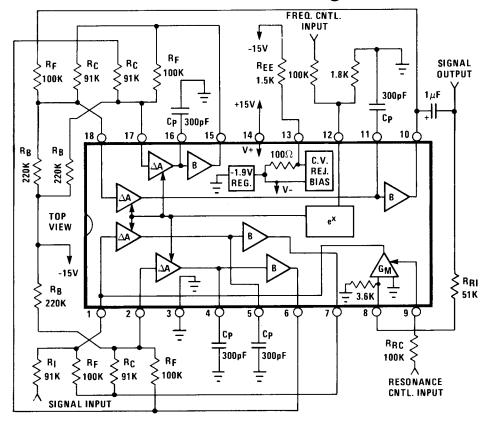
Voltage Controlled Filter

The CEM 3320 is a high performance voltage controlled four-pole filter complete with on-chip voltage controllable resonance. The four independent sections may be interconnected to provide a wide variety of filter responses, such as low pass, high pass, band pass and all pass. A single input exponentially controls the frequency over greater than a ten octave range with little control voltage feedthrough. Another input controls the resonance in a modified linear manner from zero to low distortion oscillation. For those

demanding applications, provision has been made to allow trimming for improved control voltage rejection. Each filter section features a novel variable gain cell which, unlike the traditional cell, is fully temperature compensated, exhibits a better signal-to-noise ratio and generates its low distortion predominantly in the second harmonic. The device further includes a minus two volt regulator to ensure low power dissipation and consequent low warm-up drift even with ±15 volt supplies.



Circuit Block and Connection Diagram



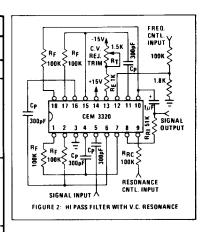
Features

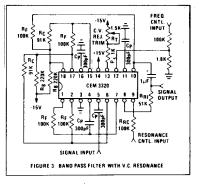
- Low Cost
- Voltage Controllable Frequency: 12 octave range minimum
- Voltage Controllable Resonance: From zero to oscillation
- Accurate Exponential Frequency Scale
- Accurate Linear Resonance Scale
- Low Control Voltage Feedthrough: -45dB typical
- Filter Configurable into Low Pass, High Pass, All Pass, etc.
- Large Output: 12V.P.P. typical
- Low Noise: -86dB typical
- Low Distortion in Passband: 0.1% typical
- Low Warm Up Drift
- Configurable into Low Distortion Voltage Controlled Sine Wave Oscillator
- ±15 Volt Supplies

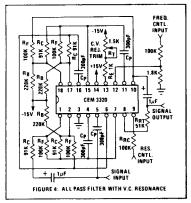
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Electrical Characteristics

| V _{CC} = +15V | $R_F = 100K$ $T_A = 25^{\circ}C$ | | | |
|---|----------------------------------|--------------------------|---------------------------|--------------------------|
| Parameter | Min. | Тур. | Max. | Units |
| Pole Frequency Control Range | 3500:1 | 10,000:1 | _ | |
| Sensitivity of Pole Frequency Control Scale, Midrange | 57.5 | 60 | 62.5 | mV/decade |
| Tempco of Pole Frequency Control Scale | 3000 | 3300 | 3600 | ppm |
| Exponential Error of Pole Frequency Control Scale 1 | _ | 4 | 12 | % |
| Gain of Variable Gain Cell at V _C =0 Max Gain of Variable Gain Cell Tempco of Variable Gain Cell ² Output Impedance of Gain Cell ² | 0.7 2.4 - 0.5 | 0.9 3.0 500 1.0 | 1.3 3.6 1500 2.0 | ppm MΩ |
| Pole Frequency Control Feedthrough Pole Frequency Warm-up Drift | _ _ _ | 60 .5 | 200 1.5 | mV % |
| Gm of Resonance Control Element at I _{CR} =100uA Amount of Resonance Obtainable | .8 | 1.0 | 1.2 | mmhos |
| Before Oscillation Resonance Control Feedthrough ³ | 20 — | 30 0.2 | _ 1.5 | d B V |
| Output Swing At Clipping Output Noise re Max Output ⁴ Rejection in Bandreject | 10 -76 73 | 12 -86 83 | 14 - - | V.P.P. dB dB |
| Distortion in Passband ^{5,7} Distortion in Bandreject ^{6,7} Distortion of Sine Wave | <u>-</u> | 0.1 0.3 | 0.3 | % % |
| Oscillation ⁸ | | 0.5 | 1.5 | % |
| Internal Reference Current, IREF Input Bias Current of Frequency Control Input | 45 0.2 | 63 0.5 | 85 1.5 | μ Α μ Α |
| Input Impedance to Resonance Signal Input | 2.7 | 3.6 | 4.5 | ΚΩ |
| Buffer Slew Rate | 1.5 | 3.0 | _ | V/uS |
| Buffer Input Bias Current (IEE=8mA) Buffer Sink Capability Buffer Output Impedance ² | ±8 .4 75 | ±30 .5 100 | ±100 .63 200 | nA mA Ω |
| Positive Supply Range Negative Supply Range ⁹ Positive Supply Current | +9 -4 3.8 | _ _ 5 | +18 -18 6.5 | V V mA |







- Note 1: -25mV < V $_{C}$ < +155mV. Most of this error occurs in upper two octaves.
- Note 2: $V_C = 0$
- Note 3: Untrimmed. $0 < I_{CR} < 100 \mu A$
- Note 4: Filter is connected as low pass and set for 20 KHz cut-off frequency.
- Note 5: Output signal is 3dB below clipping point.
- Note 6: Output signal is 3dB below passband level, which is 3dB below clipping point. In general, this is worst case condition.
- Note 7: Distortion is predominantly second harmonic.
- Note 8: Sinewave is not clipped by first stage.
- Note 9: Current limiting resistor always required.



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