

BIPOLAR ANALOG INTEGRATED CIRCUIT

μ PC4092

J-FET INPUT LOW-OFFSET DUAL OPERATIONAL AMPLIFIER

The μ PC4092 dual operational amplifier offers high input impedance, low offset voltage, high slew rate, and stable AC operating characteristics. NEC's unique high-speed PNP transistor ($f_T = 300$ MHz) in the output stage solves the oscillation problem of current sinking with a large capacitive load. Zener-zap resistor trimming in the input stage produces excellent offset voltage and temperature drift characteristics.

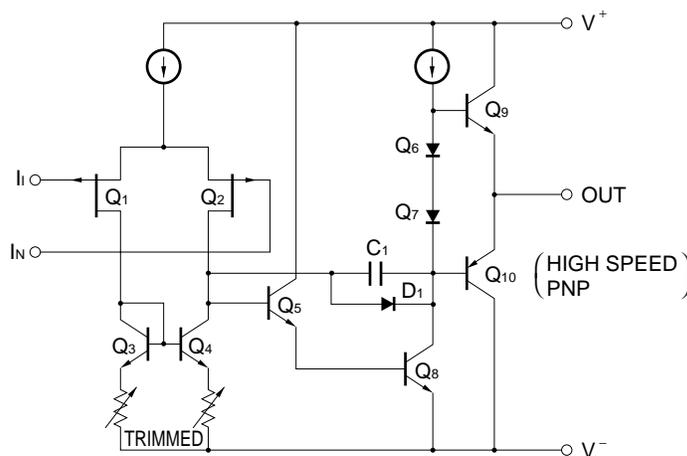
FEATURES

- Stable operation with 10000 pF capacitive load
- Low input offset voltage
 ± 3 mV (MAX.)
 ± 7 μ V/ $^{\circ}$ C (TYP.) temperature drift
- Very low input bias and offset currents
- Low noise : $e_n = 19$ nV/ $\sqrt{\text{Hz}}$ (TYP.)
- Output short circuit protection
- High input impedance ... J-FET Input Stage
- Internal frequency compensation
- High slew rate: 15 V/ μ s (TYP.)

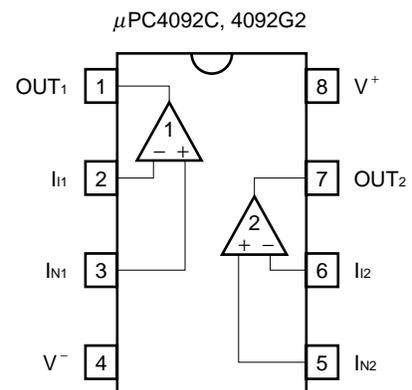
ORDERING INFORMATION

| Part Number | Package |
|----------------|-----------------------------|
| μ PC4092C | 8-pin plastic DIP (300 mil) |
| μ PC4092G2 | 8-pin plastic SOP (225 mil) |

EQUIVALENT CIRCUIT (1/2 Circuit)



PIN CONFIGURATION (Top View)



The information in this document is subject to change without notice.

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C)

| Parameter | | Symbol | Ratings | Unit |
|---|------------------------------|---------------------------------|--|------|
| Voltage between V ⁺ and V ⁻ ^{Note 1} | | V ⁺ - V ⁻ | -0.3 to +36 | V |
| Differential Input Voltage | | V _{ID} | ±30 | V |
| Input Voltage ^{Note 2} | | V _I | V ⁻ -0.3 to V ⁺ +0.3 | V |
| Output Voltage ^{Note 3} | | V _O | V ⁻ -0.3 to V ⁺ +0.3 | V |
| Power Dissipation | C Package ^{Note 4} | P _T | 350 | mW |
| | G2 Package ^{Note 5} | | 440 | mW |
| Output Short Circuit Duration ^{Note 6} | | | Indefinite | sec |
| Operating Ambient Temperature | | T _A | -20 to +80 | °C |
| Storage Temperature | | T _{stg} | -55 to +125 | °C |

- Notes**
- Reverse connection of supply voltage can cause destruction.
 - The input voltage should be allowed to input without damage or destruction. Even during the transition period of supply voltage, power on/off etc., this specification should be kept. The normal operation will establish when the both inputs are within the Common Mode Input Voltage Range of electrical characteristics.
 - This specification is the voltage which should be allowed to supply to the output terminal from external without damage or destructive. Even during the transition period of supply voltage, power on/off etc., this specification should be kept. The output voltage of normal operation will be the Output Voltage Swing of electrical characteristics.
 - Thermal derating factor is -5.0 mV/°C when operating ambient temperature is higher than 55 °C.
 - Thermal derating factor is -4.4 mV/°C when operating ambient temperature is higher than 25 °C.
 - Pay careful attention to the total power dissipation not to exceed the absolute maximum ratings, Note 4 and Note 5.

RECOMMENDED OPERATING CONDITIONS

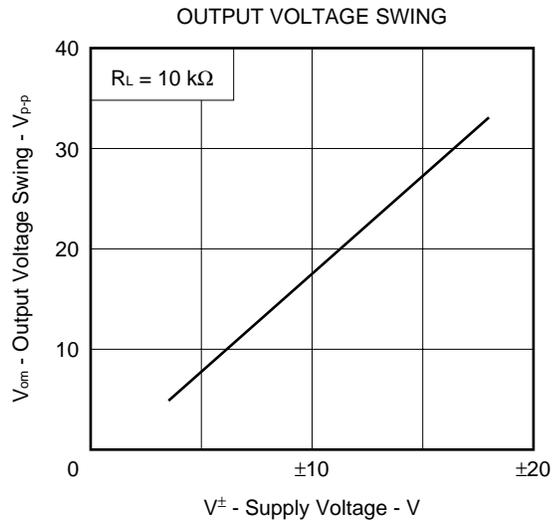
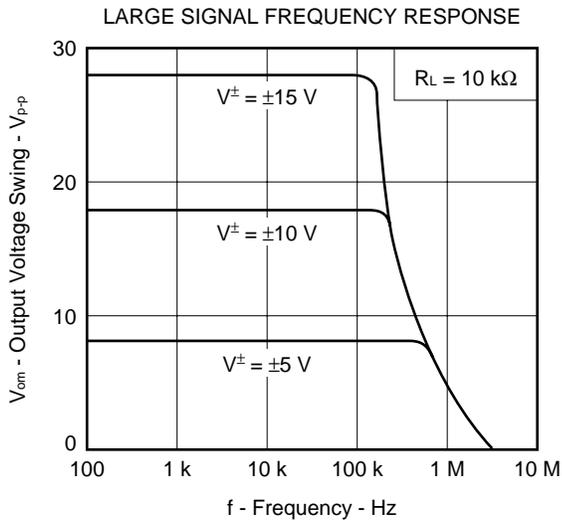
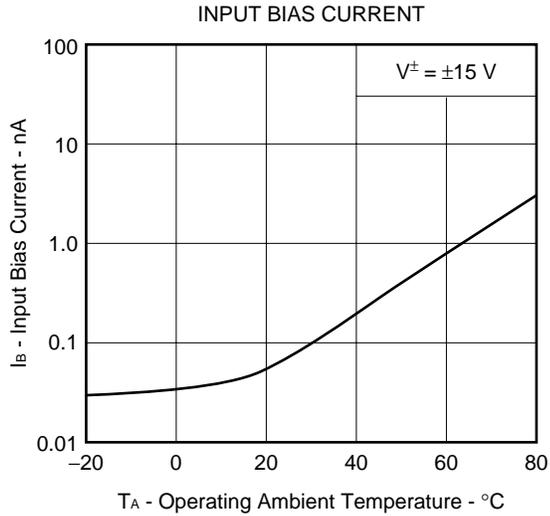
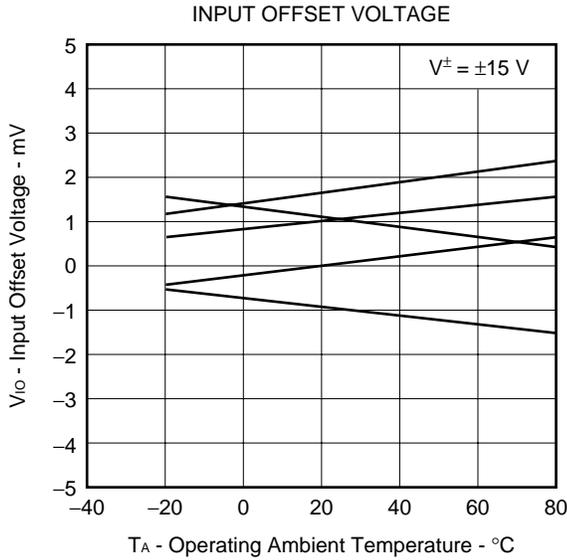
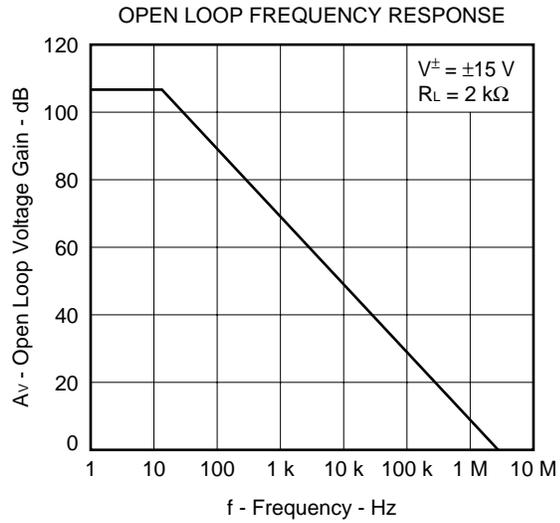
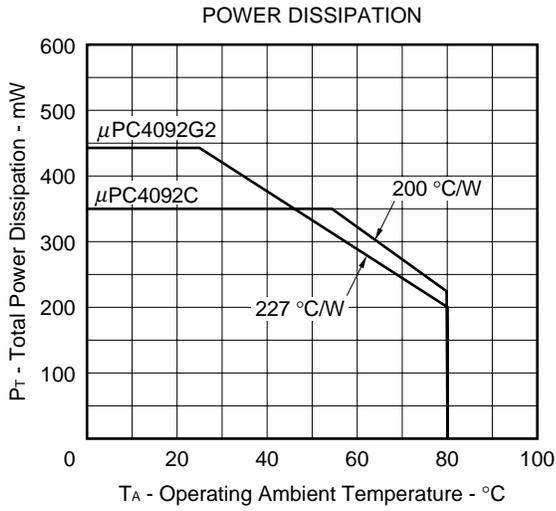
| Parameter | Symbol | MIN. | TYP. | MAX. | Unit |
|---|----------------|------|------|-------|------|
| Supply Voltage | V [±] | ±5 | | ±16 | V |
| Output Current | I _o | | | ±10 | mA |
| Capacitive Load (A _v = +1, R _f = 0 Ω) | C _L | | | 10000 | pF |

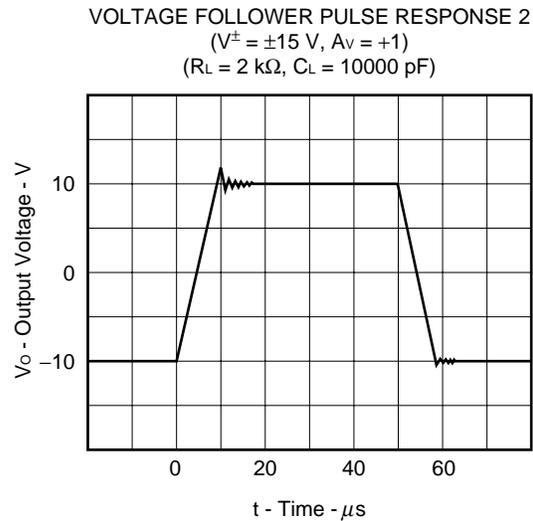
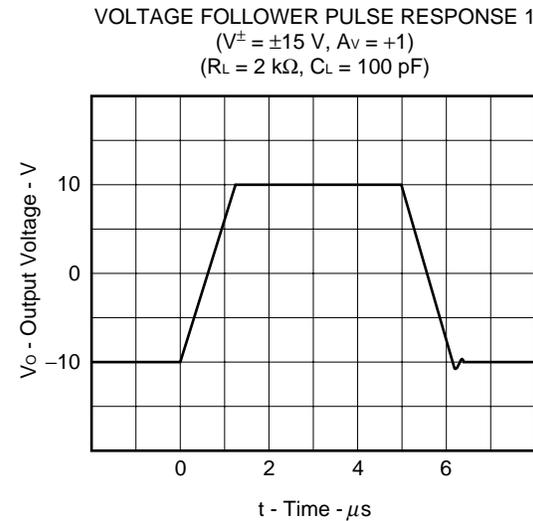
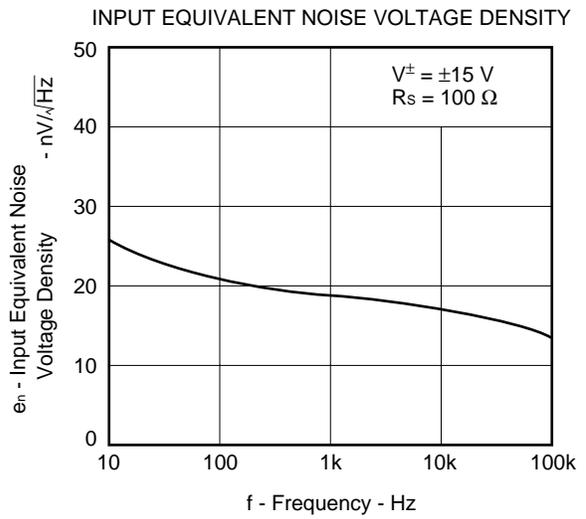
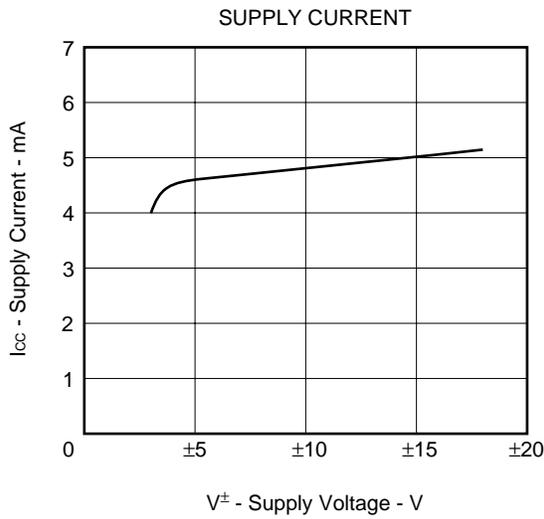
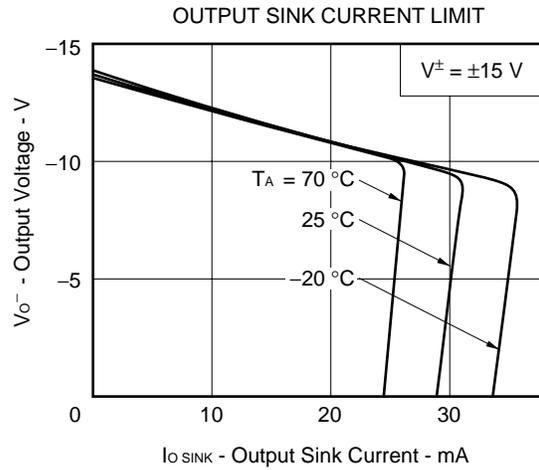
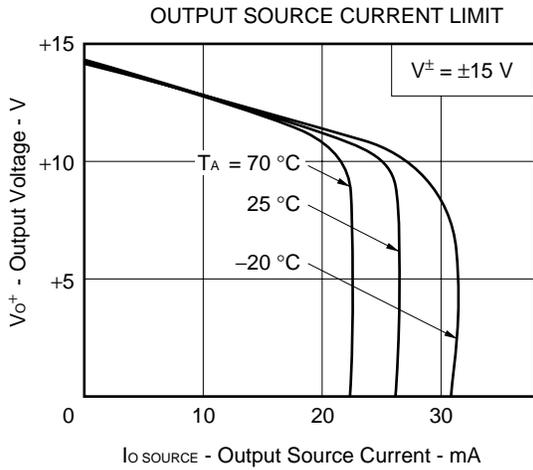
ELECTRICAL CHARACTERISTICS (T_A = 25 °C, V[±] = ±15 V)

| Parameter | Symbol | Conditions | MIN. | TYP. | MAX. | Unit |
|---|----------------------|---|-------|----------------|------|--------|
| Input Offset Voltage | V _{IO} | R _s ≤ 50 Ω | | ±1 | ±3 | mV |
| Input Offset Current ^{Note 7} | I _{IO} | | | ±25 | ±100 | pA |
| Input Bias Current ^{Note 7} | I _B | | | 50 | 200 | pA |
| Large Signal Voltage Gain | A _V | R _L ≥ 2 kΩ, V _O = ±10 V | 25000 | 200000 | | |
| Supply Current | I _{CC} | I _O = 0 A, Both Amplifiers | | 5 | 6.8 | mA |
| Common Mode Rejection Ratio | CMR | | 70 | 100 | | dB |
| Supply Voltage Rejection Ratio | SVR | | 70 | 100 | | dB |
| Output Voltage Swing | V _{om} | R _L ≥ 10 kΩ | ±12 | +14.0 -13.3 | | V |
| | | R _L ≥ 2 kΩ | ±10 | +13.5 -12.8 | | V |
| Common Mode Input Voltage Range | V _{ICM} | | ±11 | +14 -12 | | V |
| Slew Rate | SR | A _V = 1 | | 15 | | V/μs |
| Unity Gain Frequency | f _{unity} | | | 4 | | MHz |
| Input Equivalent Noise Voltage Density | e _n | R _s = 100 Ω, f = 1 kHz | | 19 | | nV/√Hz |
| Channel Separation | | | | 120 | | dB |
| Input Offset Voltage | V _{IO} | R _s ≤ 50 Ω, T _A = -20 to +70 °C | | | ±5 | mV |
| Average V _{IO} Temperature Drift | ΔV _{IO} /ΔT | T _A = -20 to +70 °C | | ±7 | | μV/°C |
| Input Offset Current ^{Note 7} | I _{IO} | T _A = -20 to +70 °C | | | ±2 | nA |
| Input Bias Current ^{Note 7} | I _B | T _A = -20 to +70 °C | | | 7 | nA |

Notes 7. Input bias currents flow into IC. Because each currents are gate leak current of P-channel J-FET on input stage. And that are temperature sensitive. Short time measuring method is recommendable to maintain the junction temperature close to the operating ambient temperature.

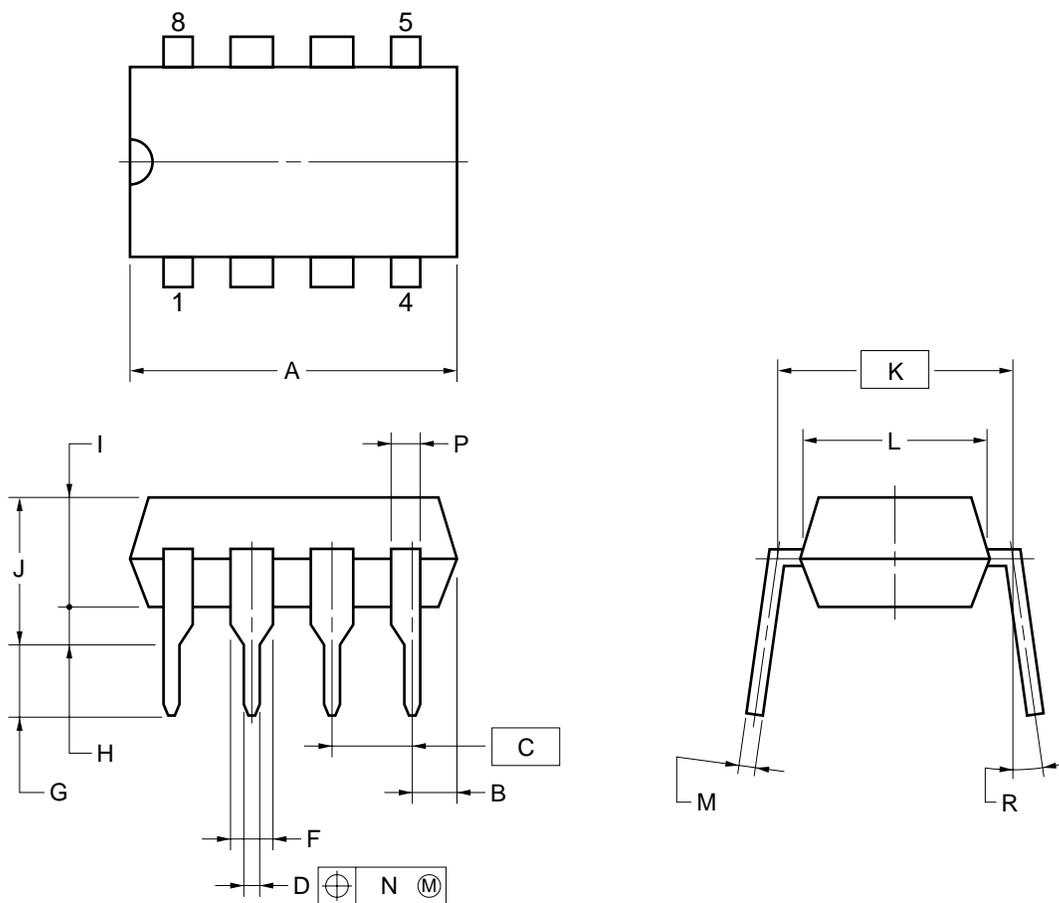
TYPICAL PERFORMANCE CHARACTERISTICS ($T_A = 25^\circ\text{C}$, TYP.)





PACKAGE DRAWINGS

8PIN PLASTIC DIP (300 mil)



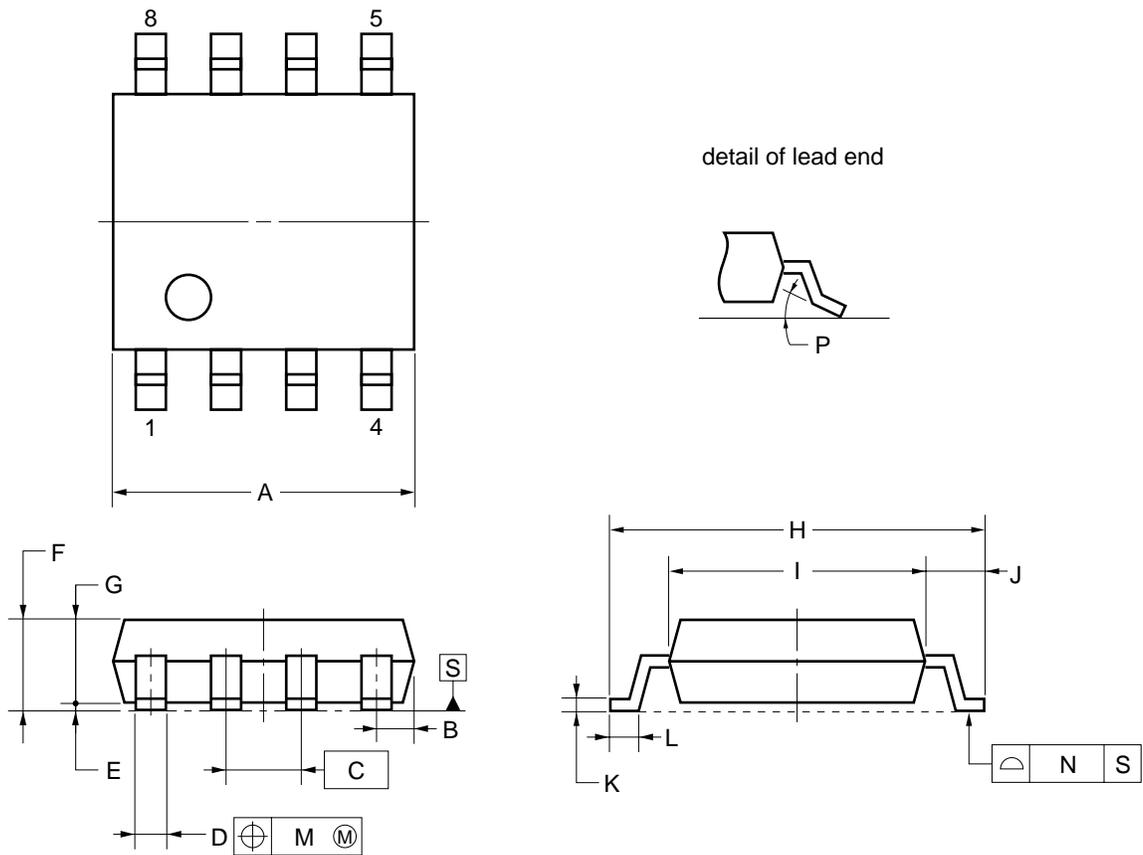
NOTES

- 1) Each lead centerline is located within 0.25 mm (0.01 inch) of its true position (T.P.) at maximum material condition.
- 2) Item "K" to center of leads when formed parallel.

| ITEM | MILLIMETERS | INCHES |
|------|--|---|
| A | 10.16 MAX. | 0.400 MAX. |
| B | 1.27 MAX. | 0.050 MAX. |
| C | 2.54 (T.P.) | 0.100 (T.P.) |
| D | 0.50±0.10 | 0.020 ^{+0.004} _{-0.005} |
| F | 1.4 MIN. | 0.055 MIN. |
| G | 3.2±0.3 | 0.126±0.012 |
| H | 0.51 MIN. | 0.020 MIN. |
| I | 4.31 MAX. | 0.170 MAX. |
| J | 5.08 MAX. | 0.200 MAX. |
| K | 7.62 (T.P.) | 0.300 (T.P.) |
| L | 6.4 | 0.252 |
| M | 0.25 ^{+0.10} _{-0.05} | 0.010 ^{+0.004} _{-0.003} |
| N | 0.25 | 0.01 |
| P | 0.9 MIN. | 0.035 MIN. |
| R | 0~15° | 0~15° |

P8C-100-300B,C-1

8 PIN PLASTIC SOP (225 mil)



NOTE
 Each lead centerline is located within 0.12 mm of its true position (T.P.) at maximum material condition.

| ITEM | MILLIMETERS |
|------|--|
| A | 5.2 ^{+0.17} _{-0.20} |
| B | 0.78 MAX. |
| C | 1.27 (T.P.) |
| D | 0.42 ^{+0.08} _{-0.07} |
| E | 0.1±0.1 |
| F | 1.59±0.21 |
| G | 1.49 |
| H | 6.5±0.3 |
| I | 4.4±0.15 |
| J | 1.1±0.2 |
| K | 0.17 ^{+0.08} _{-0.07} |
| L | 0.6±0.2 |
| M | 0.12 |
| N | 0.10 |
| P | 3° ^{+7°} _{-3°} |

S8GM-50-225B-5

RECOMMENDED SOLDERING CONDITIONS

When soldering this product, it is highly recommended to observe the conditions as shown below. If other soldering processes are used, or if the soldering is performed under different conditions, please make sure to consult with our sales offices.

For more details, refer to our document "**SEMICONDUCTOR DEVICE MOUNTING TECHNOLOGY MANUAL**"(C10535E).

Type of Surface Mount Device

μPC4092G2: 8-pin plastic SOP (225 mil)

| Process | Conditions | Symbol |
|------------------------|---|-----------|
| Infrared Ray Reflow | Peak temperature: 230 °C or below (Package surface temperature), Reflow time: 30 seconds or less (at 210 °C or higher), Maximum number of reflow processes: 1 time. | IR30-00-1 |
| Vapor Phase Soldering | Peak temperature: 215 °C or below (Package surface temperature), Reflow time: 40 seconds or less (at 200 °C or higher), Maximum number of reflow processes: 1 time. | VP15-00-1 |
| Wave Soldering | Solder temperature: 260 °C or below, Flow time: 10 seconds or less, Maximum number of flow processes: 1 time, Pre-heating temperature: 120 °C or below (Package surface temperature). | WS60-00-1 |
| Partial Heating Method | Pin temperature: 300 °C or below, Heat time: 3 seconds or less (Per each side of the device). | — |

Caution Apply only one kind of soldering condition to a device, except for "partial heating method", or the device will be damaged by heat stress.

Type of Through-hole Device

μPC4092C: 8-pin plastic DIP (300 mil)

| Process | Conditions |
|-----------------------------------|--|
| Wave Soldering (only to leads) | Solder temperature: 260 °C or below, Flow time: 10 seconds or less. |
| Partial Heating Method | Pin temperature: 300 °C or below, Heat time: 3 seconds or less (per each lead). |

Caution For through-hole device, the wave soldering process must be applied only to leads, and make sure that the package body does not get jet soldered.

REFERENCE DOCUMENTS

| | |
|---|----------|
| QUALITY GRADES ON NEC SEMICONDUCTOR DEVICES | C11531E |
| SEMICONDUCTOR DEVICE MOUNTING TECHNOLOGY MANUAL | C10535E |
| NEC IC PACKAGE MANUAL (CD-ROM) | C13388E |
| GUIDE TO QUALITY ASSURANCE FOR SEMICONDUCTOR DEVICES | MEI-1202 |
| SEMICONDUCTORS SELECTION GUIDE | X10679E |
| NEC SEMICONDUCTOR DEVICE RELIABILITY/QUALITY CONTROL SYSTEM (STANDARD LINEAR IC) | IEI-1212 |

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NEC devices are classified into the following three quality grades:

"Standard", "Special", and "Specific". The Specific quality grade applies only to devices developed based on a customer designated "quality assurance program" for a specific application. The recommended applications of a device depend on its quality grade, as indicated below. Customers must check the quality grade of each device before using it in a particular application.

Standard: Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots

Special: Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support)

Specific: Aircrafts, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems or medical equipment for life support, etc.

The quality grade of NEC devices is "Standard" unless otherwise specified in NEC's Data Sheets or Data Books. If customers intend to use NEC devices for applications other than those specified for Standard quality grade, they should contact an NEC sales representative in advance.

Anti-radioactive design is not implemented in this product.