

HA13471A, HA13472A

Three-Phase Motor Drive with Speed Discriminator

Description

HA13471A and HA13472A are three-phase brushless DC motor drive IC of 2A/phase and 4A/phase and have following functions.

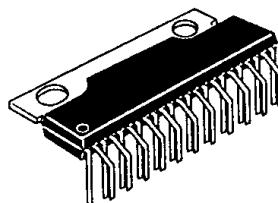
Features

- Soft switching (No spike voltage at commutation)
- Snubberless
- Large current capability
 2A/phase (HA13471A)
 4A/phase (HA13472A)
- High efficiency. Low noise driving

Functions

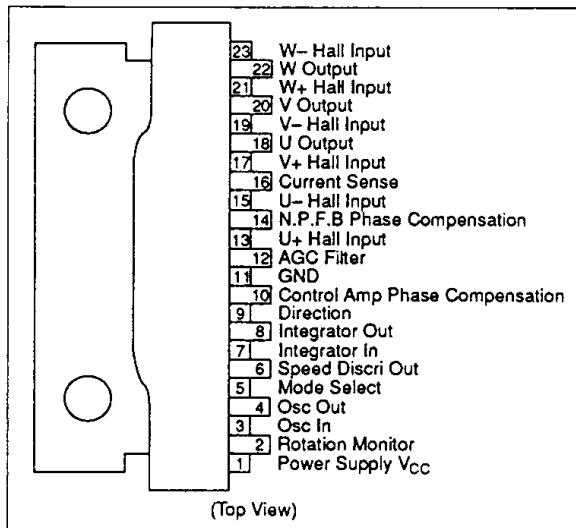
- Three phase output circuit
- Hall amp, Matrix with AGC
- Neutral point feed back
- Direction
- Output inhibit
- Oscillation circuit
- Mode select (1/4, 1/2, 1/1)
- Speed discriminator
- Integrator
- Control amp
- Current limiter
- Low voltage inhibit (LVI)
- Over temperature shutdown circuit (OTSD)

HA13471A



(SP-23TA)

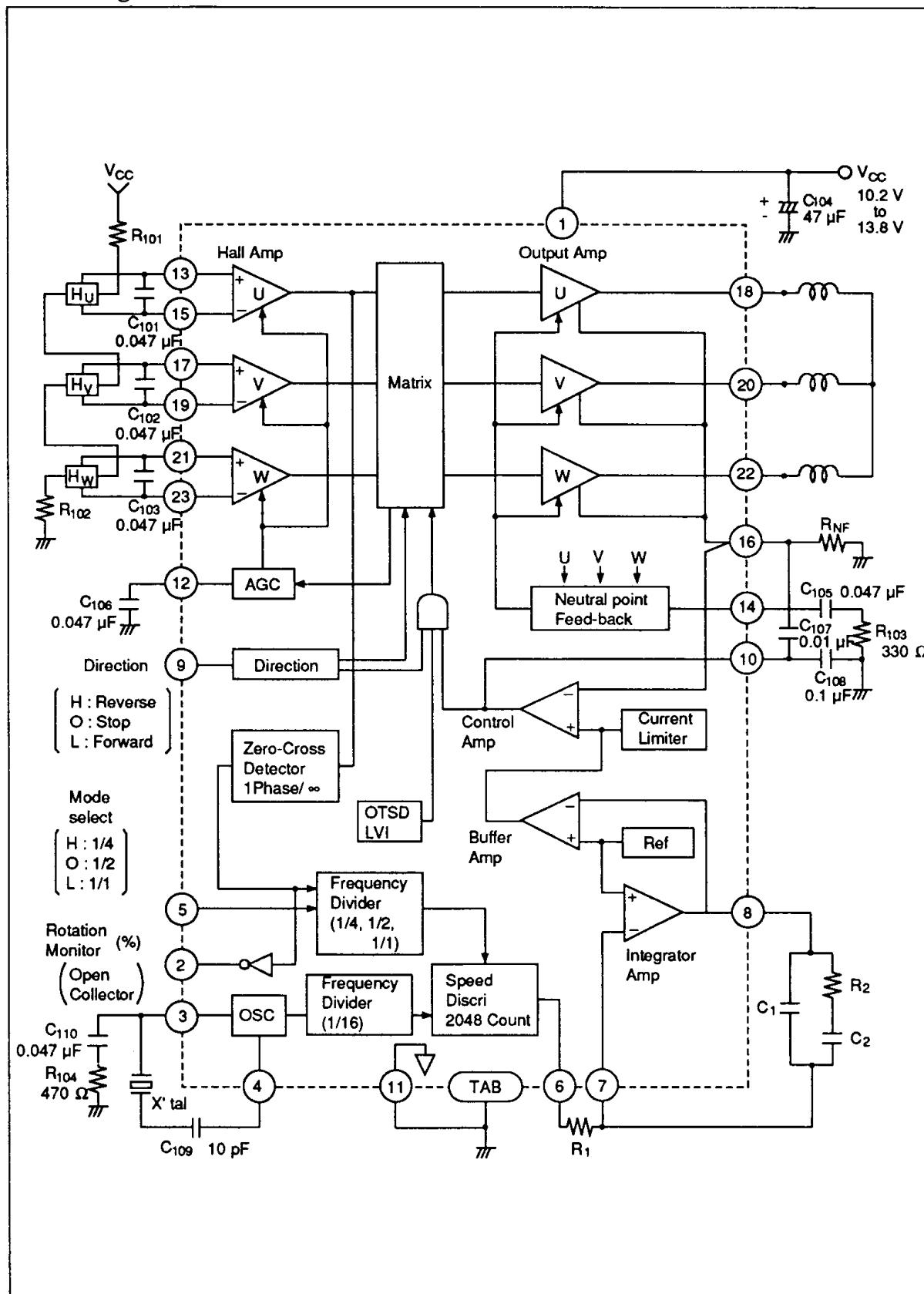
Pin Arrangement



Ordering Information

Type No.	Package
HA13471A	SP-23TA
HA13472A	SP-23TA

Block Diagram



HA13471A, HA13472A

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Item	Symbol	Rating		Unit	Notes
		HA13471A	HA13472A		
Supply Voltage	V_{CC}	+15	+15	V	1
Input Voltage	V_{in}	V_{CC}	V_{CC}	V	2
Output Current	I_O	2	4	A	3
Power Dissipation	P_T	15 (at $T_C = 105^\circ\text{C}$)	30 (at $T_C = 60^\circ\text{C}$)	W	4
Junction Temperature	T_j	150	150	$^\circ\text{C}$	5
Storage Temperature	T_{stg}	-55 to +125	-55 to +125	$^\circ\text{C}$	

The absolute maximum ratings are limiting values, to be applied individually, beyond which the device may be permanently damaged. Functional operation under any of these conditions is not guaranteed. Exposing a circuit to its absolute maximum rating for extended periods of time may affect the device's reliability.

- Notes:
1. Operating voltage range is $12 \text{ V} \pm 15\%$ ($10.2 \sim 13.8 \text{ V}$)
 2. Applied to Hall amp, Direction and Mode select Input
 3. ASO of each power transistor is shown below. Operating locus must be within the ASO

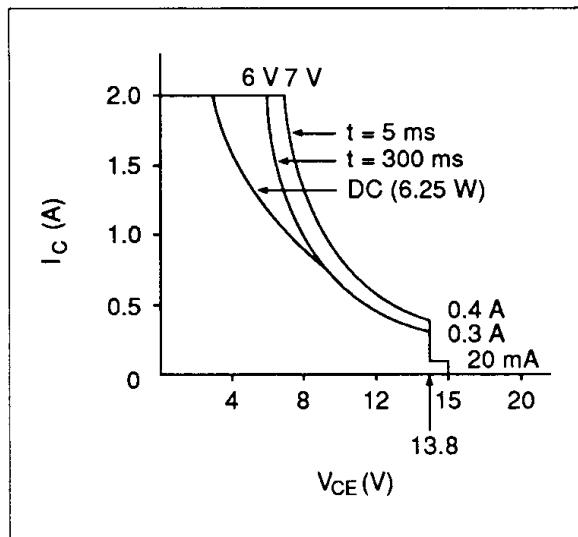


Figure 1 HA13471A

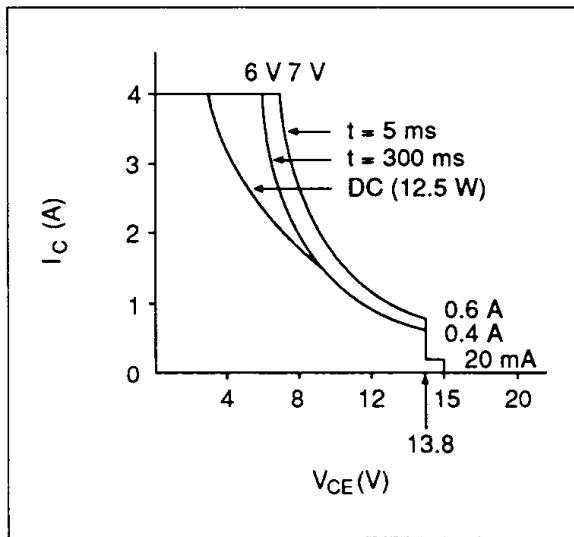


Figure 2 HA13472A

4. Thermal resistance is shown below

$$\theta_j - c \leq 3^\circ\text{C/W}$$

$$\theta_j - a \leq 40^\circ\text{C/W}$$

5. The operating junction temperature range is $T_{opr} = 0^\circ\text{C}$ to 125°C



Electrical Characteristics (Ta = 25°C, V_{CC} = 12 V)

Item		Symbol	Min	Typ	Max	Unit	Test Conditions	Pins	Notes
Dissipation current		I _{CC}	—	22	33	mA		1	
Hall amp	Input impedance	R _H	7	10	15	kΩ		13, 15, 17,	
	Common mode voltage range	V _H	2.5	—	V _{CC} — 2.5	V		19, 21, 23	4
	Differential voltage range	V _H	75	—	300	mV			
Output amp	Leak current	I _{CER}	—	—	2	mA	V _{CE} = 15 V	18, 20, 22	
	Saturation voltage	V _{sat1}	—	2.8	3.2	V	I _O = 3.0 A (1.5 A)		1
		V _{sat2}	—	1.7	2.0	V	I _O = 0.6 A (0.3 A)		1
Current limiter	Internal reference voltage	V _{ref1}	0.225	0.25	0.275	V	R _{NF} = 1.0 Ω	16	
Buffer amp	Internal reference voltage	V _{ref2}	V _{CC} /2 — 10%	V _{CC} /2	V _{CC} /2 + 10%	V		7	
	Voltage gain	G _{CTL}	-2	0	+2	dB	Pin 8 to Pin 16	16	
Integrating amp	Input bias current	I _B (ER)	—	—	±60	nA		7	
	Voltage swing	A ⁺	0.55	0.7	0.85	V	I _O = 0.5 mA	8	2
		A ⁻	-0.55	-0.7	-0.85	V	I _O = -0.5 mA		2
	Band width	BW	—	1.4	—	MHz	G _V = 0 dB		3
Speed discriminator	Output high voltage	V _{OH}	V _{CC} — 0.3	—	—	V	I _O = 0.5 mA	6	
	Output low voltage	V _{OL}	—	—	0.2	V	I _O = -0.5 mA		
	Cutoff current	I _{off}	—	—	±60	nA			
	Count number	—	2048	—					
Oscillator	Frequency error	f _{err}	—	—	±0.1	%	X'tal f _{osc} = 8.0 MHz	4	
	Operating frequency	f _{osc}	—	—	8	MHz	X'tal		
Direction	Input high voltage	V _{IH}	3.6	—	—	V	Reverse	9	
	Input middle voltage	V _{IM}	2.2	—	2.8	V	Stop		
	Input low voltage	V _{IL}	—	—	1.4	V	Forward		
	Input high current	I _{IH}	—	0.54	0.8	mA	V _H = 5.5 V		
	Input low current	I _{IL}	—	-0.54	-0.8	mA	V _L = 0.0 V		
Driver	Input high voltage	V _{IH2}	3.6	—	—	V	Ratio; 1/4	5	
	Input middle voltage	V _{IN2}	2.2	—	2.8	V	Ratio; 1/2		
	Input low voltage	V _{IL2}	—	—	1.4	V	Ratio; 1/1		
	Input high current	I _{IH2}	—	0.54	0.8	mA	V _{H2} = 5.5 V		
	Input low current	I _{IL2}	—	-0.54	-0.8	mA	V _{L2} = 0.0 V		

HA13471A, HA13472A

Electrical Characteristics ($T_a = 25^\circ\text{C}$, $V_{CC} = 12 \text{ V}$) (cont)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions	Pins	Notes
Rotation monitor	Output leak current	I_{CER2}	—	—	100	μA	$V_{CE} = 15 \text{ V}$	2
	Output low voltage	V_{OL2}	—	—	0.4	V	$I_O = 1 \text{ mA}$	
LVI operating voltage		—	—	8.0	V			
OTSD operating temperature	T_{sd}	125	150	—	$^\circ\text{C}$			3

Notes:

1. Sum of upper and lower saturation voltage.

2. Measure from V_{ref2}

3. Design guide only

4. Operating area for V_H and v_h is shown below

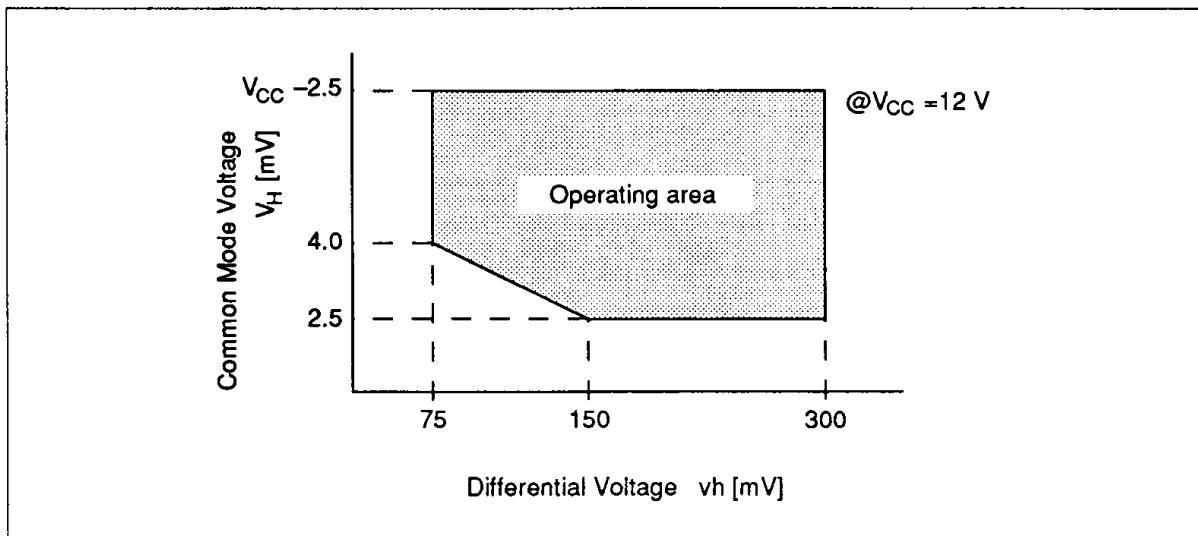
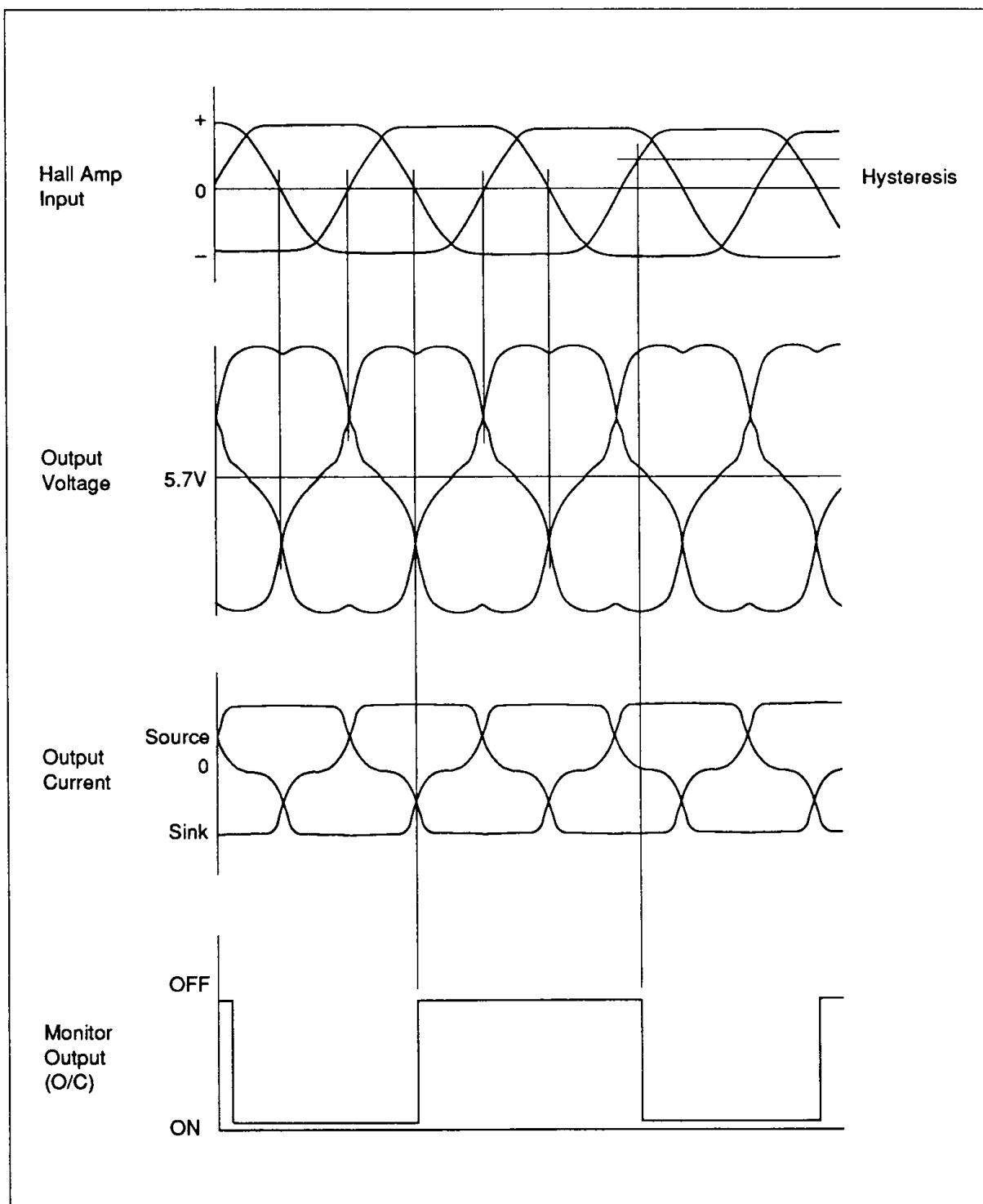


Figure 3 Hallamp Operating Area

Timing Chart

HA13471A, HA13472A

External Components

Parts No.	Recommended value		Purpose	Notes
	HA13471A 3.5" × 2 disks	HA13472A 5.25" × 6 disks		
R ₁₀₁ , R ₁₀₂	560 Ω	560 Ω	Hall element bias	1
R ₁₀₃	330 Ω	330 Ω	Stability	
R ₁₀₄	470 Ω	470 Ω	Oscillation stability	2
R ₁	470 kΩ	68 kΩ	Integral constant	3
R ₂	560 kΩ	560 kΩ		
R _{NF}	0.33 Ω × 2 para	0.33 Ω × 4 para	Current detector	4
C ₁₀₁ , C ₁₀₂ , C ₁₀₃	0.047 μF	0.047 μF	Stability	
C ₁₀₄	≥ 47 μF	≥ 47 μF	By-pass	
C ₁₀₅	0.047 μF	0.047 μF	NPF phase compensation	
C ₁₀₆	0.047 μF	0.047 μF	AGC filter	
C ₁₀₇	0.01 μF	0.01 μF	CTL Amp	
C ₁₀₈	0.1 μF	0.1 μF	Phase compensation	
C ₁₀₉	10 pF ±1%	10 pF ±1%	AC-coupling	
C ₁₁₀	0.047 μF	0.047 μF	Stability	2
C ₁	0.022 μF	0.022 μF	Integral constant	3
C ₂	0.22 μF	0.22 μF		
D ₁ , D ₂ , D ₃	—	—	Output clamp	5
X'tal	—	—	Oscillator	6

- Notes:
- Set R₁₀₁, R₁₀₂ in order to get Hall element output within 75 to 300 mV_{PP}
 - Unnecessary below 4 MHz frequency
 - The optimum value depends on the motor (Inertia, Torque constant Rotation)
 - Output Current is limited as shown below
 $I_{max} = V_{ref1}/R_{NF}$
 - It must be noted that some motors requires protection for speed discriminator misoperation
 - OSC frequency fosc is determined by following equation

$$f_{osc} = \frac{16 \times 2048 \times f_H}{m}$$

$$= \frac{16 \times 2048}{m} \times \frac{N_o}{60} \times \frac{P}{2} \text{ (Hz)}$$

Where

- | | | |
|----------------|-----|------------------------------------|
| f _H | : | frequency of hall amp input signal |
| No | : | rotation Number |
| P | : | pole Number |
| m | : | frequency divider constant |
| m | = 1 | (@ Pin 5 = Low) |
| | = 2 | (@ Pin 5 = Open) |
| | = 4 | (@ Pin 5 = High) |

In case of large jitter of hall amp input signal, select divider constant m as follows

4 pole motor → m = 2

8 pole motor → m = 4

