TOSHIBA TD62M4600F

TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT MULTI CHIP

TD62M4600F

4CH LOW SATURATION VOLTAGE SOURCE DRIVER

TD62M4600F is Multi Chip IC incorporates 4 low saturation discrete transistors which equipped Flywheeling Diodes and Bias resistor.

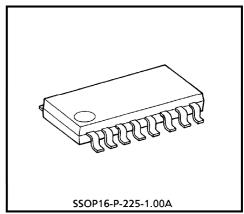
This IC is suitable for a battery use motor drive and LED display module applications.

FEATURES

- Suitable for Motor drive circuit and LED display module
- Bias Resistor and Diodes are equipped : $R = 10k\Omega$
- Low Saturation Voltage

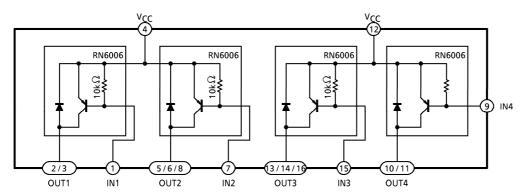
 $V_{CE (sat)} = 0.16V (Typ.)$ at $I_C = 1A$ $V_{CE (sat)} = 0.30V (Typ.)$ at $I_C = 2A$

SSOP16 1mm pitch small package sealed

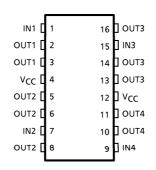


Weight: 0.14g (Typ.)

BLOCK DIAGRAM



PIN CONNECTION (TOP VIEW)



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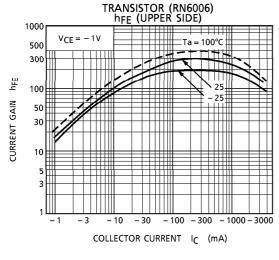
MAXIMUM RATINGS (Ta = 25°C)

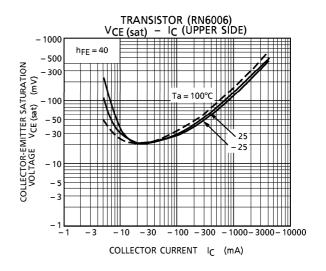
CHARACTERISTIC	SYMBOL	RATING	UNIT	
Supply Voltage	VCC	- 10	V	
Breakdown Voltage	V _{CBO}	- 10	V	
	V _{CER}	– 10		
	V _{EBO}	– 6		
Output Current	lo	-2	A / ch	
	IO (PEAK)	(Note 1) -4	A/CII	
Base Current	IB (AVE)	-0.4	Α	
Base Current	IB (PEAK)	- 0.8		
Fly-wheeling Diode Forward Current	IF	(Note 2) – 2	Α	
Power Dissipation	PD	490	mW	
Junction Temperature	Тј	150	°C	
Operating Temperature	T _{opr}	- 40∼85	°C	
Storage Temperature	T _{stg}	- 55∼150	°C	

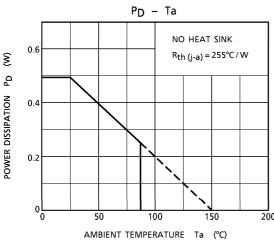
(Note 1) T = 10ms MAX. and maximum duty is less than 30%. (Note 2) T = 10ms single pulse

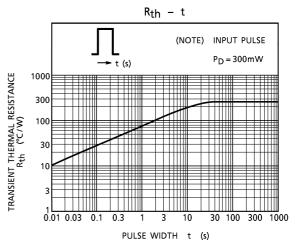
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Current Gain	h _{FE} (1)	_	$V_{CE} = -1V$, $I_{C} = -0.5A$	160	_	600	_	
	h _{FE} (2)	_	$V_{CE} = -1V, I_{C} = -1.5A$	60	130			
Saturation Voltage	V _{CE} (sat)	_	I _C = -1A, I _B = -25mA	_	- 0.13	- 0.25	- V I	
			$I_C = 2A$, $I_B = -50mA$	_	- 0.25	- 0.50		
Transition Frequency	fT	_	$V_{CE} = -2V$, $I_{C} = -0.5A$	_	150	_	MHz	
Leakage Current	lOL	_	V _{CC} = - 10V	_	0	- 10	μΑ	
Fly-wheeling Diode Forward	V _F		I _F = -300mA	_	- 0.89	- 1.2	V	
Voltage	*F		I _F = -450mA, 10ms	_	- 1.60	_		
Base-Emitter Resistor	R _{BE}	_	_	7	10	13	kΩ	
Base-Emitter Forward Voltage	V _{BE}	_	$V_{CE} = -1V, I_{C} = -2.0A$	_	- 0.84	- 1.5	V	









PRECAUTIONS for USING

Utmost care is necessary in the design of the output line, V_{CC} and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

OUTLINE DRAWING SSOP16-P-225-1.00A Unit : mm 0.6TYP 8.7MAX 8.2±0.2 0.6TYP 1.0 0.4±0.1 0.2W 1.0

Weight: 0.14g (Typ.)

0.525±0.2