



No.4600A

2SK2160

N-Channel MOS Silicon FET

Very High-Speed
Switching Applications**Features**

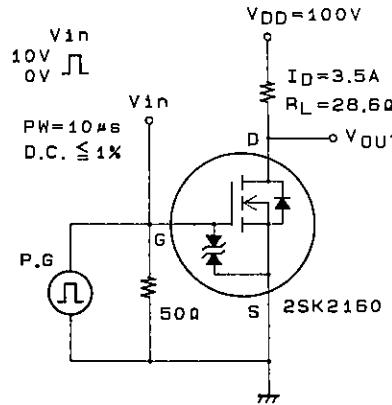
- Low ON resistance.
- Very high-speed switching.
- Low-voltage drive.
- Micaless package facilitating mounting.

Absolute Maximum Ratings at Ta = 25°C

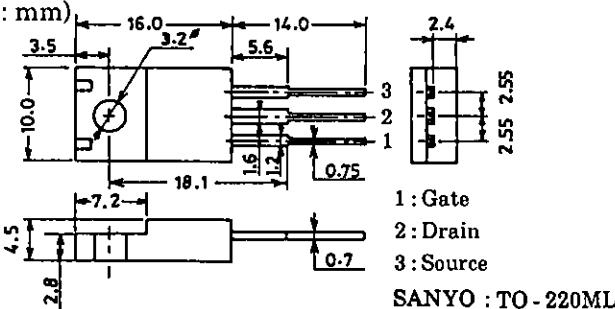
			unit
Drain-to-Source Voltage	V _{DSS}	200	V
Gate-to-Source Voltage	V _{GSS}	±20	V
Drain Current(DC)	I _D	7	A
Drain Current(Pulse)	I _{DP}	PW ≤ 10μs, duty cycle ≤ 1% 28	A
Allowable Power Dissipation	P _D	2.0	W
		T _c = 25°C 25	W
Channel Temperature	T _{ch}	150	°C
Storage Temperature	T _{stg}	-55 to + 150	°C

Electrical Characteristics at Ta = 25°C

			min	typ	max	unit
D-S Breakdown Voltage	V _{(BR)DSS}	I _D = 1mA, V _{GS} = 0	200			V
G-S Breakdown Voltage	V _{(BR)GSS}	I _G = ±100μA, V _{DS} = 0	±20			V
Zero-Gate Voltage	I _{DSS}	V _{DS} = 200V, V _{GS} = 0			100	μA
Drain Current						
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} = ±16V, V _{DS} = 0		±10		μA
Cutoff Voltage	V _{GS(off)}	V _{DS} = 10V, I _D = 1mA	1.5		2.5	V
Forward Transfer Admittance	Y _{fs}	V _{DS} = 10V, I _D = 3.5A	3	5		S
Static Drain-to-Source	R _{D(on)}	I _D = 3.5A, V _{GS} = 10V	350	450		mΩ
ON-State Resistance						
Input Capacitance	C _{iss}	V _{DS} = 20V, f = 1MHz	550			pF
Output Capacitance	C _{oss}	V _{DS} = 20V, f = 1MHz	110			pF
Reverse Transfer Capacitance	C _{rss}	V _{DS} = 20V, f = 1MHz	45			pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit.	12			ns
Rise Time	t _r	"	15			ns
Turn-OFF Delay Time	t _{d(off)}	"	190			ns
Fall Time	t _f	"	65			ns
Diode Forward Voltage	V _{SD}	I _S = 7A, V _{GS} = 0	1.0	1.5		V

Switching Time Test Circuit**Package Dimensions 2063A**

(unit : mm)



1 : Gate

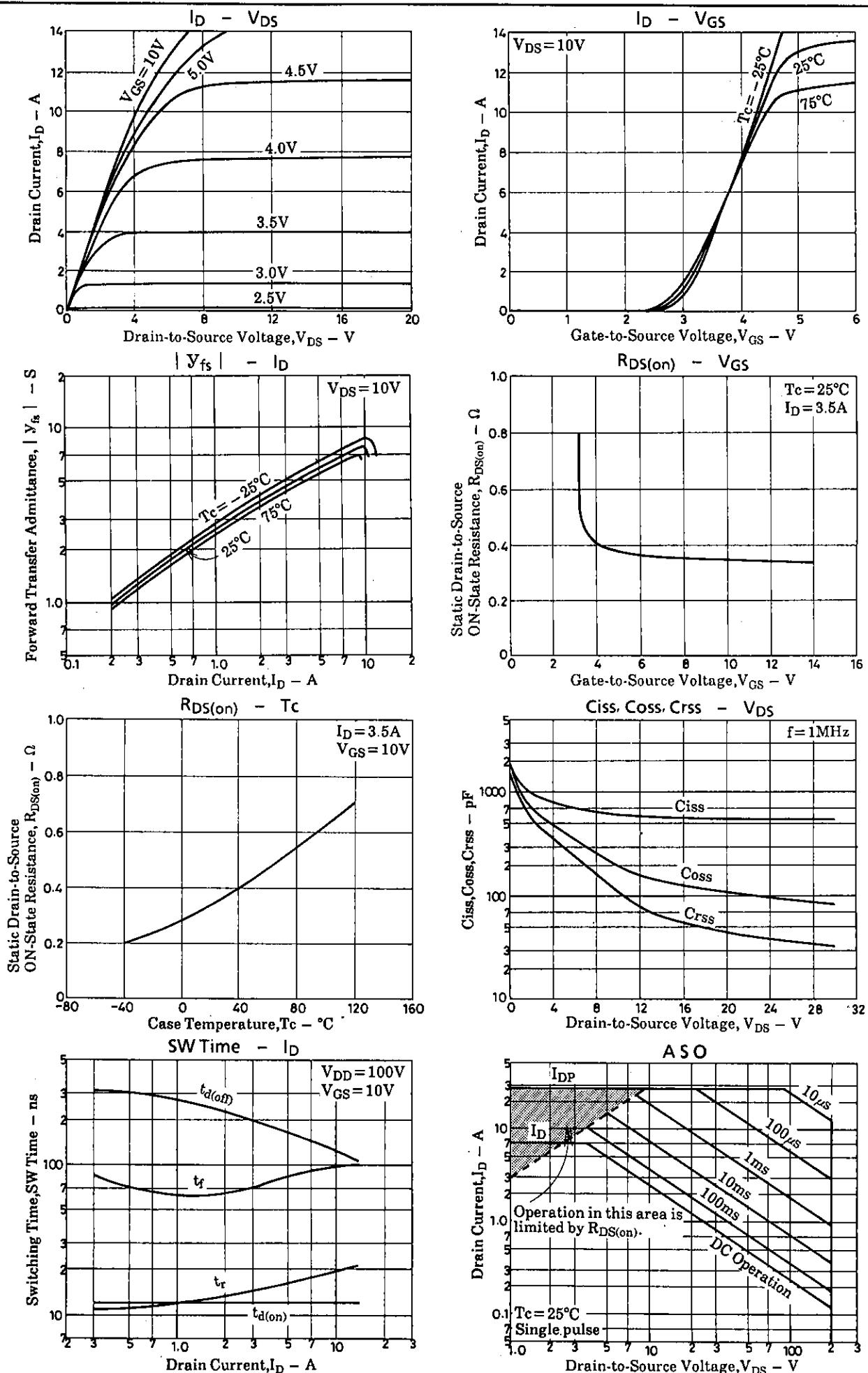
2 : Drain

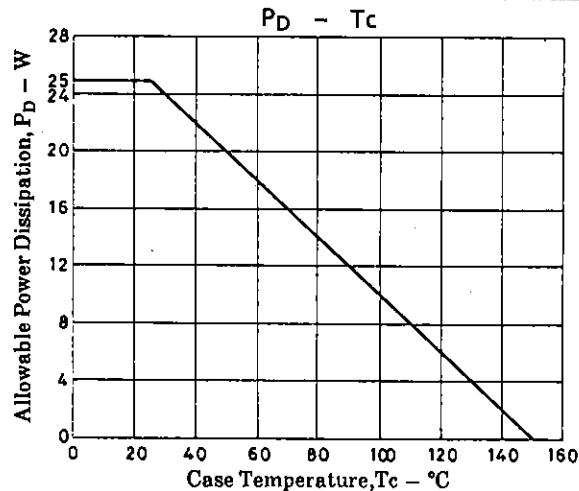
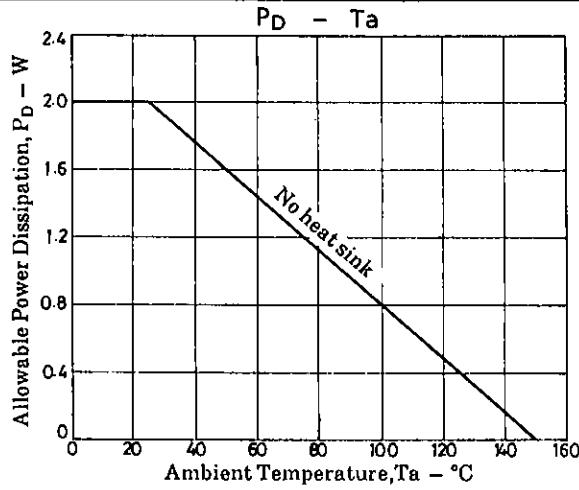
3 : Source

SANYO : TO - 220ML

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