TOSHIBA TA2028F/P

TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA2028F, TA2028P

FILTER IC FOR Σ - Δ MODULATION SYSTEM DA CONVERTER

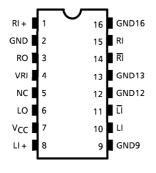
TA2028F, TA2028P are an analog filter IC for Σ - Δ modulation system DA converter. Using the TA2028F, TA2028P in combination the TC9237BF, TC9237BN (the Σ - Δ modulation system DA converter with a built-in digital filter), it is possible to construct a DA conversion system with less external parts.

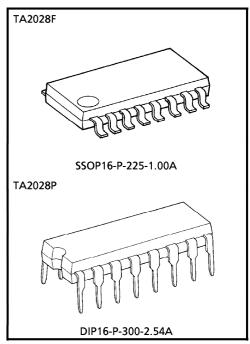
FEATURES

- Built-in CR for LPFs and output (differential) amplifiers for the left and right channel.
- Single power supply operation. (+9V operation: BS tuner system)
- Noise distortion factor and S/N ratio are as follows (when operating at +5V single power supply):

Noise distortion factor: -86dB (Typ.) S/N : 100dB (Typ.)

PIN CONNECTION (Top view)





Weight

SSOP16-P-225-1.00A: 0.14g (Typ.) DIP16-P-300-2.54A : 1.00g (Typ.)

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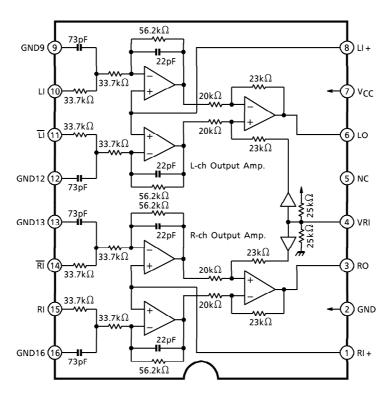
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BLOCK DIAGRAM



DESCRIPTION OF PIN FUNCTIONS

PIN No.	SYMBOL	1/0	FUNCTION & OPERATION	REMARKS	
1	RI +	ı	R channel operational amplifier forward input pin. Connect to VRI.	_	
2	GND	_	Ground pin.	_	
3	RO	0	R channel analog output pin.	_	
4	VRI	_	Reference voltage pin. (V _{CC} /2)	See the block diagram	
5	NC	_	Non-connecting pin. NC pin is used in the open state.	_	
6	LO	0	L channel analog output pin.	_	
7	VCC	_	Supply voltage pin.	_	
8	LI +	-	L channel operational amplifier forward input pin. Connect to VRI.	_	
9	GND9	_	Ground pin for L channel reverse input side filter.	_	
10	LI	-	L channel forward input pin.	Connect to LO of TC9237BF, TC9237BN	
11	Π	I	L channel reverse input pin.	Connect to LO of TC9237BF, TC9237BN	
12	GND12	_	Ground pin for L channel forward input side filter.	_	
13	GND13	_	Ground pin for R channel forward input side filter.	_	
14	RI	l	R channel reverse input pin.	Connect to RO of TC9237BF, TC9237BN	
15	RI	I	R channel forward input pin. Connect to RO of TC9237BF, TC9237BF		
16	GND16	_	Ground pin for R channel reverse input side filter. —		

MAXIMUM RATINGS (Ta = 25°C)

CHARACTER	ISTIC	SYMBOL	RATING	UNIT	
Supply Voltage		V _{CC}	11	V	
Power Dissipation	TA2028F	D-	350 (*)	mW	
Power Dissipation	TA2028P	PD	1388 (**)		
Operating Tempera	ature	T _{opr}	- 25∼75	°C	
Storage Temperatu	re	T _{stg}	- 55∼150	°C	

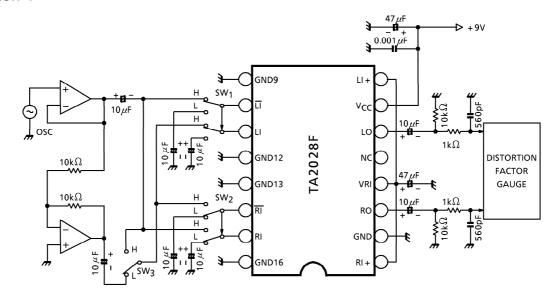
- (*) Reduce $2.8 \text{mW}/^{\circ}\text{C}$ at $Ta = above 25^{\circ}\text{C}$.
- (**) Reduce $11.2 \text{mW} / ^{\circ}\text{C}$ at $Ta = above 25 ^{\circ}\text{C}$.

ELECTRICAL CHARACTERISTICS (Unless otherwise specified, V_{CC} = 5V, Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Supply Voltage	V _{CC}	_	Ta = −35~85°C	8.0	9.0	10	V
Operating Supply Current	lccQ	_	At no signal	8.1	11.0	13.7	mA
Reference Voltage	VRI	_	_	4.4	4.5	4.6	V
	THD (1)	1	1kHz, V _o = 2mV _{rms}	_	- 86	- 83	dB
Noise Distortion Factor	THD (2)		10kHz, $V_0 = 2mV_{rms}$	_	- 86	- 83	
	THD (3)		1kHz, $V_0 = 100 \text{mV}_{rms}$	_	- 74	– 70	
Cross Talk	CT	1	1kHz, V _o = 2mV _{rms}	_	- 100	- 90	dB
A44	ATT (1)	1	$40kHz$, $V_0 = -10dBV_{rms}$	0.51	0.71	1.41	dB
Attenuation	ATT (2)		80kHz, $V_0 = -10$ dB V_{rms}	1.50	2.70	4.50	
Max. Output Level	V _{omax}	1	1kHz, THD = 1%	2.5	2.6	_	V _{rms}
Differential Balance	GVB	1	1kHz, 1.1dBV _{rms} In-phase input	_	_	- 40	dB
LR Output Difference	G _{VD}	1	1kHz, 1.1dBV _{rms} Differential input	_	0	0.5	dB

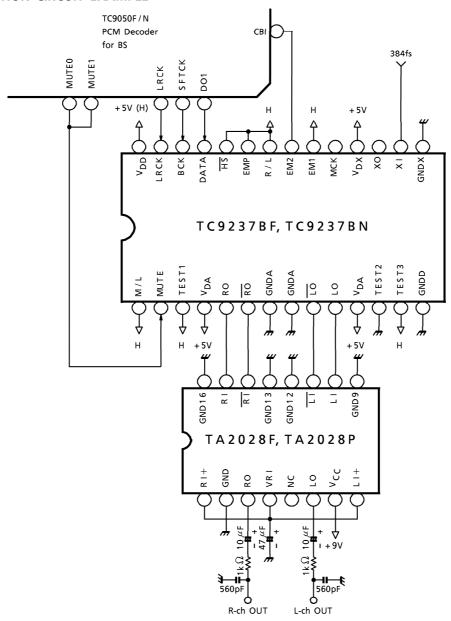
- (Note 1) When the TC9237BF, TC9237BN (+5V) and +9V single power supply are operated : Full scale = $2mV_{rms}$ (Typ.).
- (Note 2) The amount of attenuations is based on 1 kHz, $V_0 = -10 \, dBV_{rms}$.
- (Note 3) Measuring circuit-1: indicates the measuring circuit.

TEST CIRCUIT-1



sw ₁	sw ₂	SW ₃	MEASURING ITEM
L	L	_	Operating supply voltage, Reference voltage
L	Н	L	Cross talk (R→L)
Н	L	L	Cross talk (L→R)
Н	Н	L	Noise distortion factor, Attenuation, Maximum output level, LR output difference
Н	Н	Н	Difference balance

APPLICATION CIRCUIT EXAMPLE



(Cautions)

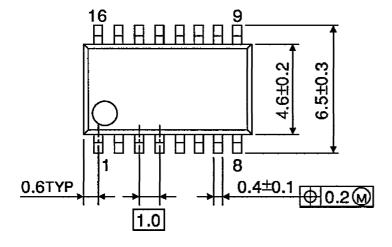
- Quality of crystal oscillation waveform largely effects S/N ratio.

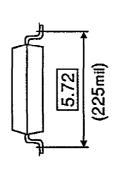
 Further, this is also true when system clock is input externally through the XI pin of pin[®].
- Suppress glitch of input signals (LRCK, BCK, DATA) as could as possible.
- The wiring between the TC9237BF, TC9237BN output and the analog filter amplifier input must be made the shortest
- ullet The capacitor between $V_{\mbox{DA}}$ and GNDA shall be connected as close to the pin as possible.
- NC pin is used in the open state.

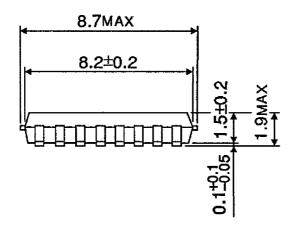
Unit: mm

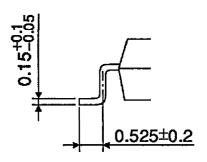
PACKAGE DIMENSIONS

SSOP16-P-225-1.00A







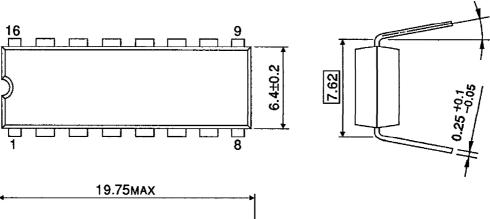


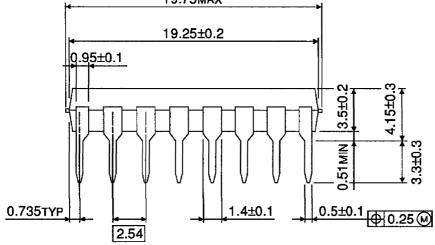
Weight: 0.14g (Typ.)

Unit: mm

PACKAGE DIMENSIONS

DIP16-P-300-2.54A





Weight: 1.00g (Typ.)