



S DM4410

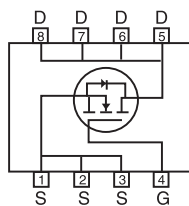
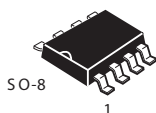
N-Channel Enhancement Mode Field Effect Transistor

PRODUCT SUMMARY

V _{DSS}	I _D	R _{DS(ON)} (m Ω) TYP
30V	10A	11 @ V _{GS} = 10V
		15 @ V _{GS} = 4.5V

FEATURES

- Super high dense cell design for low R_{DS(ON)}.
- Rugged and reliable.
- Surface Mount Package.



ABSOLUTE MAXIMUM RATINGS (T_A=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous ^a @ T _J =125°C -Pulsed ^b (300ms Pulse Width)	I _D	10	A
	I _{DM}	30	A
Drain-Source Diode Forward Current ^a	I _S	2.3	A
Maximum Power Dissipation ^a	P _D	2.5	W
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 to 150	°C

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient ^a	R θ JA	50	°C/W
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ELECTRICAL CHARACTERISTICS (T_A=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ ^c	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	30			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =24V, V _{GS} =0V			1	uA
Gate-Body Leakage	I _{GSS}	V _{GS} = ± 16V, V _{DS} =0V			±100	nA
ON CHARACTERISTICS ^b						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D = 250uA	1	1.5	3	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D = 10A		11	13.5	m ohm
		V _{GS} =4.5V, I _D = 5A		15	20	m ohm
On-State Drain Current	I _{D(ON)}	V _{DS} = 10V, V _{GS} = 10V	40			A
Forward Transconductance	g _{FS}	V _{DS} = 10V, I _D =20A		18		S
DYNAMIC CHARACTERISTICS ^c						
Input Capacitance	C _{ISS}	V _{DS} =15V, V _{GS} = 0V f =1.0MHz		1375		pF
Output Capacitance	C _{OSS}			670		pF
Reverse Transfer Capacitance	C _{RSS}			200		pF
SWITCHING CHARACTERISTICS ^c						
Turn-On Delay Time	t _{D(ON)}	V _{DD} = 15V, I _D = 1A, V _{GS} = 10V, R _{GEN} = 6 ohm		30		ns
Rise Time	t _r			32		ns
Turn-Off Delay Time	t _{D(OFF)}			132		ns
Fall Time	t _f			30		ns
Total Gate Charge	Q _g	V _{DS} =10V, I _D = 10A,V _{GS} =10V		40	50	nC
		V _{DS} =10V, I _D = 10A,V _{GS} =4.5V		20	24	nC
Gate-Source Charge	Q _{gs}	V _{DS} =10V, I _D = 10A, V _{GS} =10V		8.2		nC
Gate-Drain Charge	Q _{gd}			5.3		nC

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ELECTRICAL CHARACTERISTICS (T_A=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ ^c	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS ^b						
Diode Forward Voltage	V _{SD}	V _{GS} = 0V, I _S =2.3A		0.76	1.1	V

Notes

- a.Surface Mounted on FR4 Board, t≤10sec.
- b.Pulse Test:Pulse Width≤ 300us, Duty Cycle≤ 2%.
- c.Guaranteed by design, not subject to production testing.

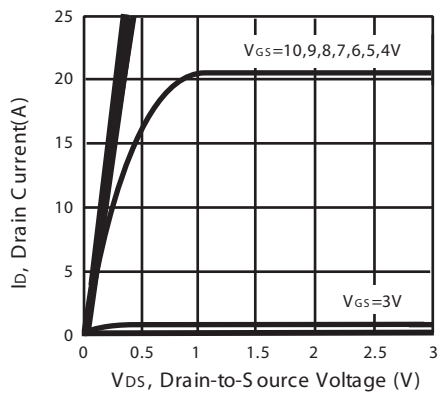


Figure 1. Output Characteristics

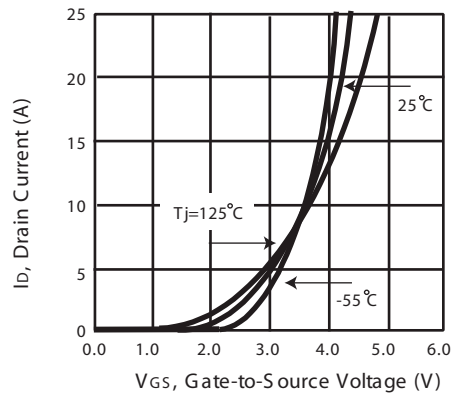


Figure 2. Transfer Characteristics

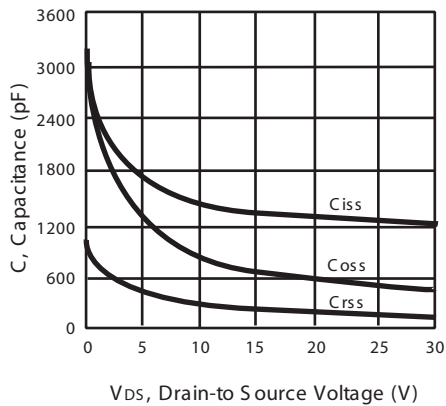


Figure 3. Capacitance

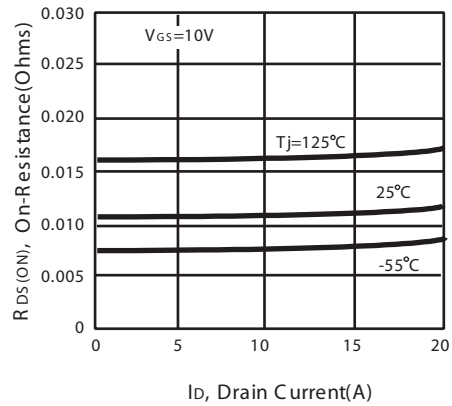


Figure 4. On-Resistance Variation with Drain Current and Temperature

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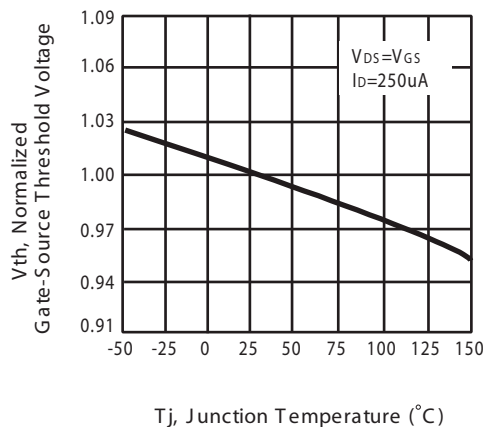


Figure 5. Gate Threshold Variation with Temperature

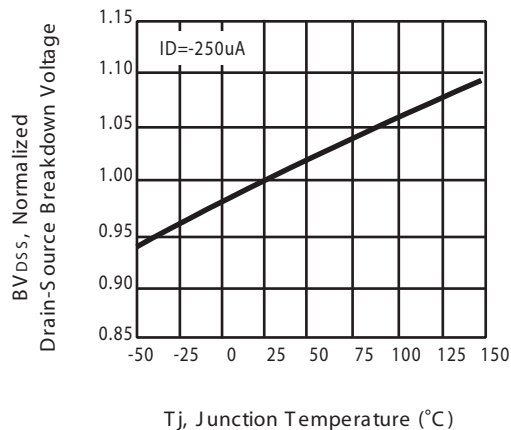


Figure 6. Breakdown Voltage Variation with Temperature

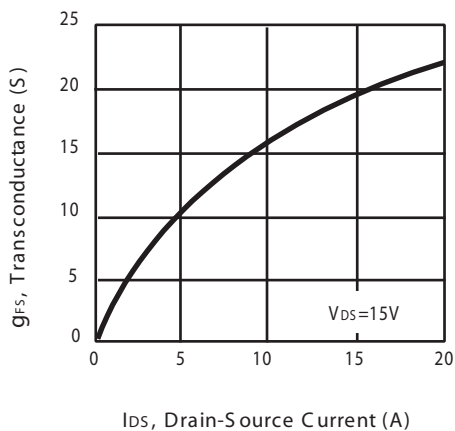


Figure 7. Transconductance Variation with Drain Current

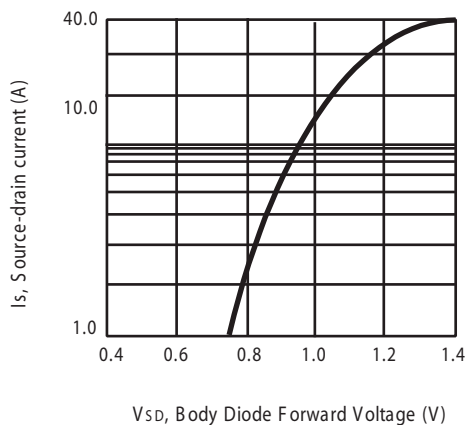


Figure 8. Body Diode Forward Voltage Variation with Source Current

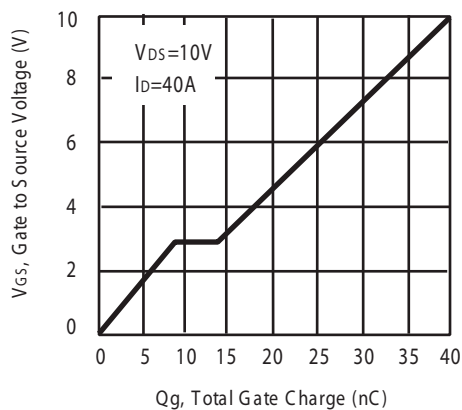


Figure 9. Gate Charge

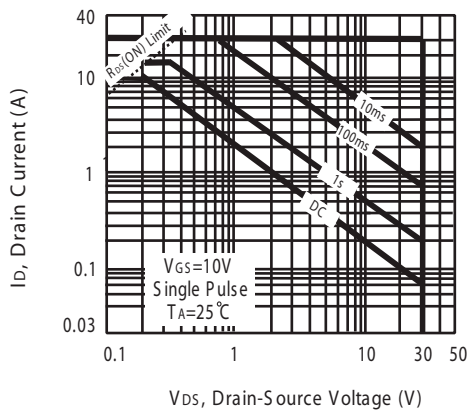


Figure 10. Maximum Safe Operating Area

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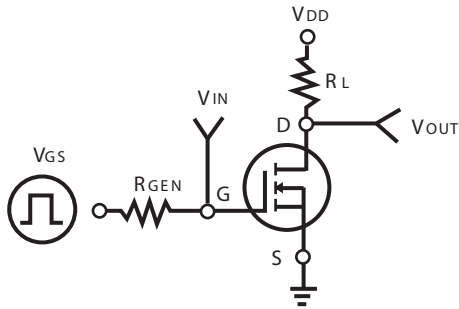


Figure 11. S switching Test Circuit

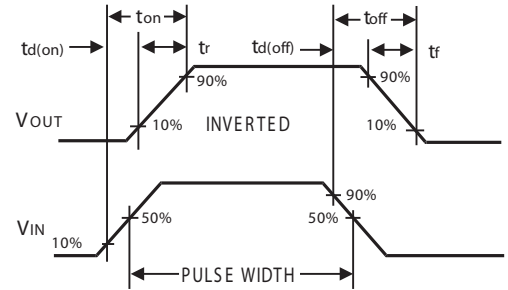


Figure 12. Switching Waveforms

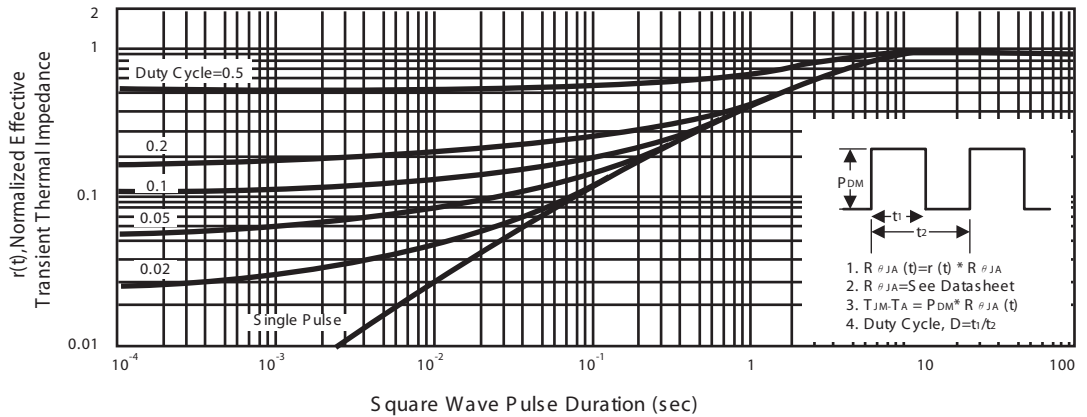


Figure 13. Normalized Thermal Transient Impedance Curve