Regarding the change of names mentioned in the document, such as Hitachi Electric and Hitachi XX, to Renesas Technology Corp.

The semiconductor operations of Mitsubishi Electric and Hitachi were transferred to Renesas Technology Corporation on April 1st 2003. These operations include microcomputer, logic, analog and discrete devices, and memory chips other than DRAMs (flash memory, SRAMs etc.) Accordingly, although Hitachi, Hitachi, Ltd., Hitachi Semiconductors, and other Hitachi brand names are mentioned in the document, these names have in fact all been changed to Renesas Technology Corp. Thank you for your understanding. Except for our corporate trademark, logo and corporate statement, no changes whatsoever have been made to the contents of the document, and these changes do not constitute any alteration to the contents of the document itself.

Renesas Technology Home Page: http://www.renesas.com

Renesas Technology Corp. Customer Support Dept. April 1, 2003



Cautions

Keep safety first in your circuit designs!

Renesas Technology Corporation puts the maximum effort into making semiconductor products better
and more reliable, but there is always the possibility that trouble may occur with them. Trouble with
semiconductors may lead to personal injury, fire or property damage.
 Remember to give due consideration to safety when making your circuit designs, with appropriate
measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or
(iii) prevention against any malfunction or mishap.

Notes regarding these materials

- 1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corporation product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corporation or a third party.
- 2. Renesas Technology Corporation assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.
- 3. All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corporation without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corporation or an authorized Renesas Technology Corporation product distributor for the latest product information before purchasing a product listed herein.
 - The information described here may contain technical inaccuracies or typographical errors. Renesas Technology Corporation assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.
 - Please also pay attention to information published by Renesas Technology Corporation by various means, including the Renesas Technology Corporation Semiconductor home page (http://www.renesas.com).
- 4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Renesas Technology Corporation assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.
- 5. Renesas Technology Corporation semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Renesas Technology Corporation or an authorized Renesas Technology Corporation product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
- 6. The prior written approval of Renesas Technology Corporation is necessary to reprint or reproduce in whole or in part these materials.
- 7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.
 - Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.
- 8. Please contact Renesas Technology Corporation for further details on these materials or the products contained therein.

Silicon N Channel Power MOS FET Power Switching



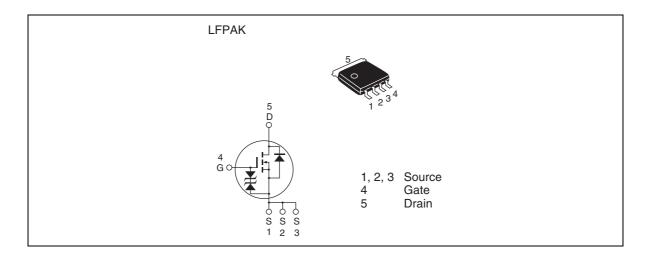
ADE-208-1582E(Z)

Preliminary 6th. Edition Sep. 2002

Features

- Capable of 7 V gate drive
- Low drive current
- High density mounting
- Low on-resistance $R_{_{DS(on)}} = 22 \text{ m}\Omega \text{ typ. (at } V_{_{GS}} = 10 \text{ V})$

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit	
Drain to source voltage	V _{DSS}	100	V	
Gate to source voltage	V _{GSS}	±20	V	
Drain current	I _D	15	Α	
Drain peak current	Note1 D(pulse)	60	А	
Body-drain diode reverse drain current	I _{DR}	15	Α	
Avalanche current	I Note 3	15	Α	
Avalanche energy	E _{AR} Note 3	22.5	mJ	
Channel dissipation	Pch Note2	20	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to + 150	°C	

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Tc = 25°C
- 3. Value at Tch = 25°C, Rg \geq 50 Ω

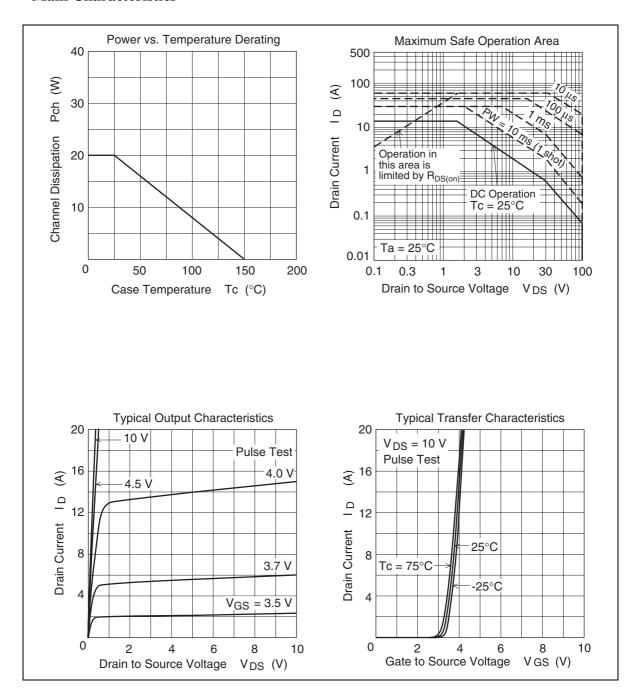
Electrical Characteristics

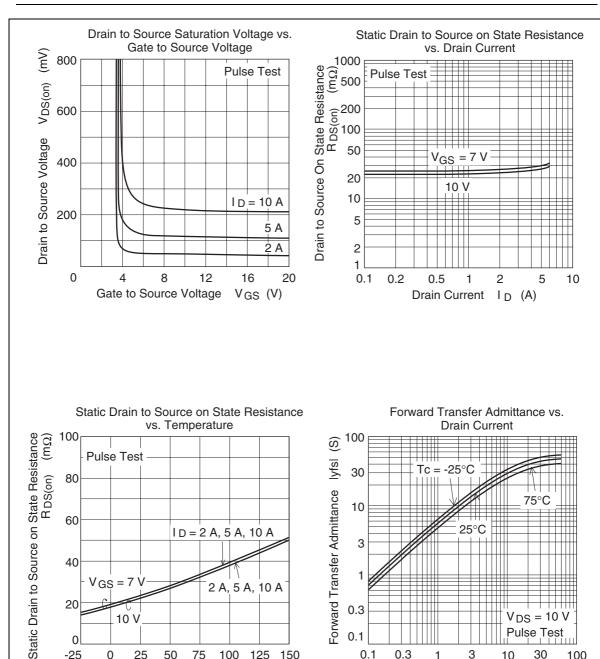
 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{_{(BR)DSS}}$	100	_	_	V	$I_{D} = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	± 20	_	_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	± 10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 100 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{_{\text{GS(off)}}}$	2.0	_	3.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	$R_{\scriptscriptstyle DS(on)}$	_	22	27.5	mΩ	$I_{D} = 7.5 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
resistance	$R_{\scriptscriptstyle DS(on)}$	_	23.5	32	mΩ	$I_{D} = 7.5 \text{ A}, V_{GS} = 7 \text{ V}^{Note4}$
Forward transfer admittance	ly _{fs} l	15	25	_	S	$I_{D} = 7.5 \text{ A}, V_{DS} = 10 \text{ V}^{Note4}$
Input capacitance	Ciss	_	3200	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	255	_	pF	V _{GS} = 0
Reverse transfer capacitance	Crss	_	125	_	pF	f = 1 MHz
Total gate charge	Qg	_	46	_	nc	V _{DD} = 50 V
Gate to source charge	Qgs	_	11	_	nc	$V_{GS} = 10 \text{ V}$
Gate to drain charge	Qgd	_	10	_	nc	I _D = 15 A
Turn-on delay time	$t_{d(on)}$	_	22	_	ns	$V_{GS} = 10 \text{ V}, I_{D} = 7.5 \text{ A}$
Rise time	t _r	_	13	_	ns	$V_{DD} \cong 30 \text{ V}$
Turn-off delay time	$\mathbf{t}_{\text{d(off)}}$	_	70	_	ns	$R_L = 4 \Omega$
Fall time	t _f	_	10		ns	$Rg = 4.7 \Omega$
Body-drain diode forward voltage	V _{DF}	_	0.82	1.07	V	$IF = 15 \text{ A}, V_{GS} = 0^{\text{Note4}}$
Body-drain diode reverse recovery time	t _{rr}	_	50	_	ns	IF = 15 A, V _{GS} = 0 diF/ dt = 100 A/ μs

Notes: 4. Pulse test

Main Characteristics





2 A, 5 A, 10 A

125 150

(°C)

100

Tc

20

0 -25

25

50

Case Temperature

75

 $V_{DS} = 10 V$ Pulse Test

30

100

10

0.3

0.1

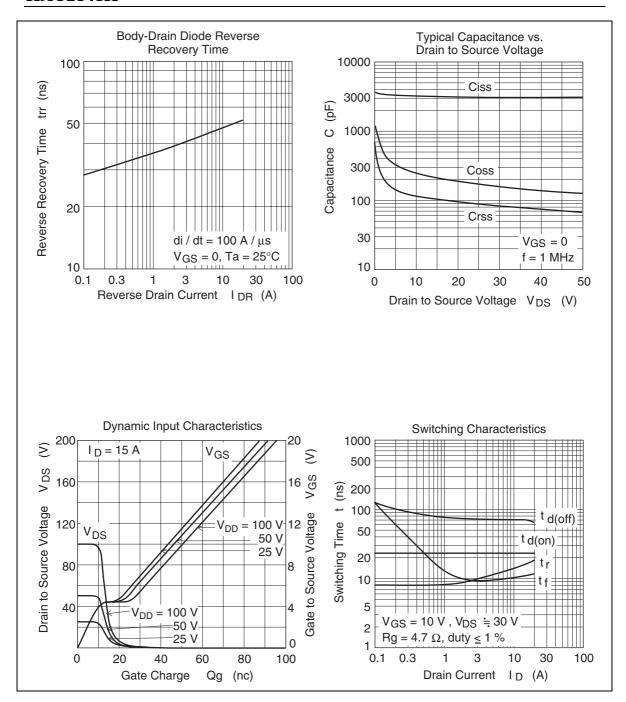
0.1

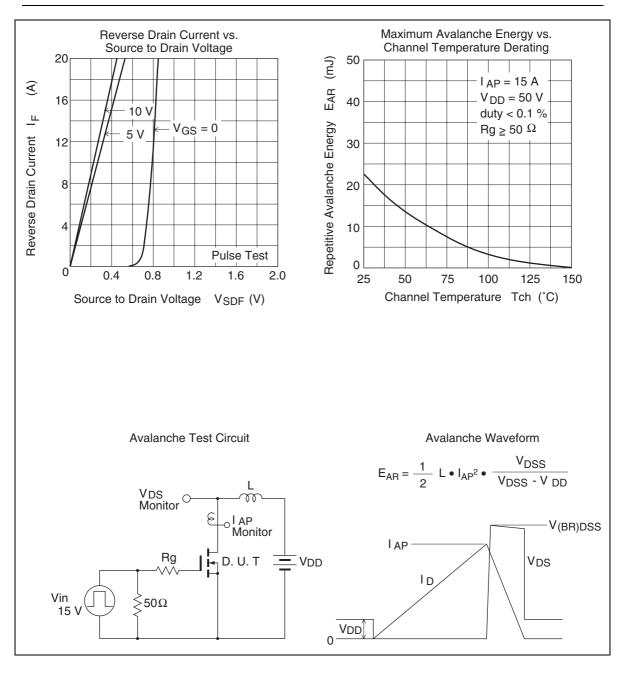
0.3

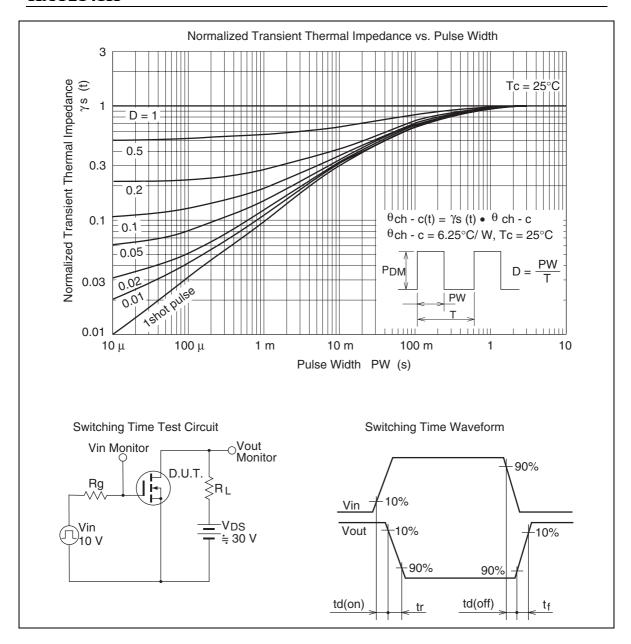
3

Drain Current ID (A)

1







Package Dimensions

