# **Zener Voltage Regulators**

# 100 mW SOD-523 Surface Mount

This series of Zener diodes is packaged in a SOD-523 surface mount package. They are designed to provide voltage regulation protection and are especially attractive in situations where space is at a premium. They are well suited for applications such as cellular phones, hand held portables, and high density PC boards.

### **Specification Features**

- Standard Zener Breakdown Voltage Range -2.4 V to 9.1 V
- Steady State Power Rating of 100 mW
- Small Body Outline Dimensions: 0.047" x 0.032" (1.20 mm x 0.80 mm)
- Low Body Height: 0.028" (0.7 mm)
- ESD Rating of Class 3 (>16 kV) per Human Body Model
- Tight Tolerance V<sub>Z</sub>
- These devices are manufactured with a Pb–Free external lead finish only.

### **Mechanical Characteristics**

**CASE:** Void-free, transfer-molded, thermosetting plastic

Epoxy Meets UL 94, V-0

**LEAD FINISH:** 100% Matte Sn (Tin)

**MOUNTING POSITION:** Any

QUALIFIED MAX REFLOW TEMPERATURE: 260°C

Device Meets MSL 1 Requirements

### **MAXIMUM RATINGS**

Rating	Symbol	Max	Unit
Total Device Dissipation FR–5 Board, (Note 1) @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	200 1.5	mW mW/°C
Thermal Resistance from Junction-to-Ambient	$R_{\theta JA}$	635	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C

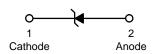
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. FR-4 Minimum Pad.



# ON Semiconductor®

### http://onsemi.com





SOD-523 CASE 502

### **MARKING DIAGRAM**



XX = Specific Device Code d = Date Code

### ORDERING INFORMATION

Device	Package	Shipping <sup>†</sup>
MM5ZxxxST1	SOD-523 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

### **DEVICE MARKING INFORMATION**

See specific marking information in the device marking column of the Electrical Characteristics table on page 2 of this data sheet

# **ELECTRICAL CHARACTERISTICS**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted,}$ 

 $V_F = 0.9 \text{ V Max.} @ I_F = 10 \text{ mA for all types})$ 

Symbol	Parameter						
VZ	Reverse Zener Voltage @ I <sub>ZT</sub>						
I <sub>ZT</sub>	Reverse Current						
Z <sub>ZT</sub>	Maximum Zener Impedance @ I <sub>ZT</sub>						
I <sub>ZK</sub>	Reverse Current						
Z <sub>ZK</sub>	Maximum Zener Impedance @ I <sub>ZK</sub>						
I <sub>R</sub>	Reverse Leakage Current @ V <sub>R</sub>						
V <sub>R</sub>	Reverse Voltage						
I <sub>F</sub>	Forward Current						
V <sub>F</sub>	Forward Voltage @ I <sub>F</sub>						
ΘVZ	Maximum Temperature Coefficient of V <sub>Z</sub>						
С	Max. Capacitance @V <sub>R</sub> = 0 and f = 1 MHz						

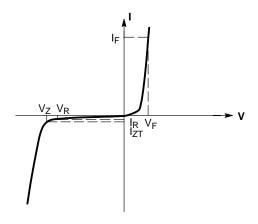


Figure 1. Zener Voltage Regulator

# **ELECTRICAL CHARACTERISTICS** ( $V_F = 0.9 \text{ Max} \otimes I_F = 10 \text{ mA}$ for all types)

		Test	Zener Voltage VZ		Z <sub>ZK</sub> I <sub>Z</sub> = 0.5	Z <sub>ZT</sub> I <sub>Z</sub> = IZT @ 10%	Max IR @ VR		d <sub>VZ</sub> /dt (mV/k) @ l <sub>ZT1</sub> = 5 mA		C pF Max @
Device	Device Marking	Current Izt mA	Min	Max	mA Ω Max	Mod Ω Max	μΑ	v	Min	Max	V <sub>R</sub> = 0 f = 1 MHz
MM5Z2V4ST1	T2	5.0	2.43	2.63	1000	100	120	1.0	-3.5	0	450
MM5Z2V7ST1	T3	5.0	2.67	2.91	1000	100	100	1.0	-3.5	0	450
MM5Z3V6ST1	T6	5.0	3.60	3.85	1000	90	5.0	1.0	-3.5	0	450
MM5Z3V9ST1	T7	5.0	3.89	4.16	1000	90	3.0	1.0	-3.5	-2.5	450
MM5Z4V3ST1	T8	5.0	4.17	4.43	1000	90	3.0	1.0	-3.5	0	450
MM5Z4V7ST1	Т9	5.0	4.55	4.75	800	80	3.0	2.0	-3.5	0.2	260
MM5Z5V1ST1	TA	5.0	4.98	5.2	500	60	2.0	2.0	-2.7	1.2	225
MM5Z5V6ST1	TC	5.0	5.49	5.73	200	40	1.0	2.0	-2.0	2.5	200
MM5Z6V2ST1	TE	5.0	6.06	6.33	100	10	3.0	4.0	0.4	3.7	185
MM5Z6V8ST1	TF	5.0	6.65	6.93	160	15	2.0	4.0	1.2	4.5	155
MM5Z7V5ST1	TG	5.0	7.28	7.6	160	15	1.0	5.0	2.5	5.3	140
MM5Z8V2ST1	TH	5.0	8.02	8.36	160	15	0.7	5.0	3.2	6.2	1358
MM5Z9V1ST1	TK	5.0	8.85	9.23	160	15	0.5	6.0	3.8	7.0	130

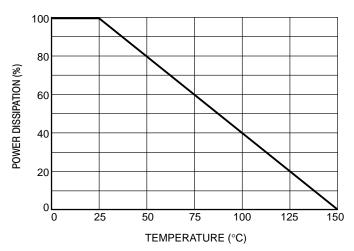
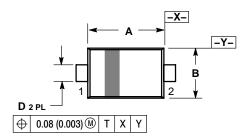
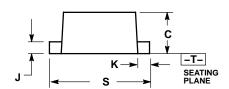


Figure 2. Steady State Power Derating

### PACKAGE DIMENSIONS

SOD-523 CASE 502-01 **ISSUE A** 



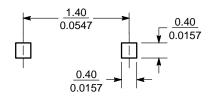


#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  CONTROLLING DIMENSION: MILLIMETER.
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

	МІ	LLIMETE	RS	INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	1.10	1.20	1.30	0.043	0.047	0.051	
В	0.70	0.80	0.90	0.028	0.032	0.035	
C	0.50	0.60	0.70	0.020	0.024	0.028	
D	0.25	0.30	0.35	0.010	0.012	0.014	
J	0.07	0.14	0.20	0.0028	0.0055	0.0079	
K	0.15	0.20	0.25	0.006	0.008	0.010	
S	1.50	1.60	1.70	0.059	0.063	0.067	

### **SOLDERING FOOTPRINT\***



SCALE 10:1

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and was are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its partnif rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 61312, Phoenix, Arizona 85082-1312 USA Phone: 480-829-7710 or 800-344-3860 Toll Free USA/Canada Fax: 480-829-7709 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free

Japan: ON Semiconductor, Japan Customer Focus Center 2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051 Phone: 81-3-5773-3850

ON Semiconductor Website: http://onsemi.com

Order Literature: http://www.onsemi.com/litorder

For additional information, please contact your local Sales Representative.