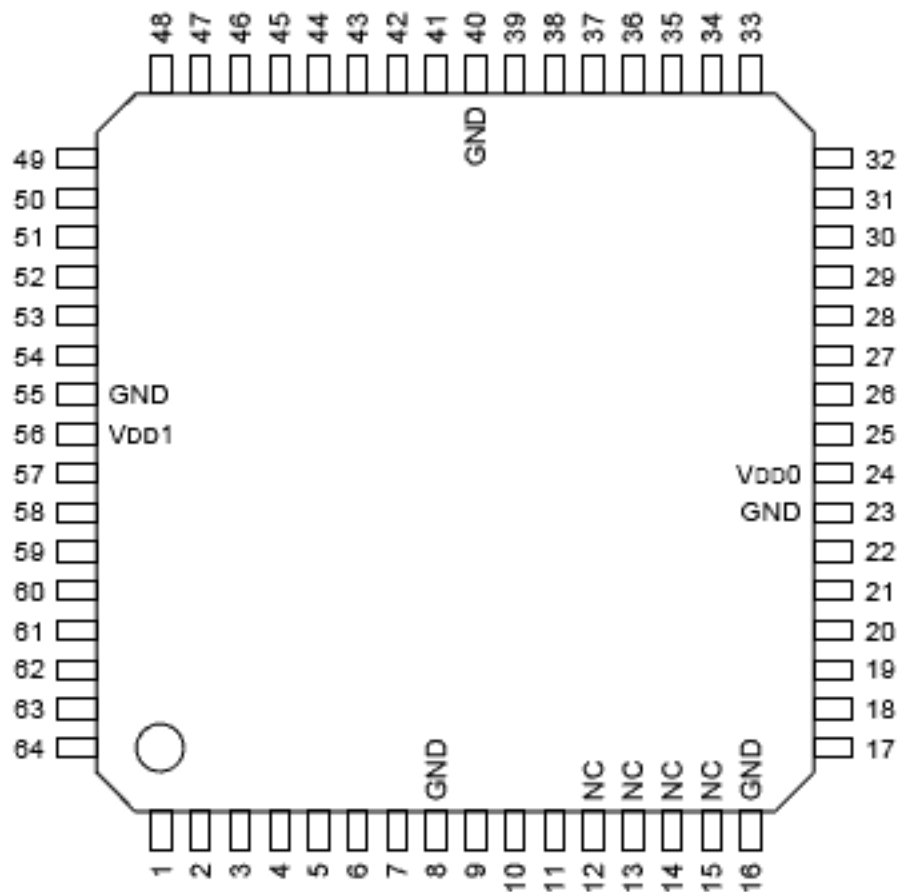


C-MOS SIGNAL PROCESSOR

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	MIXIN0	17	I	IN0	33	I	DATA4	49	I	ARDTIN
2	I	MIXIN1	18	I	IN1	34	I	DATA5	50	I	BLKGI
3	I	MIXIN2	19	I	IN2	35	I	DATA6	51	I	OC
4	I	MIXIN3	20	I	IN3	36	I	DATA7	52	O	OUT9
5	I	MIXIN4	21	I	IN4	37	I	TSTEN	53	O	OUT8
6	O	MIXOUT0	22	I	IN5	38	I	COEFSEL1	54	O	OUT7
7	O	MIXOUT1	23	—	GND	39	I	COEFSEL0	55	—	GND
8	—	GND	24	—	VDD0	40	—	GND	56	—	VDD1
9	O	MIXOUT2	25	I	IN6	41	I	MCK	57	O	OUT6
10	O	MIXOUT3	26	I	IN7	42	I	NRLEV3	58	O	OUT5
11	O	MIXOUT4	27	I	IN8	43	I	NRLEV2	59	O	OUT4
12	—	NC	28	I	IN9	44	I	NRLEV1	60	O	OUT3
13	—	NC	29	I	DATA0	45	I	NRLEV0	61	O	OUT2
14	—	NC	30	I	DATA1	46	I	RCK	62	O	OUT1
15	—	NC	31	I	DATA2	47	I	NREN	63	O	OUT0
16	—	GND	32	I	DATA3	48	I	RCFEN	64	O	ARDTOUT