

MOS FIELD EFFECT TRANSISTOR

2SK3054

N-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

DESCRIPTION

The 2SK3054 is a switching device which can be driven directly by a 2.5-V power source.

The 2SK3054 has excellent switching characteristics, and is suitable for use as a high-speed switching device in digital circuits.

ORDERING INFORMATION

PART NUMBER	PACKAGE	
2SK3054	SC-70	

FEATURES

- Can be driven by a 2.5-V power source
- Low gate cut-off voltage

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs= 0 V)	VDSS	50	V
Gate to Source Voltage (Vps= 0 V)	Vgss	±7	V
Drain Current (DC)	ID(DC)	±0.1	Α
Drain Current (pulse) Note	ID(pulse)	±0.2	Α
Total Power Dissipation	Рт	150	mW
Channel Temperature	Tch	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Note PW \leq 10 ms, Duty cycle \leq 50 %

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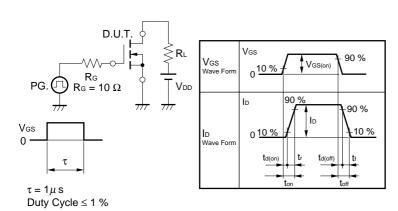
Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.



ELECTRICAL CHARACTERISTICS (T_A = 25 °C)

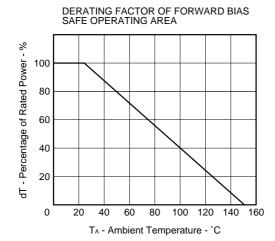
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Drain Cut-off Current	IDSS	V _{DS} = 50 V, V _{GS} = 0 V			1	μΑ
Gate Leakage Current	Igss	Vgs = ±7 V, Vps = 0 V			±5	μΑ
Gate to Source Cut-off Voltage	V _{GS(off)}	$V_{DS} = 3 \text{ V}, I_{D} = 1 \mu A$	0.9	1.2	1.5	V
Forward Transfer Admittance	yfs	V _{DS} = 3 V, I _D = 10 mA	20	38		mS
Drain to Source On-state Resistance	RDS(on)1	V _{GS} = 2.5 V, I _D = 10 mA		22	40	Ω
	R _{DS(on)2}	V _{GS} = 4.0 V, I _D = 10 mA		14	20	Ω
Input Capacitance	Ciss	V _{DS} = 3 V		8		pF
Output Capacitance	Coss	V _G S = 0 V		7		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		3		pF
Turn-on Delay Time	td(on)	V _{DD} = 3 V		15		ns
Rise Time	tr	I _D = 20 mA		100		ns
Turn-off Delay Time	t d(off)	V _{GS(on)} = 3 V		30		ns
Fall Time	tf	$R_G = 10 \Omega$, $R_L = 150 \Omega$		35		ns

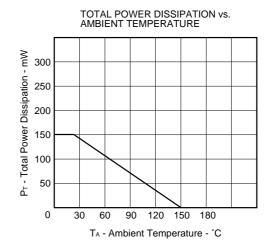
TEST CIRCUIT SWITCHING TIME

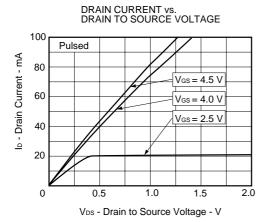


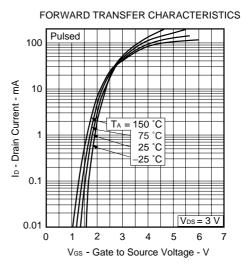


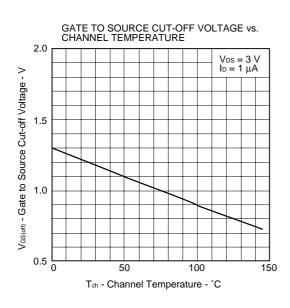
TYPICAL CHARACTERISTICS (TA = 25 °C)

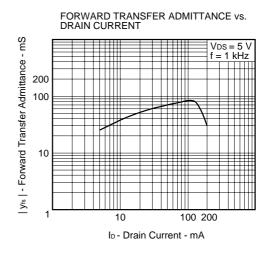






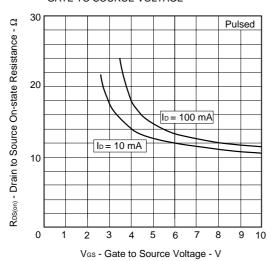


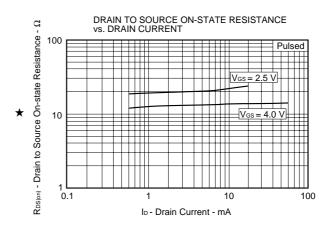




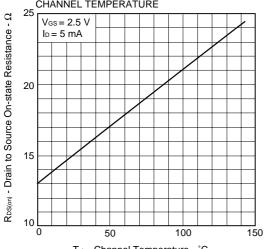
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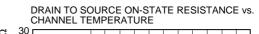
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

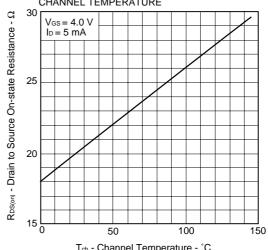




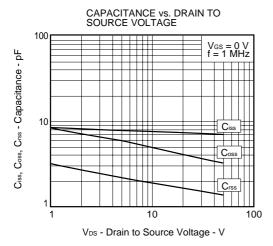
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



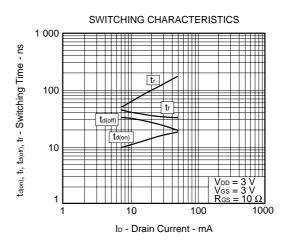




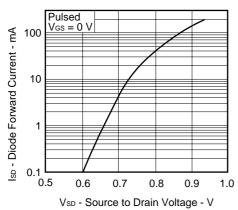
T_{ch} - Channel Temperature - $^{\circ}C$



Tch - Channel Temperature - °C

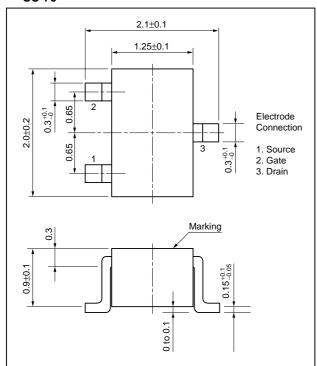


SOURCE TO DRAIN DIODE FORWARD VOLTAGE

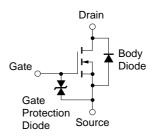


PACKAGE DRAWING (Unit: mm)

SC-70



EQUIVALENT CIRCUIT



Marking: G25

Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

NEC 2SK3054

[MEMO]

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