

STGY50NB60HD

PRELIMINARY DATA

N-CHANNEL 50A - 600V MAX247 PowerMESHTM IGBT

TYPE	Vces	V _{CE(sat)}	Ι _C
STGY50NB60HD	600 V	< 2.8 V	50 A

- HIGH INPUT IMPEDANCE
- (VOLTAGE DRIVEN)
- LOW ON-VOLTAGE DROP (VCESAT)
- LOW GATE CHARGE
- HIGH CURRENT CAPABILITY
- VERY HIGH FREQUENCY OPERATION
- OFF LOSSES INCLUDE TAIL CURRENT
- CO-PACKAGED WITH TURBOSWITCHTM ANTIPARALLEL DIODE

DESCRIPTION

Using the latest high voltage technology based on a patented strip layout, STMicroelectronics has designed an advanced family of IGBTs, the PowerMESHTM IGBTs, with outstanding perfomances. The suffix "H" identifies a family optimized to achieve very low switching times for high frequency applications (<120kHz).

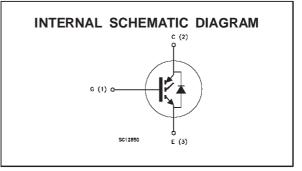
APPLICATIONS

- HIGH FREQUENCY MOTOR CONTROLS
- WELDING EQUIPMENTS

ABSOLUTE MAXIMUM RATINGS

 SMPS AND PFC IN BOTH HARD SWITCH AND RESONANT TOPOLOGIES

MAX247



Symbol	Parameter	Value	Unit
V _{CES}	Collector-Emitter Voltage ($V_{GS} = 0$)	600	V
V _{GE}	Gate-Emitter Voltage	± 20	V
Ι _C	Collector Current (continuous) at $T_c = 25 \ ^{\circ}C$	100	A
lc	Collector Current (continuous) at $T_c = 100$ °C	50	A
Ісм(●)	Collector Current (pulsed)	400	A
P _{tot}	Total Dissipation at $T_c = 25 \ ^{\circ}C$	250	W
	Derating Factor	2	W/ºC
Tstg	Storage Temperature	-65 to 150	°C
Tj	Max. Operating Junction Temperature	150	°C

(•) Pulse width limited by safe operating area

THERMAL DATA

ſ	R _{thj-case}	Thermal	Resistance	Junction-case	Мах	0.5	°C/W
	R _{thj-amb}	Thermal	Resistance	Junction-ambient	Max	30	oC/W
	R _{thc-h}	Thermal	Resistance	Case-heatsink	Тур	0.1	°C/W

ELECTRICAL CHARACTERISTICS (T_j = 25 $^{\circ}$ C unless otherwise specified) OFF

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
VBR(CES)	Collector-Emitter Breakdown Voltage	$I_{C} = 250 \ \mu A$ $V_{GE} = 0$	600			V
I _{CES}	Collector cut-off (V _{GE} = 0)				100 1000	μΑ μΑ
I _{GES}	Gate-Emitter Leakage Current (V _{CE} = 0)	$V_{GE} = \pm 20 \text{ V} \qquad V_{CE} = 0$			± 100	nA

ON (*)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{GE(th)}	Gate Threshold Voltage	$V_{CE} = V_{GE}$ I _C = 250 µA	3		5	V
V _{CE(SAT)}		$ \begin{array}{lll} V_{GE} = \ 15 \ V & I_C = 50 \ A \\ V_{GE} = \ 15 \ V & I_C = 50 \ A & T_j = \ 125 \ ^oC \end{array} $		2.3 1.9	2.8	V V

DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
gfs	Forward Transconductance	V _{CE} =25 V I _C = 50 A		22		S
C _{ies} C _{oes} C _{res}	Input Capacitance Output Capacitance Reverse Transfer Capacitance	$V_{CE} = 25 V f = 1 MHz V_{GE} = 0$		4500 450 90		pF pF pF
Q _G Q _{GE} Q _{GC}	Total Gate Charge Gate-Emitter Charge Gate-Collector Charge	$V_{CE} = 480 \text{ V}$ I _C = 50 A V _{GE} = 15 V		260 28 15		nC nC nC
I _{CL}	Latching Current		200			A

SWITCHING ON

Symbol	Parameter	Test Cond	ditions	Min.	Тур.	Max.	Unit
t _{d(on)} t _r	Delay Time Rise Time	V _{CC} = 480 V V _{GE} = 15 V	$I_{\rm C} = 50 \text{ A}$ $R_{\rm G} = 10\Omega$		20 70		ns ns
(di/dt) _{on}	Turn-on Current Slope	V _{CC} = 480 V R _G = 10 Ω	I _C = 50 A V _{GE} = 15 V		350		A/µs
E _{on} (ɔ)	Turn-on Switching Losses	T _j = 125 °C			950		μJ

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ELECTRICAL CHARACTERISTICS (continued)

SWITCHING OFF

Symbol	Parameter	Test Con	ditions	Min.	Тур.	Max.	Unit
$\begin{array}{c} t_c \\ t_r(v_{off}) \\ t_d(_{off}) \\ t_f \\ E_{off}(^{**}) \\ E_{ts}(_{o}) \end{array}$	Cross-Over Time Off Voltage Rise Time Delay Time Fall Time Turn-off Switching Loss Total Switching Loss	V _{CC} = 480 V R _{GE} = 10 Ω	I _C = 50 A V _{GE} = 15 V		166 48 326 90 2.1 3		ns ns ns mJ mJ
$\begin{array}{c} t_c \\ t_r(v_{off}) \\ t_d(_{off}) \\ t_f \\ E_{off}(^{**}) \\ E_{ts}(_{O}) \end{array}$	Cross-Over Time Off Voltage Rise Time Delay Time Fall Time Turn-off Switching Loss Total Switching Loss	$V_{CC} = 480 V$ $R_{GE} = 10 \Omega$ $T_j = 125 \ ^{\circ}C$	I _C = 50 A V _{GE} = 15 V		270 75 340 200 2.9 3.85		ns ns ns mJ mJ

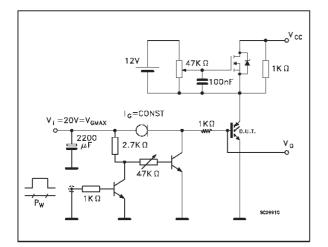
COLLECTOR-EMITTER DIODE

Symbol	Parameter	Test Conc	ditions	Min.	Тур.	Max.	Unit
l _f I _{fm}	Forward Current Forward Current pulsed					50 400	A A
V _f	Forward On-Voltage	$I_{f} = 50 A$ $I_{f} = 50 A$	T _j = 125 °C		2		V V
t _{rr} Q _{rr} I _{rrm}	Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current	l _f = 50 A dI/dt = 100 A/μS	V _R = 200 V T _j = 125 °C		200		nS nC A

(•) Pulse width limited by max. junction temperature (>) Include recovery losses on the STTA2006 freewheeling diode

(*) Pulsed: Pulse duration = 300 $\mu s,$ duty cycle 1.5 % (**)Losses Include Also The Tail (Jedec Standardization)

Fig. 1: Gate Charge test Circuit





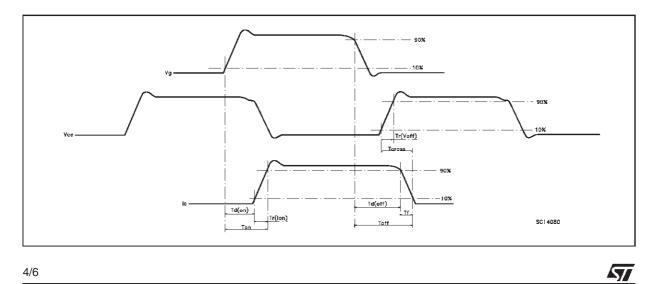
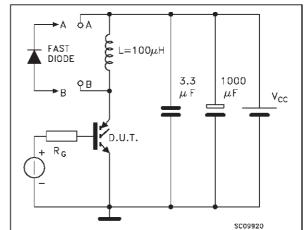
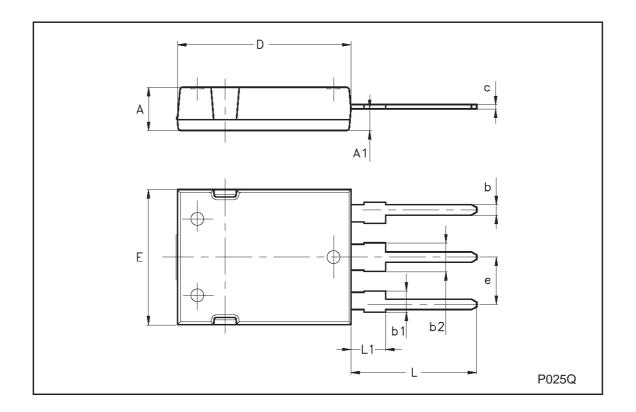


Fig. 2: Test Circuit For Inductive Load Switching



DIM.		mm			inch	
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А	4.70		5.30			
A1	2.20		2.60			
b	1.00		1.40			
b1	2.00		2.40			
b2	3.00		3.40			
С	0.40		0.80			
D	19.70		20.30			
е	5.35		5.55			
E	15.30		15.90			
L	14.20		15.20			

Max247 MECHANICAL DATA



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