

Video Signal Driver for DVD Player Monolithic IC MM1566

Outline

This IC is a video signal driver that supports 3-ch interlace, and was developed for use in DVD players. It contains a low pass filter that attenuates the noise element during DA conversion, and a 6dB amp and 75ΩX3 line driver.

It also has a SAG correction pin for reducing output coupling capacitance, and reinforced output pin ESD protection element allows reduction of external ESD protection diodes.

Features

- (1)Has SAG correction pin.
- (2)3-line drive possible with 75Ω driver.
- (3)Built-in fourth low pass filter.
Frequency response: 6.75MHz ± 1dB / 27MHz – 27dB min.
- (4)Built-in 6dB amp.
- (5)Built-in power save function.
- (6)S/N=80dB typ.(Y/C mix:74dB typ.)
- (7)For aerial discharge, ±15kV ESD protection withstand pressure (IEC standard)
- (8)Mode selection pins corresponding to each video signal.

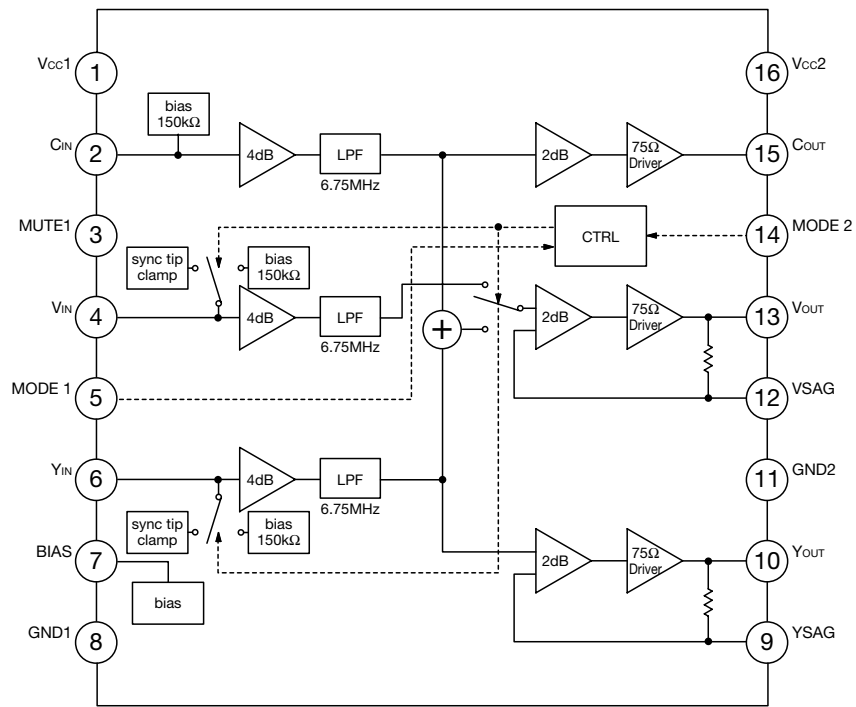
Package

SOP-16C

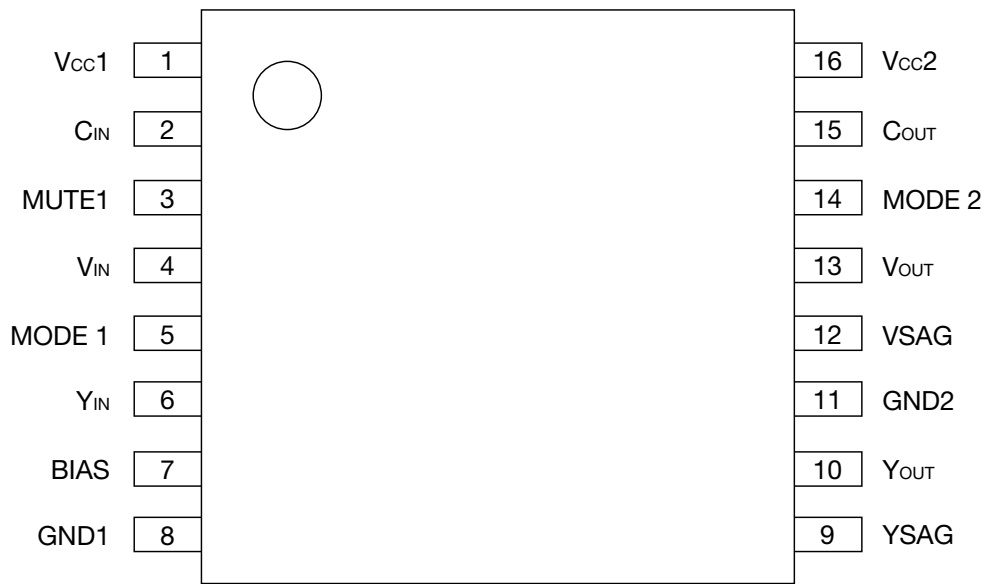
Applications

- (1)DVD players
- (2)Digital STB
- (3)Other digital video equipment

Block diagram



Pin Assignment



SOP-16C

1	Vcc1	9	YSAG
2	Cin	10	Yout
3	MUTE1	11	GND2
4	Vin	12	VSAG
5	MODE 1	13	Vout
6	Yin	14	MODE 2
7	BIAS	15	Cout
8	GND1	16	Vcc2

Pin Description

Pin No.	Pin name	Function	Internal equivalent circuit diagram
1 16	V _{cc1} V _{cc2}	V _{cc}	
2	C _{IN}	Croma input	
3	MUTE1	Mute select Using of MUTE and POWER-SAVING.	
4 6	V _{IN} Y _{IN}	Video input The input can select Sync tip clamp or Bias	
5 14	MODE1 MODE2	Mode select	

Pin Description

Pin No.	Pin name	Function	Internal equivalent circuit diagram
7	BIAS	Bias	
8 11	GND1 GND2	GND	
10 13		Signal output	
9 12		SAG correction	
15	COUT	Croma output	

Absolute Maximam Ratings (Ta=25°C)

Item	Symbol	Rating	Unit
Storage temperature	T _{STG}	-65 ~ +150	°C
Operating temperature	T _{OPR}	-40 ~ +85	°C
Supply Voltage	V _{CC max.}	7	V
Power dissipation *1	P _d	1.0	W

note *1 Board mounting power dissipation. Board size 21.3mmX38.1mmX1.0mm

Recommended Operating Conditions

Item	Symbol	Rating	Unit
Operating temperature	T _{OPR}	-40 ~ +85	°C
Operating Voltage	V _{CCOP}	4.5 ~ 5.5	V

Electrical Characteristics (Unless otherwise specified, Ta=25°C,Vcc=5V)

Item	Symbol	Measurement Conditions	Min.	Typ.	Max.	Unit
Consumption current 1	I _{CC1}	No signal	39	55	71	mA
Consumption current 2	I _{CC2}	No signal Mute1:ON	1	3	5	mA
Bias input	V _{INbias}	at Bias select	1.9	2.4	2.9	V
Clamp input	V _{INclamp}	at Clamp select	1.15	1.4	1.65	V
Bias output	V _{OUTbias}	at Bias select		2.4		V
Clamp output	V _{OUTclamp}	at Clamp select		1.1		V
Control terminal input current	H I _{IHM} *2				350	μA
	L I _{IL} *2				35	μA
Control terminal input voltage	H V _{thHM} *2		2.1			V
	L V _{thLM} *2				0.7	V
Input impedance	Z _{INbias}	at Bias select	100	150	200	kΩ
Voltage gain	G _n *3	S _{IN} wave:1V f=100kHz	5.7	6.0	6.3	dB
Frequency characteristic 1	f _{1n} *3	S _{IN} wave:1V 6.75MHz/100kHz	-1.0	0	1.0	dB
Frequency characteristic 2	f _{2n} *3	S _{IN} wave:1V 27MHz/100kHz		-40	-27	dB
Differential gain	DG _{1~3} *3	Staircase signal 1V		0.6	1.0	%
Differential phase	DP _{1~3} *3	Staircase signal 1V		0.6	1.0	°
Output dynamic range	DR _n *3	S _{IN} wave:100kHz THD=1.0%	2.6	3.0		V
Crosstalk	CT _n *3	f=4.43MHz, 1V		-60	-55	dB
S/N 1	SN _{14,5} *3	BW:100k ~ 6MHz		-80		dB
S/N 2	SN _{21~3} *3	BW:100k ~ 6MHz at MIX OUT		-74		dB
Group delay	t _{GDn} *3	at 100kHz		50		ns
Group delay deviation	Δt _{GDn} *3	to 3.58MHz		4		ns
		to 4.43MHz		7		ns
		to 6MHz		12		ns

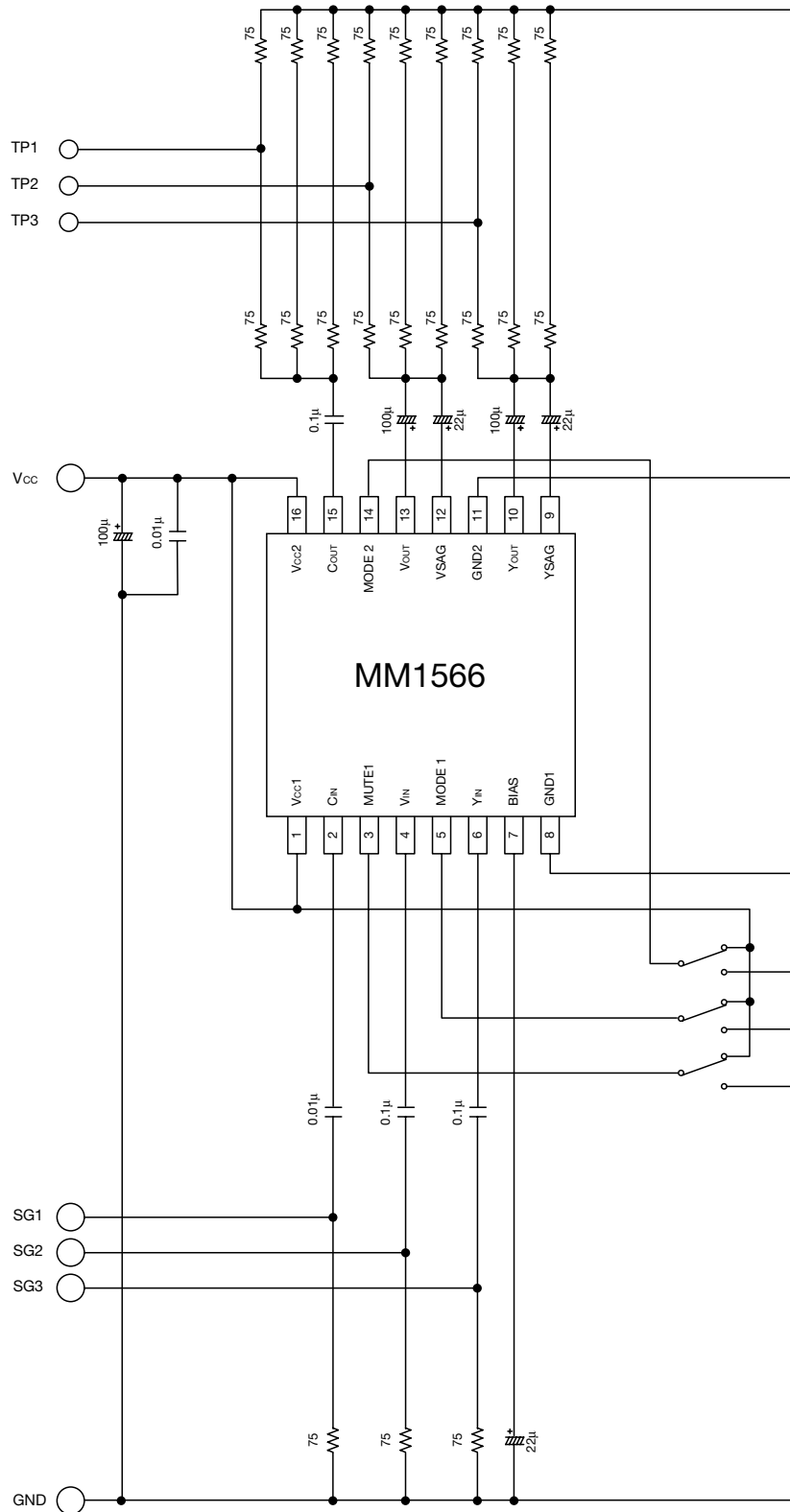
note *2 The subscript number "m" is the terminal of right table.

m	Terminal
1	MUTE1
2	MODE1
3	MODE2

note *3 The subscript number "n" is the combination of right table.

n	Input	Output
1	C _{IN}	V _{OUT}
2	V _{IN}	
3	Y _{IN}	
4	C _{IN}	C _{OUT}
5	Y _{IN}	Y _{OUT}

Measuring Circuit

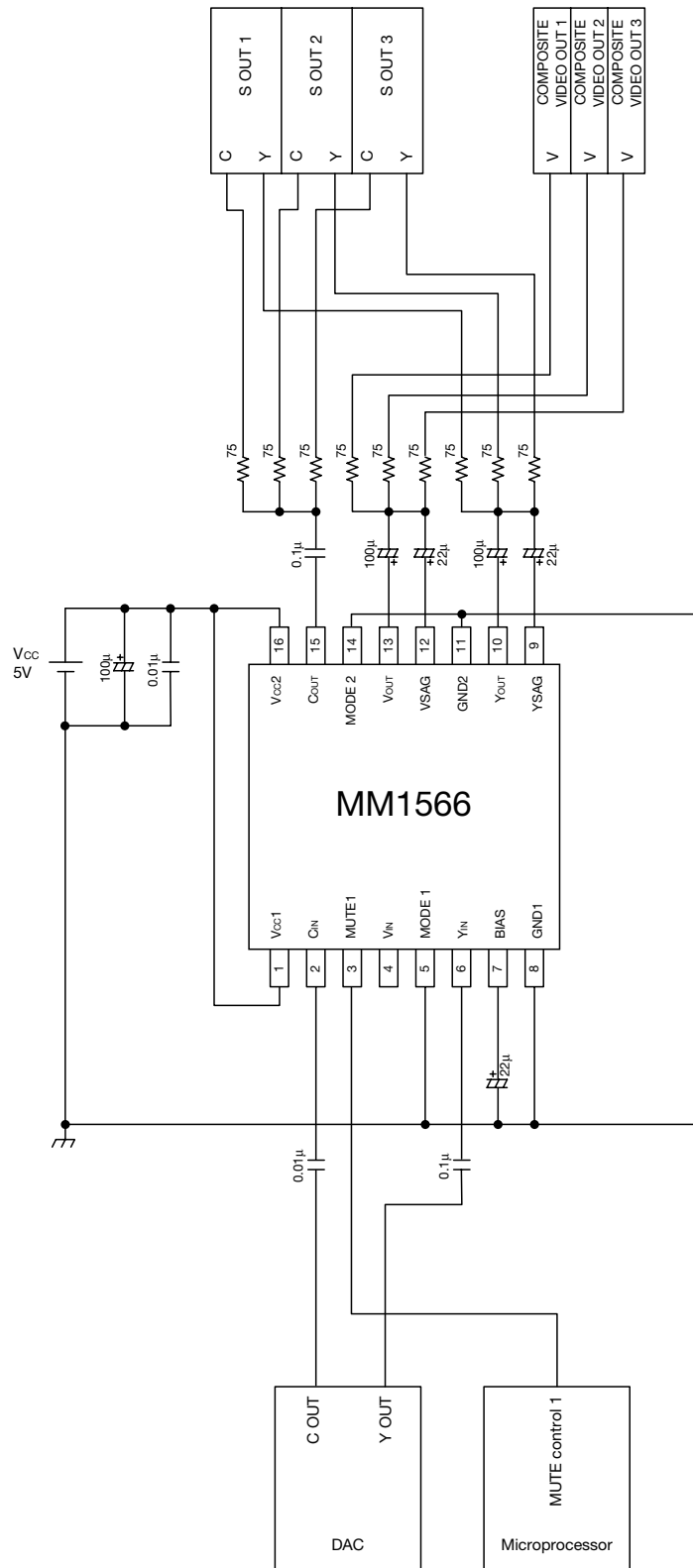


Switch Control Table

	Input Terminal	Input Signal (Input clamp)	Output Signal	Control Terminal		
				MUTE1	MODE1	MODE2
1	MUTE	*	MUTE	Low	*	*
	C _{IN}	C(bias)	C	High	Low	Low
	V _{IN}		Composite Video(Y+C)			
	Y _{IN}	Y(clamp)	Y			
2	MUTE	*	MUTE	Low	*	*
	C _{IN}	C(bias)	C	High	High	Low
	V _{IN}	Composite Video(clamp)	Composite Video			
	Y _{IN}	Y(clamp)	Y			
3	MUTE	*	MUTE	Low	*	*
	C _{IN}	Cr(bias)	Cr	High	Low	High
	V _{IN}	Cb(bias)	Cb			
	Y _{IN}	Y(clamp)	Y			
4	MUTE	*	MUTE	Low	*	*
	C _{IN}	R(bias)	R	High	High	High
	V _{IN}	G(bias)	G			
	Y _{IN}	B(bias)	B			

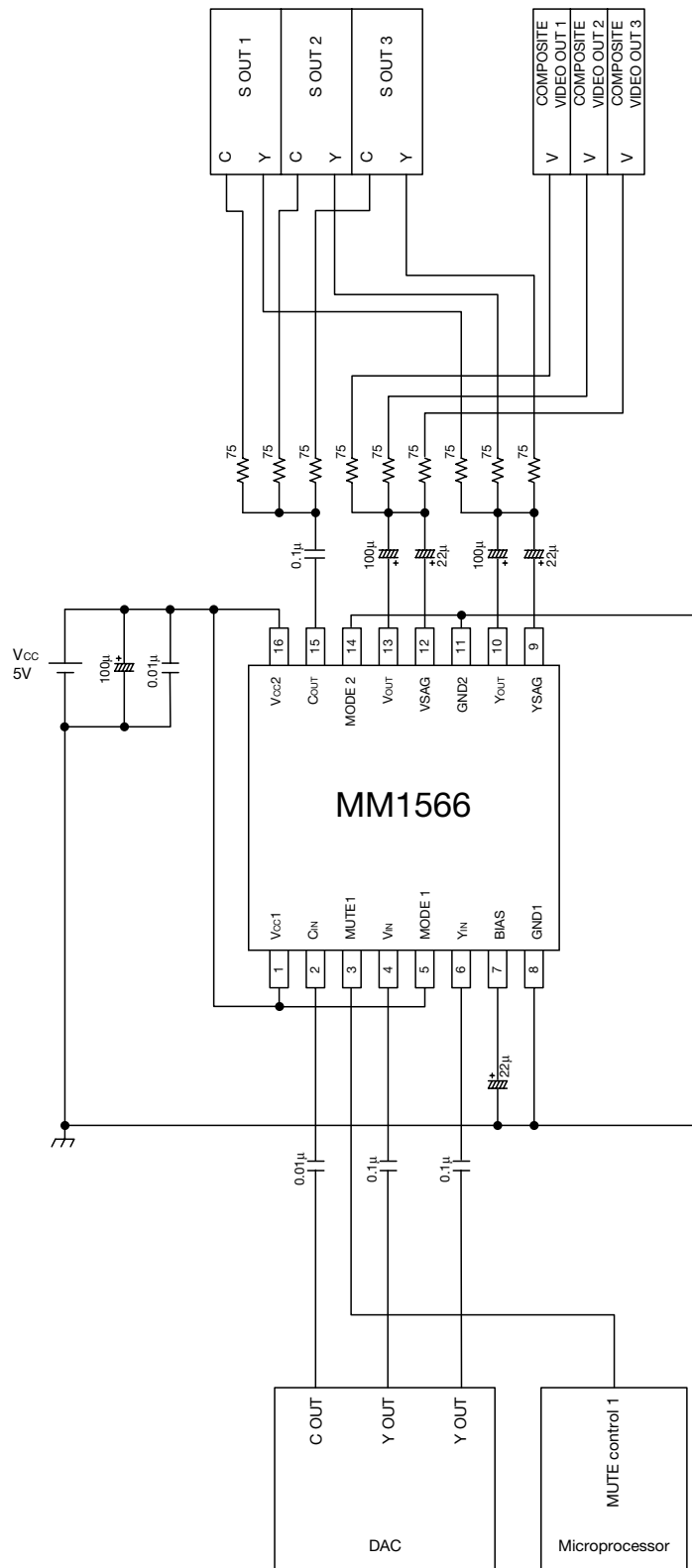
* : Don't care

Application Circuit 1



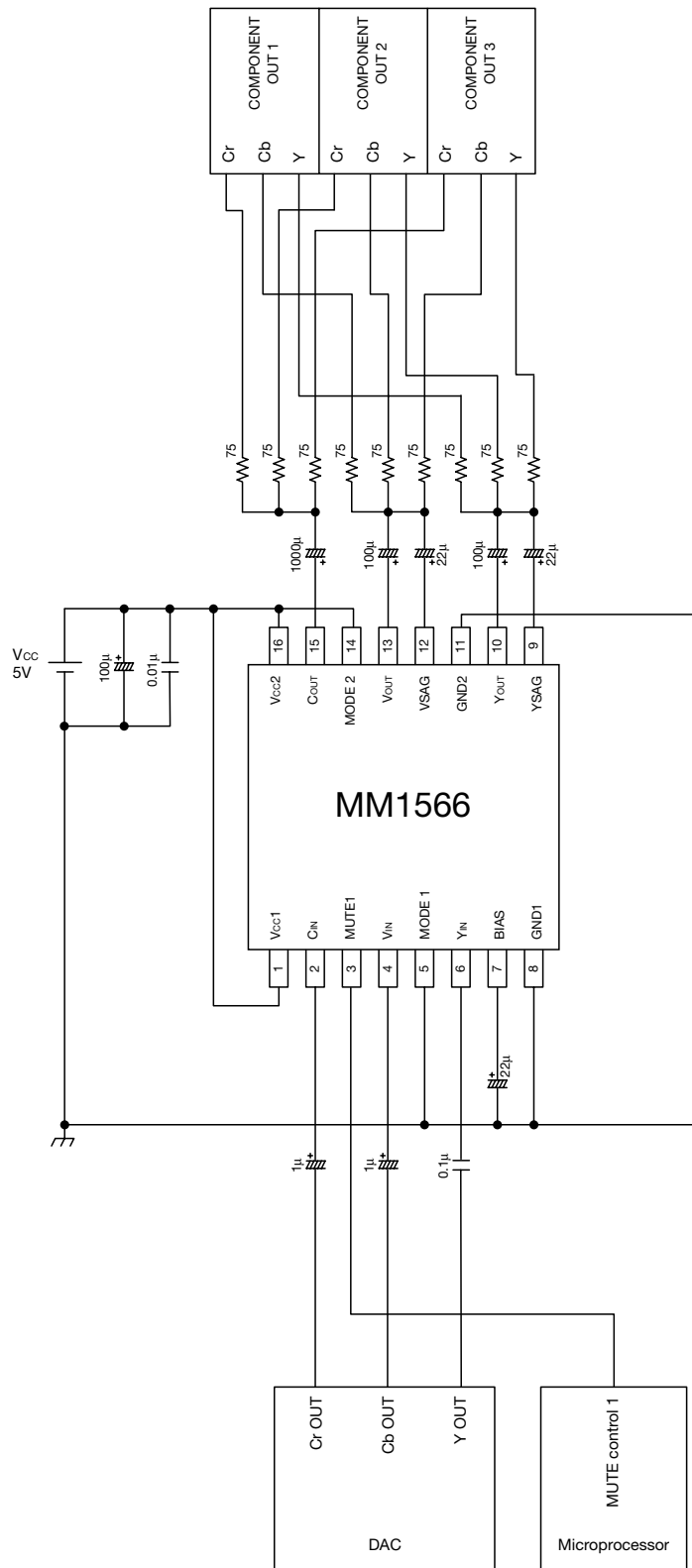
(note) : Please arrange power supply bypass capacitor near the Vcc2 terminal (16PIN).

Application Circuit 2



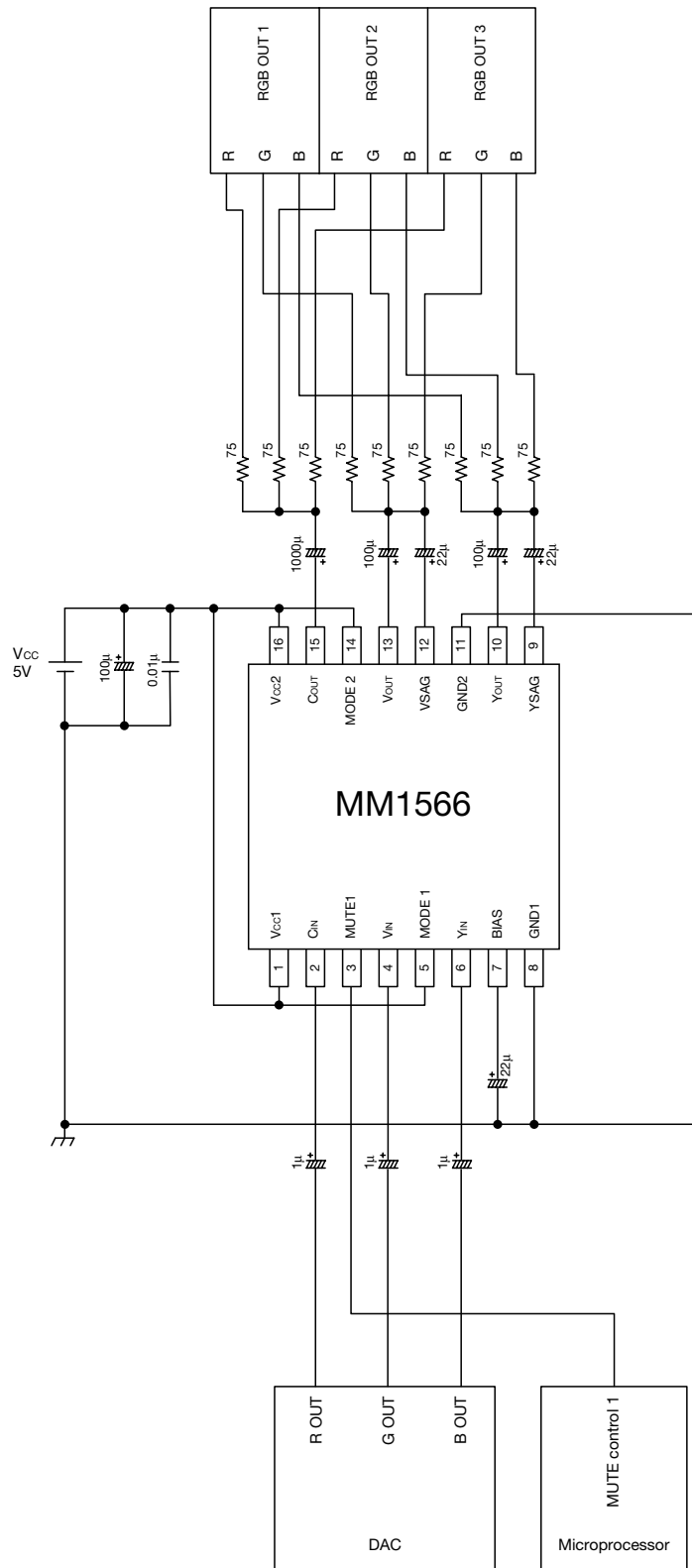
(note) : Please arrange power supply bypass capacitor near the Vcc2 terminal (16PIN).

Application Circuit 3



(note) : Please arrange power supply bypass capacitor near the Vcc2 terminal (16PIN).

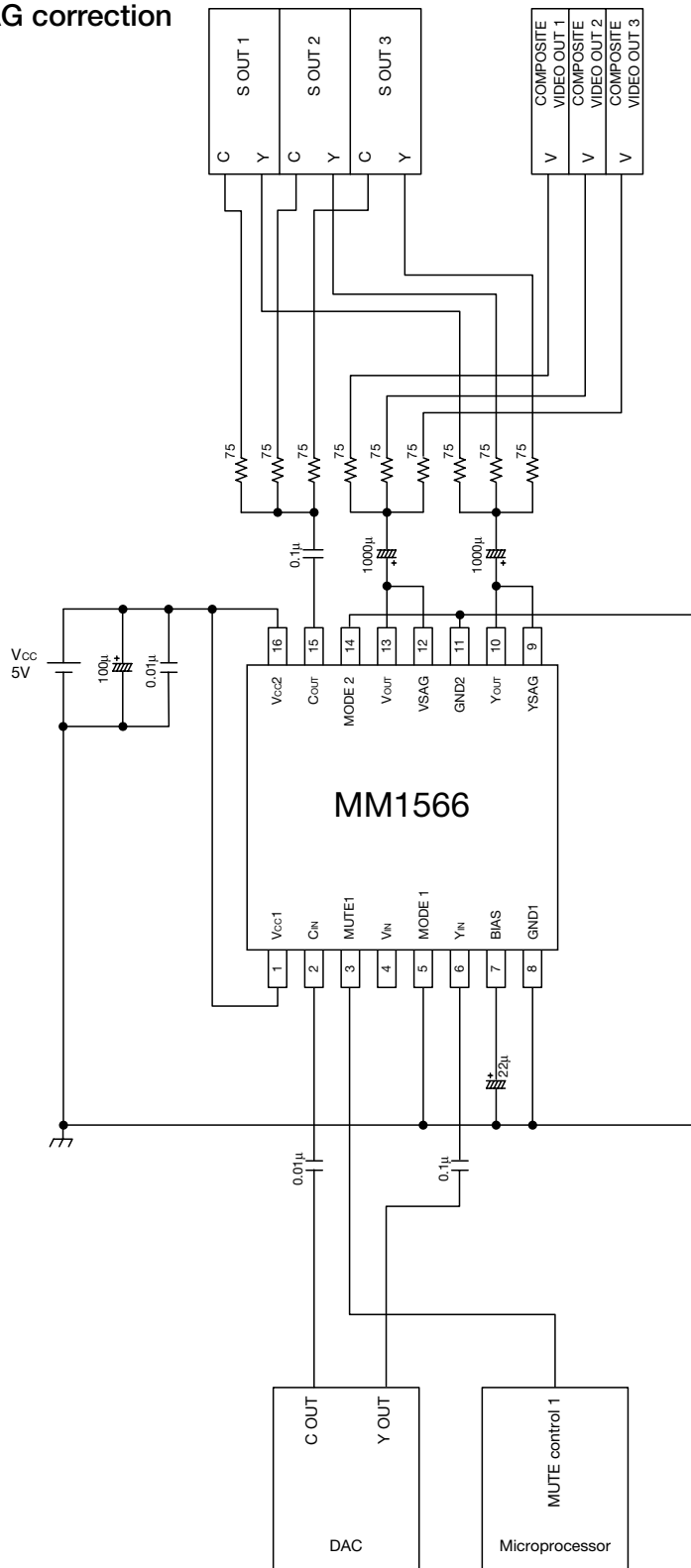
Application Circuit 4



(note) : Please arrange power supply bypass capacitor near the Vcc2 terminal (16PIN).

Application Circuit 5

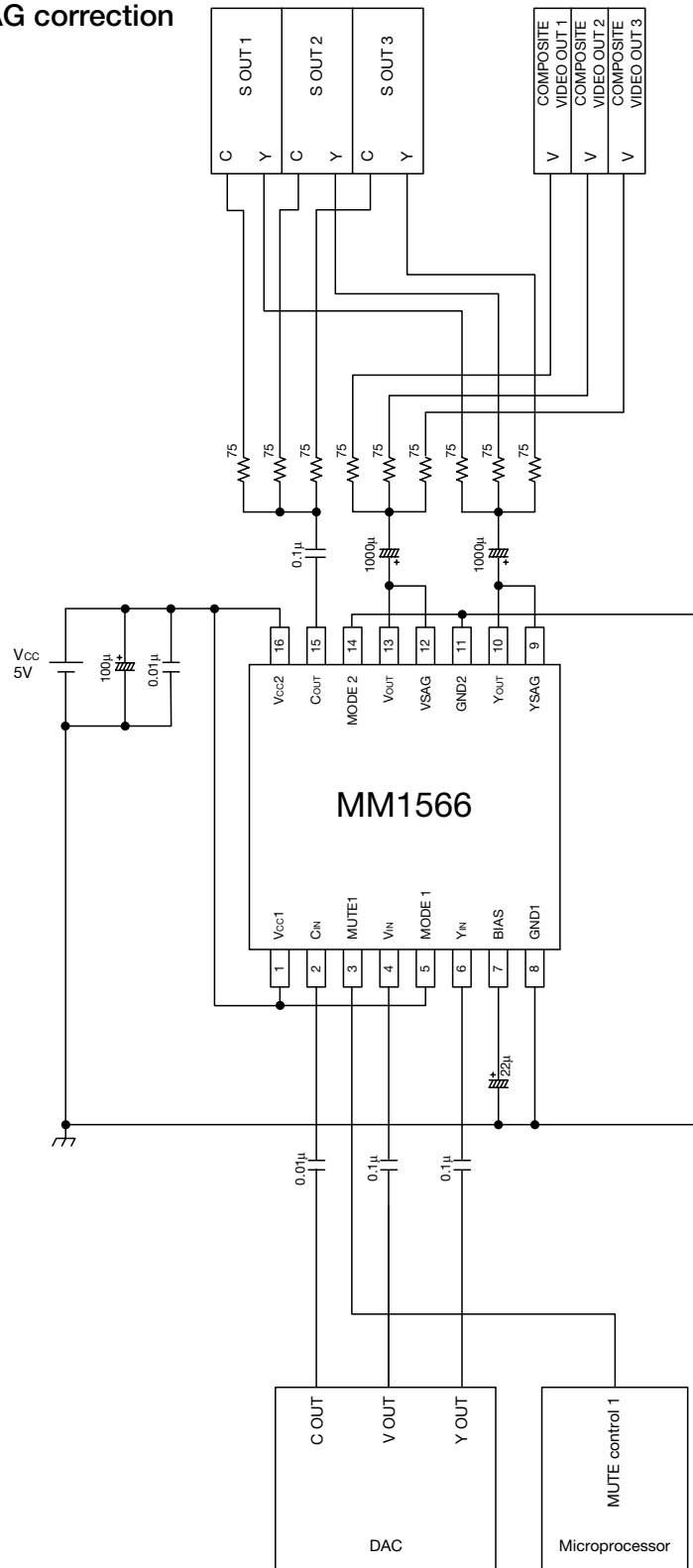
■ At unused the SAG correction function



(note) : Please arrange power supply bypass capacitor near the Vcc2 terminal (16PIN).

Application Circuit 6

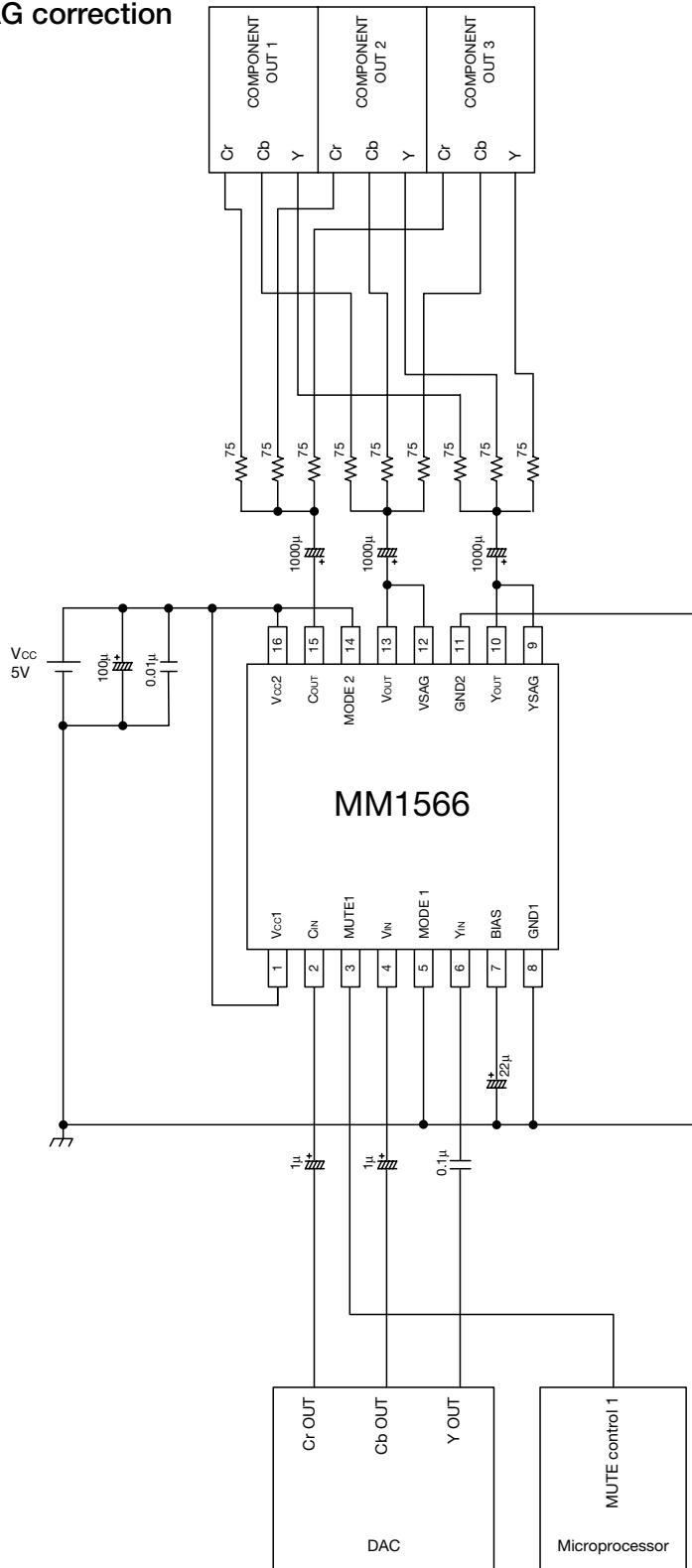
■ At unused the SAG correction function



(note) : Please arrange power supply bypass capacitor near the Vcc2 terminal (16PIN).

Application Circuit 7

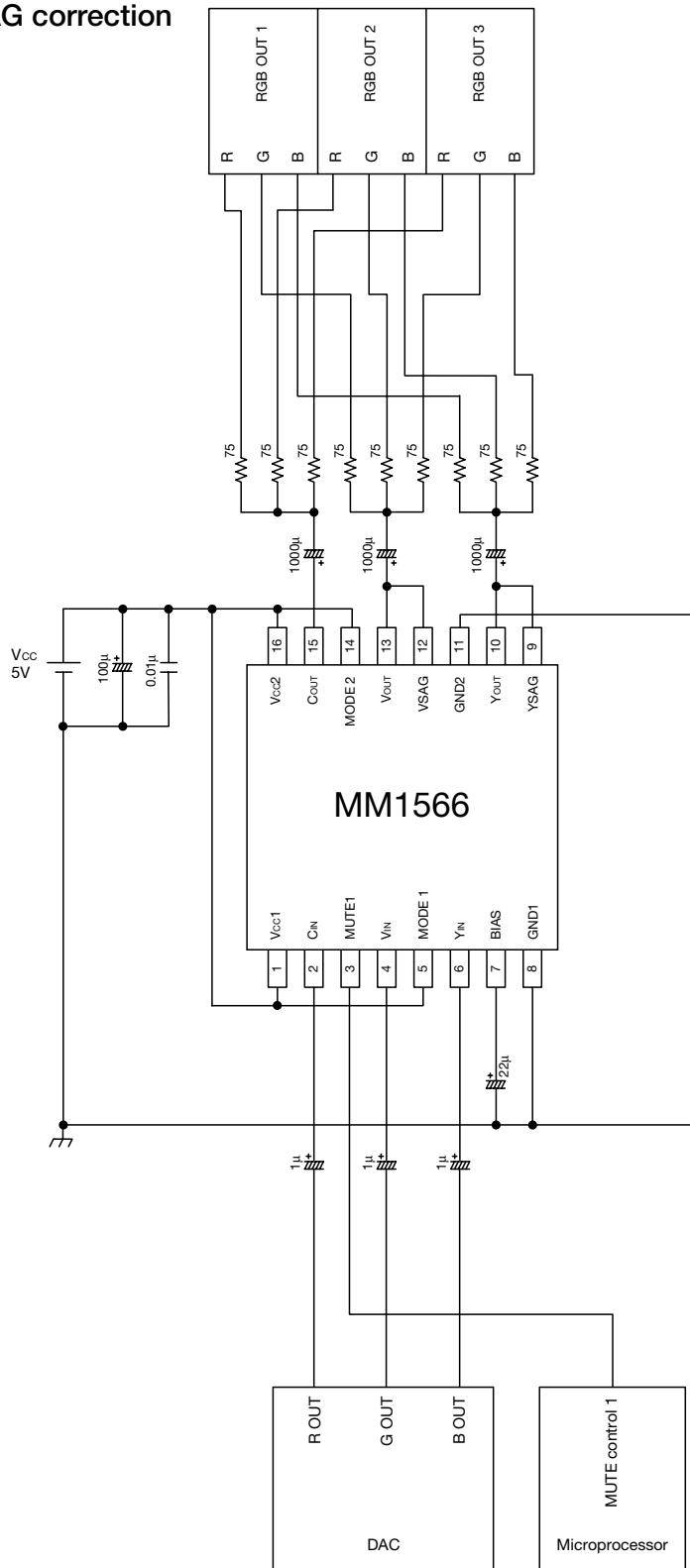
- At unused the SAG correction function



(note) : Please arrange power supply bypass capacitor near the Vcc2 terminal (16PIN).

Application Circuit 8

■ At unused the SAG correction function



(note) : Please arrange power supply bypass capacitor near the Vcc2 terminal (16PIN).