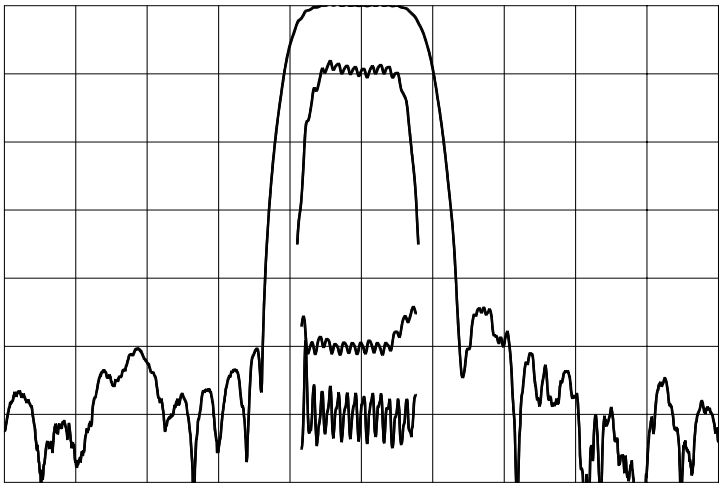
Micro Networks, 324 Clark Street, Worcester, MA 01606, USA tel: 508-852-5400, fax:508-852-8456, www.micronetworks.com

TYPICAL PERFORMANCE



Horizontal: 4 MHz/div

Vertical (from top):

Magnitude	10,1	dB/div
Phase	5	deg/div
Group Delay	50	ns/div

SPECIFICATION

Parameter		Min	Typ	Max	Units
Center Frequency, Fc (fixed reference)			77.8		MHz
Operating Bandwidth			3.84		MHz
Insertion Loss at Fc			12	15	dB
Amplitude Ripple[1]			0.3	1	dB
Triple travel suppression		35	42		dB
Lower 35 dB Frequency		71.5	72.5		MHz
Upper 35 dB Frequency			83.0	84.1	MHz
Phase Linearity[1]			2	8	deg
Group Delay Variation[1]			45	100	ns
Absolute Delay			1.06	1.5	µs
Rejection[2]	0.3~7 MHz	25	44		dB
	7~63 MHz	45	55		dB
	63~71.5 MHz	35	50		dB
	84.1~87.8 MHz	32	42		dB
	87.8~93 MHz	40	53		dB
	93~140 MHz	45	57		dB
	140~170 MHz	25	31		dB
	170~300 MHz	45	60		dB
Input and Output Return Loss[1,5]		10			dB
Operating Input Power				-10	dBm
Maximum Input Power Without Damage		17			dBm
Operating Temperature Range[4]		-10		+85	°C
Storage Temperature Range		-40		+95	°C
System Source and Load Impedance			50		Ω

Notes:
1. Applies over the operating bandwidth
2. Rejection relative to passband insertion loss
3. Measured with tones spaced at 10 and 20 MHz above and below Fc
4. All parameters to be met over the operating temperature range, except return loss
5. Room temperature only.

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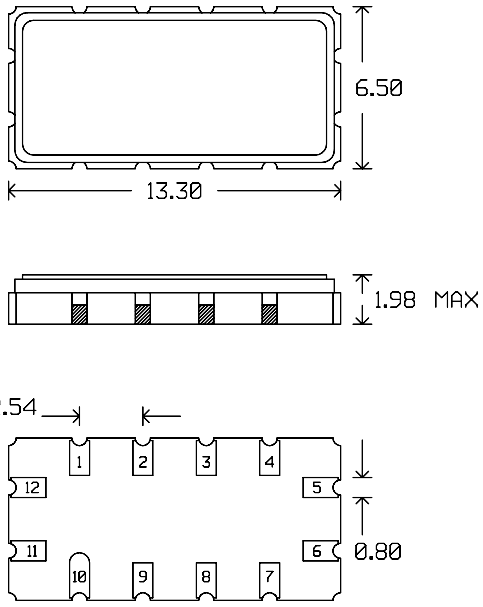
PACKAGE OUTLINE

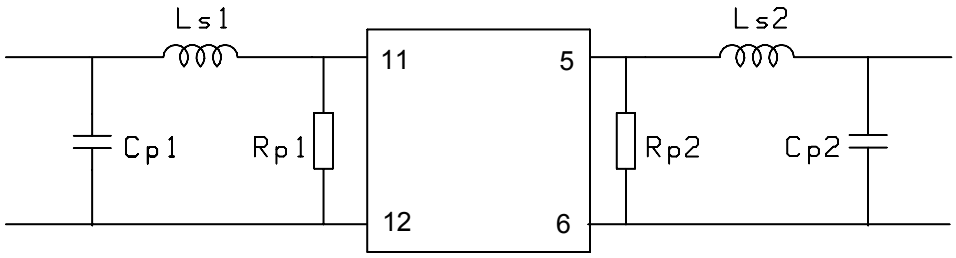


Units: mm

Pin Configuration:

Input:	11
Input Return:	12
Output:	5
Output Return:	6
Ground:	1,2,3,4,7,8,9,10

MATCHING CIRCUIT



Component values in 50 Ω:	Rp1 = 220 Ω	Rp2= 220 Ω
(Minimum Q = 45)	Ls1 = 120 nH	Ls2= 150 nH
	Cp1 = 47 pF	Cp2= 33 pF

Notes

- Optimum component values may change depending on board layout. The values shown here are intended as a guide only.