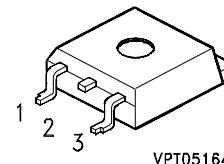
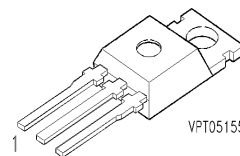


SIPMOS® Power Transistor

- N-Channel
- Enhancement mode
- Avalanche rated
- Logic Level
- dv/dt rated
- 175°C operating temperature



Pin 1	Pin 2	Pin 3
G	D	S

Type	V_{DS}	I_D	$R_{DS(on)}$	@ V_{GS}	Package	Ordering Code
SPB70N10L	100 V	70 A			P-TO263-3-2	Q67040-S4170
SPP70N10L			0.025 Ω 0.016 Ω	$V_{GS} = 4.5 \text{ V}$ $V_{GS} = 10 \text{ V}$	P-TO220-3-1	Q67040-S4175

Maximum Ratings, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Value	Unit
Continuous drain current $T_C = 25^\circ\text{C}$ $T_C = 100^\circ\text{C}$	I_D	70 50	A
Pulsed drain current $T_C = 25^\circ\text{C}$	I_{Dpulse}	280	
Avalanche energy, single pulse $I_D = 70 \text{ A}$, $V_{DD} = 25 \text{ V}$, $R_{GS} = 25 \Omega$	E_{AS}	1000	mJ
Avalanche current, periodic limited by T_{jmax}	I_{AR}	70	A
Avalanche energy, periodic limited by $T_{j(max)}$	E_{AR}	25	mJ
Reverse diode dv/dt $I_S = 70 \text{ A}$, $V_{DD} \leq V_{(BR)DSS}$, $dI/dt = 200 \text{ A}/\mu\text{s}$, $T_{jmax} = 175^\circ\text{C}$	dv/dt	6	kV/ μs
Gate source voltage	V_{GS}	± 14	V
Gate source peak voltage, $t_p \leq 100 \mu\text{s}$	V_{gs}	± 20	
Power dissipation $T_C = 25^\circ\text{C}$	P_{tot}	250	W
Operating temperature	T_j	-55 ... +175	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 ... +175	
IEC climatic category; DIN IEC 68-1		55/175/56	

Electrical Characteristics

Parameter at $T_j = 25\text{ °C}$, unless otherwise specified	Symbol	Values			Unit
		min.	typ.	max.	

Thermal Characteristics

Thermal resistance, junction - case	R_{thJC}	-	tbd	0.6	K/W
Thermal resistance, junction - ambient	R_{thJA}	-	62.5	-	
SMD version, device on PCB: @ min. footprint @ 6 cm ² cooling area ¹⁾	R_{thJA}	- -	tbd tbd	- -	

Static Characteristics

Drain- source breakdown voltage $V_{GS} = 0\text{ V}$, $I_D = 0.25\text{ mA}$	$V_{(BR)DSS}$	100	-	-	V
Gate threshold voltage, $V_{GS} = V_{DS}$ $I_D = 240\text{ }\mu\text{A}$	$V_{GS(th)}$	1.2	1.6	2	
Zero gate voltage drain current $V_{DS} = 100\text{ V}$, $V_{GS} = 0\text{ V}$, $T_j = 25\text{ °C}$ $V_{DS} = 100\text{ V}$, $V_{GS} = 0\text{ V}$, $T_j = 150\text{ °C}$	I_{DSS}	- -	0.1 -	1 100	μA
Gate-source leakage current $V_{GS} = 20\text{ V}$, $V_{DS} = 0\text{ V}$	I_{GSS}	-	10	100	
Drain-Source on-state resistance $V_{GS} = 4.5\text{ V}$, $I_D = 50\text{ A}$ $V_{GS} = 10\text{ V}$, $I_D = 50\text{ A}$	$R_{DS(on)}$	- -	0.014 0.01	0.025 0.016	Ω

¹⁾ Device on 50mm*50mm*1.5mm epoxy PCB FR4 with 6 cm² (one layer, 70μm thick) copper area for drain connection. PCB is vertical without blown air.

Electrical Characteristics

Parameter at $T_j = 25\text{ °C}$, unless otherwise specified	Symbol	Values			Unit
		min.	typ.	max.	

Dynamic Characteristics

Transconductance $V_{DS} \geq 2 \cdot I_D \cdot R_{DS(on)max}$, $I_D = 50\text{ A}$	g_{fs}	30	65	-	S
Input capacitance $V_{GS} = 0\text{ V}$, $V_{DS} = 25\text{ V}$, $f = 1\text{ MHz}$	C_{iss}	-	3630	4540	pF
Output capacitance $V_{GS} = 0\text{ V}$, $V_{DS} = 25\text{ V}$, $f = 1\text{ MHz}$	C_{oss}	-	640	800	
Reverse transfer capacitance $V_{GS} = 0\text{ V}$, $V_{DS} = 25\text{ V}$, $f = 1\text{ MHz}$	C_{rss}	-	345	430	
Turn-on delay time $V_{DD} = 30\text{ V}$, $V_{GS} = 4.5\text{ V}$, $I_D = 70\text{ A}$, $R_G = 1.3\text{ }\Omega$	$t_{d(on)}$	-	70	105	ns
Rise time $V_{DD} = 30\text{ V}$, $V_{GS} = 4.5\text{ V}$, $I_D = 70\text{ A}$, $R_G = 1.3\text{ }\Omega$	t_r	-	250	375	
Turn-off delay time $V_{DD} = 30\text{ V}$, $V_{GS} = 4.5\text{ V}$, $I_D = 70\text{ A}$, $R_G = 1.3\text{ }\Omega$	$t_{d(off)}$	-	250	375	
Fall time $V_{DD} = 30\text{ V}$, $V_{GS} = 4.5\text{ V}$, $I_D = 70\text{ A}$, $R_G = 1.3\text{ }\Omega$	t_f	-	95	145	

Electrical Characteristics

Parameter at $T_j = 25\text{ °C}$, unless otherwise specified	Symbol	Values			Unit
		min.	typ.	max.	

Dynamic Characteristics

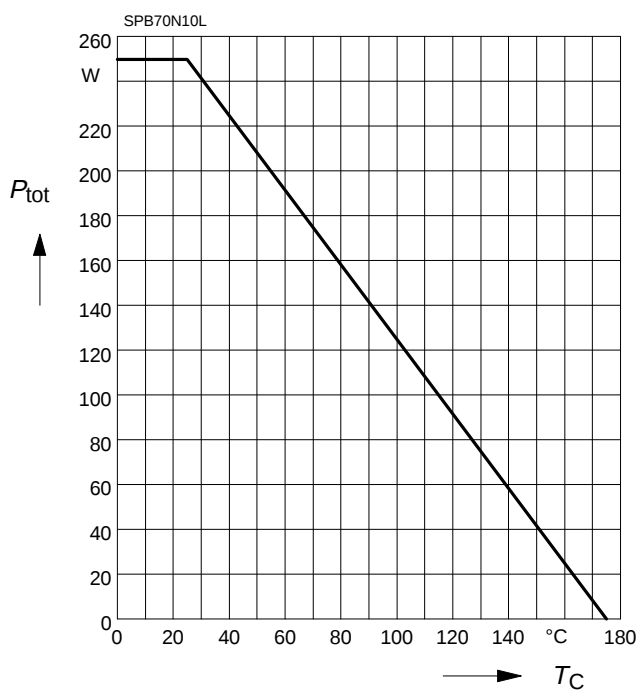
Gate charge at threshold $V_{DD} = 80\text{ V}$, $I_D \geq 0,1\text{ A}$, $V_{GS} = 0\text{ to }1\text{ V}$	$Q_{G(th)}$	-	10	15	nC
Gate charge at $V_{GS}=5\text{V}$ $V_{DD} = 80\text{ V}$, $I_D = 70\text{ A}$, $V_{GS} = 0\text{ to }5\text{ V}$	$Q_{g(5)}$	-	74	110	
Gate charge total $V_{DD} = 80\text{ V}$, $I_D = 70\text{ A}$, $V_{GS} = 0\text{ to }10\text{ V}$	Q_g	-	160	240	nC
Gate plateau voltage $V_{DD} = 80\text{ V}$, $I_D = 70\text{ A}$	$V_{(plateau)}$	-	3.22	-	V

Reverse Diode

Inverse diode continuous forward current $T_C = 25\text{ °C}$	I_S	-	-	70	A
Inverse diode direct current, pulsed $T_C = 25\text{ °C}$	I_{SM}	-	-	280	
Inverse diode forward voltage $V_{GS} = 0\text{ V}$, $I_F = 140\text{ A}$	V_{SD}	-	1.2	1.8	V
Reverse recovery time $V_R = 80\text{ V}$, $I_F = I_S$, $di_F/dt = 100\text{ A}/\mu\text{s}$	t_{rr}	-	100	150	ns
Reverse recovery charge $V_R = 80\text{ V}$, $I_F = I_S$, $di_F/dt = 100\text{ A}/\mu\text{s}$	Q_{rr}	-	600	900	nC

Power Dissipation

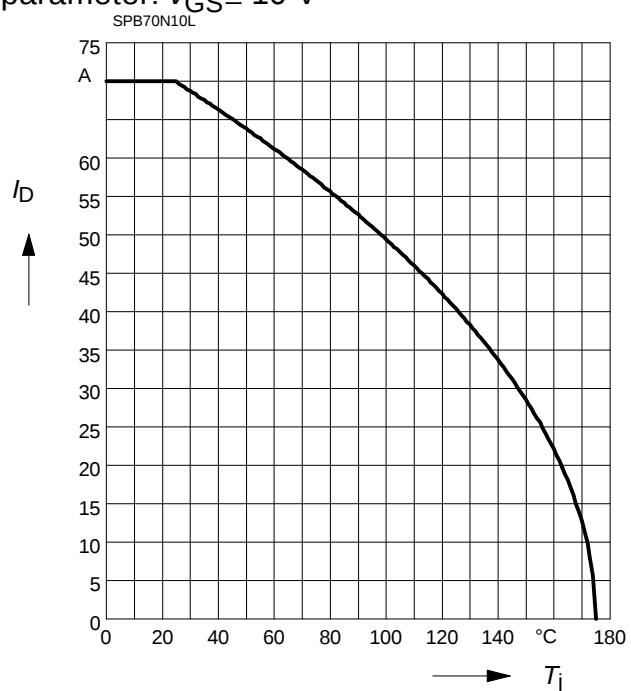
$$P_{\text{tot}} = f(T_C)$$



Drain current

$$I_D = f(T_C)$$

parameter: $V_{GS} \geq 10 \text{ V}$



Transient thermal impedance

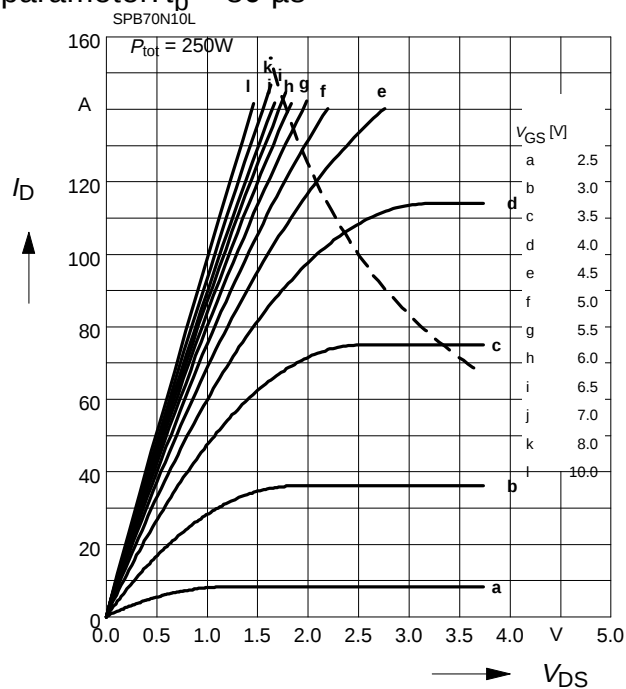
$$Z_{\text{thJC}} = f(t_p)$$

parameter: $D = t_p/T$

Typ. output characteristics

$$I_D = f(V_{DS})$$

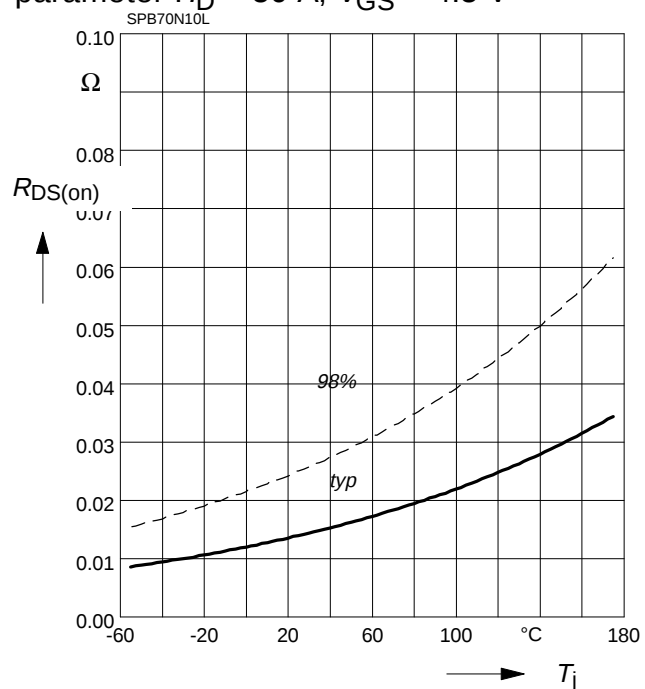
parameter: $t_D = 80 \mu s$



Drain-source on-resistance

$$R_{DS(on)} = f(T_j)$$

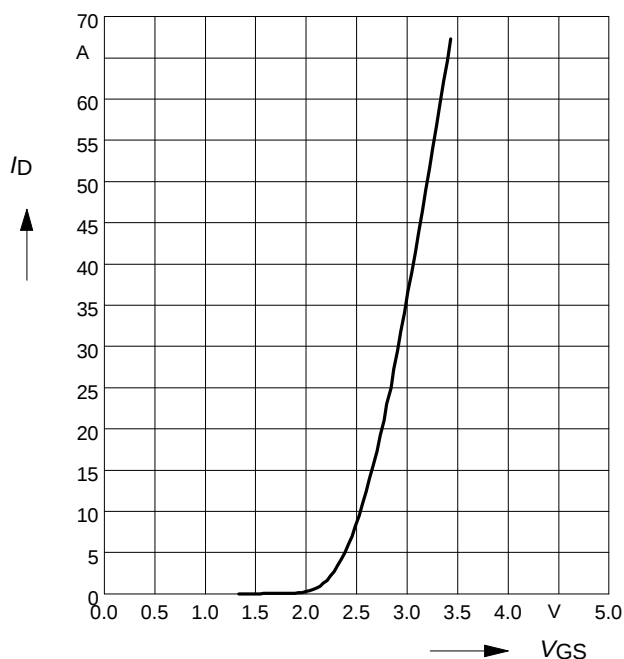
parameter: $I_D = 50 A$, $V_{GS} = 4.5 V$



Typ. transfer characteristics $I_D = f(V_{GS})$

parameter: $t_p = 80 \mu s$

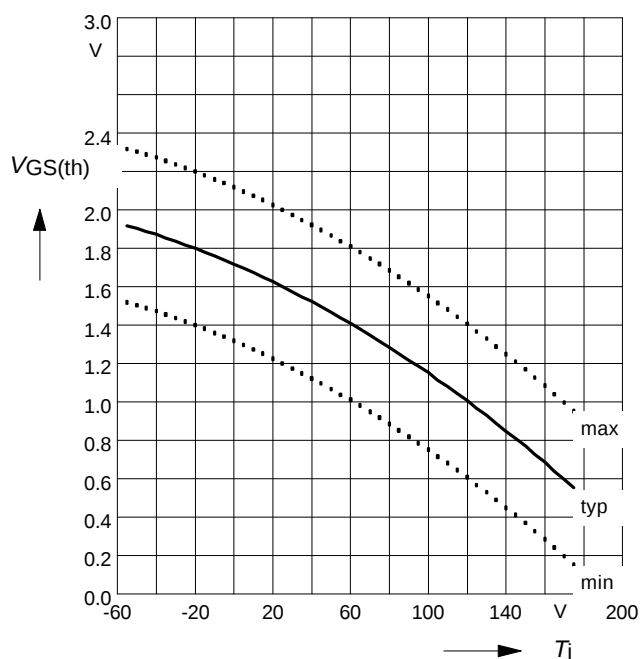
$$V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$$



Gate threshold voltage

$$V_{GS(th)} = f(T_j)$$

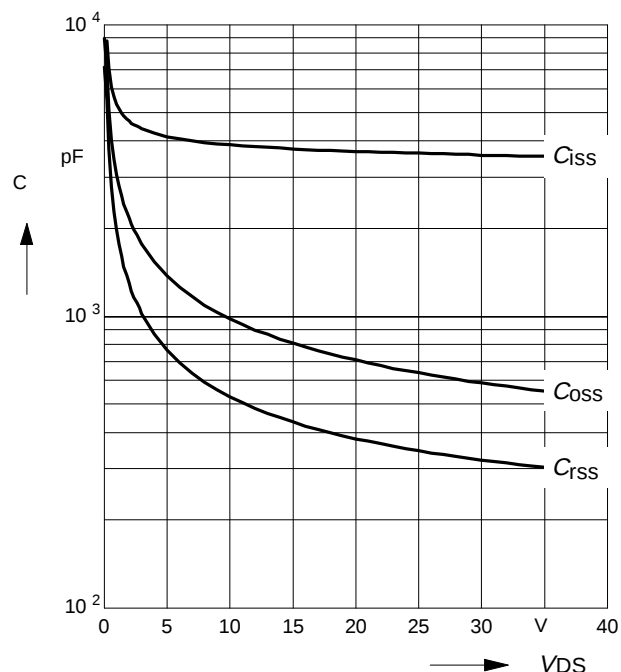
parameter: $V_{GS} = V_{DS}$, $I_D = 240 \mu A$



Typ. capacitances

$$C = f(V_{DS})$$

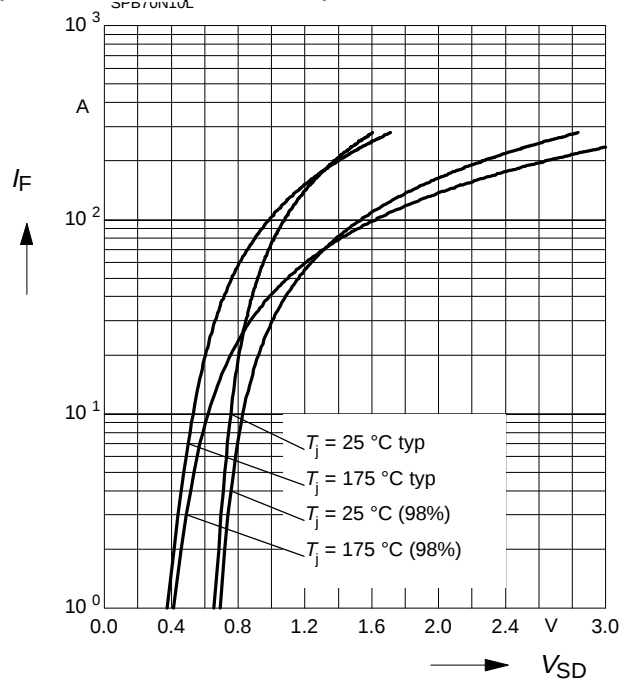
Parameter: $V_{GS} = 0$ V, $f = 1$ MHz



Forward characteristics of reverse diode

$$I_F = f(V_{SD})$$

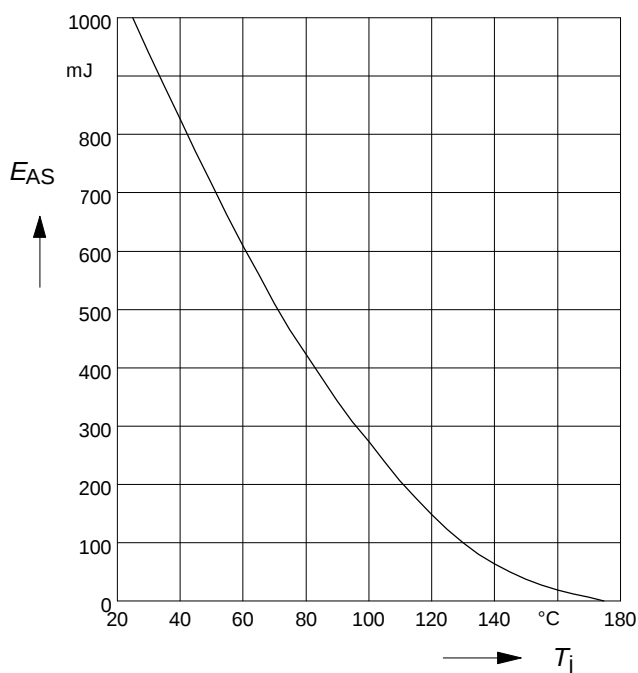
parameter: T_j , $t_p = 80 \mu s$



Avalanche Energy $E_{AS} = f(T_j)$

parameter: $I_D = 70 \text{ A}$, $V_{DD} = 25 \text{ V}$

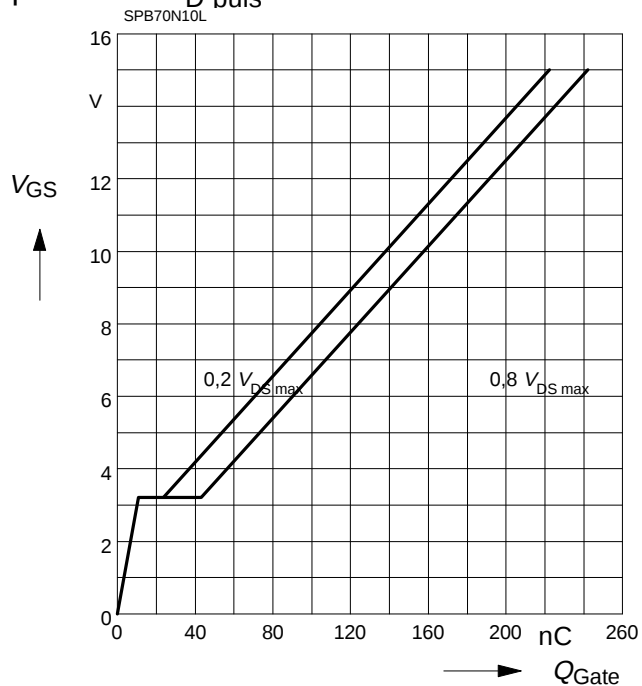
$R_{GS} = 25 \Omega$



Typ. gate charge

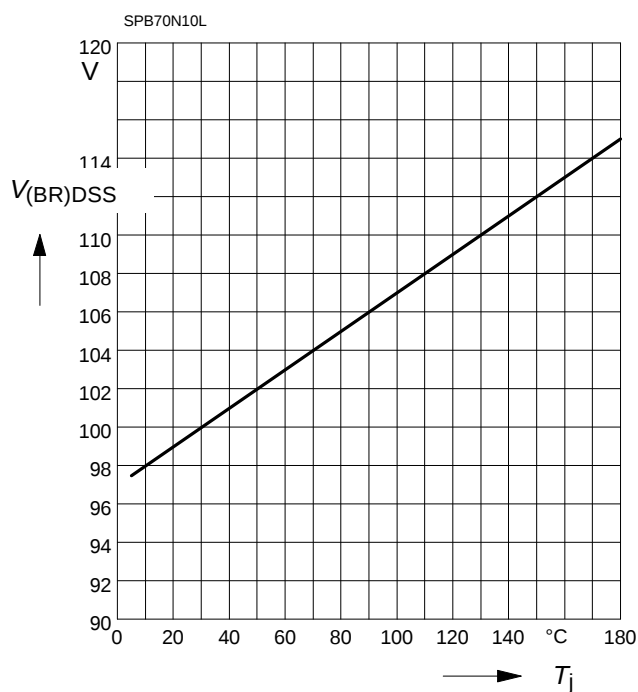
$V_{GS} = f(Q_{Gate})$

parameter: $I_{D \text{ puls}} = 70 \text{ A}$



Drain-source breakdown voltage

$V_{(BR)DSS} = f(T_j)$



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