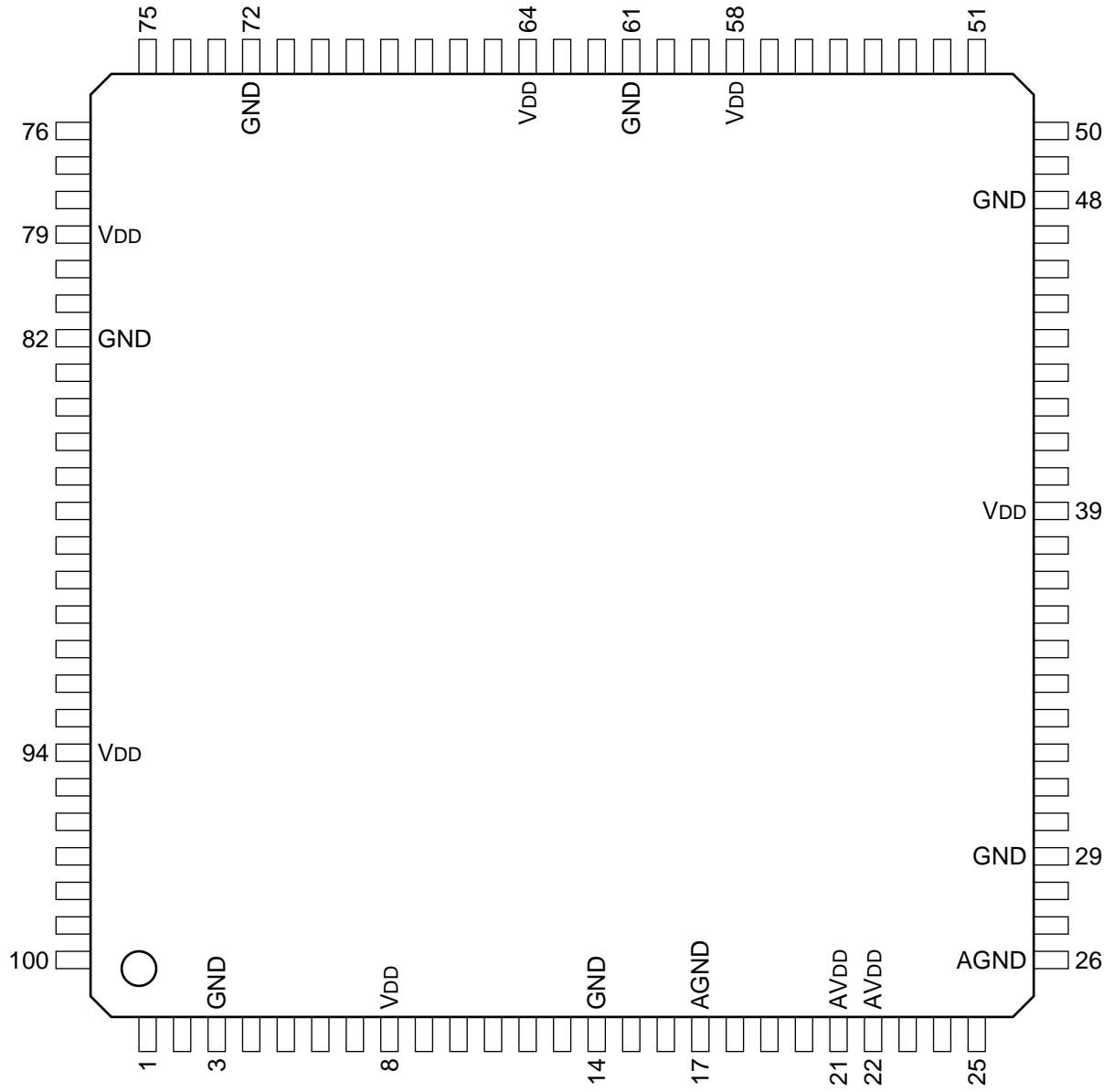


C-MOS 1CHIP CCD COLOR CAMERA SIGNAL PROCESSOR LSI

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	AFY1	26	—	AGND	51	O	VD	76	I	PBLK
2	O	AFY0	27	I	VREFY	52	O	SYNC	77	I	ID
3	—	GND	28	I	VBY	53	O	FLD	78	I	MCK
4	I	CI3	29	—	GND	54	O	CBLK	79	—	VDD
5	I	CI2	30	O	DICK	55	I	VRI	80	O	CLP1
6	I	CI1	31	O	YO7	56	O	AHD	81	I	TEST
7	I	CI0	32	O	YO6	57	O	AVD	82	—	GND
8	—	VDD	33	O	YO5	58	—	GND	83	I	AD8
9	O	CO3	34	O	YO4	59	O	HCOMP	84	I	AD7
10	O	CO2	35	O	YO3	60	I	SELVLP	85	I	AD6
11	O	CO1	36	O	YO2	61	—	GND	86	I	AD5
12	O	CO0	37	O	YO1	62	I	OSCI	87	I	AD4
13	O	NRB	38	O	YO0	63	O	OSCO	88	I	AD3
14	—	GND	39	—	VDD	64	—	VDD	89	I	AD2
15	I	VBC	40	I	YI7	65	I	PBV	90	I	AD1
16	I	VREFC	41	I	YI6	66	I	PBSYNC	91	I	AD0
17	—	AGND	42	I	YI5	67	I	SCK	92	I	DEF
18	O	IREFC	43	I	YI4	68	I	SI	93	I/O	DOP/DISP
19	I	VGC	44	I	YI3	69	I	XCE	94	—	VDD
20	O	IOC	45	I	YI2	70	O	SO	95	O	AFY7
21	—	AVDD	46	I	YI1	71	I	XCLR	96	O	AFY6
22	—	AVDD	47	I	YI0	72	—	GND	97	O	AFY5
23	O	IOY	48	—	GND	73	I	XV1	98	O	AFY4
24	I	VGY	49	O	FSCO	74	I	XSG1	99	O	AFY3
25	O	IREFY	50	O	HD	75	I	CLK	100	O	AFY2

INPUT

AD0 - AD8 : DIGITAL SIGNAL DATA
 CI0 - CI3 : CHROMA DIGITAL INTERFACE
 CLK : CLOCK (NTSC : 910fH, PAL : 908fH)
 DEF : DEFECT CORRECTION PULSE
 ID : LINE COLOR DISCRIMINATION
 MCK : MASTER CLOCK
 OSCI : 4fsc OSCILLATOR
 PBLK : PRE-BLANKING PULSE
 PBSYNC : PLAY BACK SYNC/EXTERNAL SYNC HD
 PBV : LATCH PULSE
 SCK : SERIAL CLOCK
 SELVLP : LATCH PULSE SELECT (H : AVD, L : PBV)
 SI : SERIAL DATA
 TEST : TEST (H : TEST, L : NORMAL)
 VBC, VGC : CHROMA DAC CAPACITOR
 VGY, VBY : Y DAC CAPACITOR
 VREFC : CHROMA DAC REFERENCE VOLTAGE
 VREFY : Y DAC REFERENCE VOLTAGE
 VRI : RESET
 XCE : CHIP SELECT
 XCLR : CLEAR
 XSG1 : SENSOR CHARGE READ OUT PULSE
 XV1 : VERTICAL REGISTER DRIVE CLOCK
 YI0 - YI7 : Y DIGITAL INTERFACE

OUTPUT

AFY0 - AFY7 : AF DETECT
 AHD : TG HORIZONTAL DIRECTION SYNC SIGNAL
 AVD : TG VERTICAL DIRECTION SYNC SIGNAL
 CBLK : COMPOSITE BLANKING
 CLP1 : OPB CLAMP PULSE
 CO0 - CO3 : CHROMA DIGITAL INTERFACE
 DICK : DIGITAL INTERFACE CLOCK
 FLD : FIELD DISCRIMINATION
 FSCO : SUB CARRIER
 HD : HORIZONTAL DIRECTION SYNC SIGNAL
 HCOMP : PLL PHASE COMPARATOR
 IOC : CHROMA SIGNAL
 IOY : Y SIGNAL
 IREFC : CHROMA DAC REFERENCE CURRENT
 IREFY : Y DAC REFERENCE CURRENT
 NRB : DIGITAL INTERFACE COLOR DISCRIMINATION
 OSCO : 4fsc OSCILLATOR
 SO : SERIAL DATA
 SYNC : COMPOSITE SYNC
 VD : VERTICAL DIRECTION SYNC SIGNAL
 YO0 - YO7 : Y DIGITAL INTERFACE

INPUT/OUTPUT

DOP/DISP : DROP OUT DETECT/OPD DETECT

