

# **LA5311M**

# Variable Divided Voltage Generator for LCD

#### Overview

The LA5311M is a variable divided voltage generator IC for multiple drive of LCD matrix.

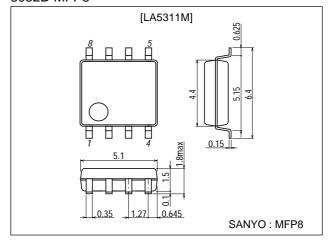
### **Features**

- Power supply for variable bias LCD drive.
- 4 operational amplifiers deliver 4 voltage outputs.
- Low current drain (1.0mA max).
- Miniflat package.

## **Package Dimensions**

unit:mm

#### 3032B-MFP8



# **Specifications**

#### **Absolute Maximum Ratings** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max		30	V
Output current	IOUT		5	mA
Allowable power dissipation	Pd max		300	mW
Operating temperature	Topr		-20 to +75	°C
Storage temperature	Tstg		-40 to +125	°C

#### **Operating Conditions** at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage range	V <sub>CC</sub> op	V <sub>CC</sub> -V <sub>1</sub> >1.0V	11 to 28	V
Recommended output current	I <sub>1</sub>		0 to 3	mA
	l <sub>2</sub> , l <sub>3</sub>		-3 to +3	mA
	14		-3 to 0	mA

#### Operating Characteristics at Ta = 25°C, $V_{CC}=20$ V

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Onit
Current drain	Icc				1.0	mA
Output ratio1	R1	$V2/V1$ , $V_{CC}$ =0, GND= $-20V$ = $V_5$ External R <sub>A</sub> = $100$ k $Ω$	1.94		2.06	
Output ratio2	R2	$V5$ – $V3$ / $V5$ – $V4$ , $V_{CC}$ =0, GND=–20 $V$ = $V_5$ External R <sub>A</sub> =100kΩ	1.94		2.06	

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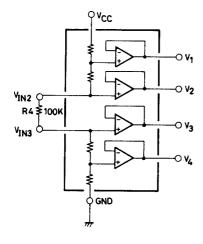
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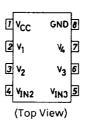
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Output ratio3	R3	$V2/V5-V3$ , $V_{CC}=0$ , $GND=-20V=V_5$ External $R_A=100k\Omega$	0.97		1.03	
Output ratio4	R4	V1/V5–V4, $V_{CC}$ =0, GND=–20V= $V_5$ External $R_A$ =100k $\Omega$	0.97		1.03	
Load regulation	ΔV1	+100μA <i<sub>OUT&lt;+3mA</i<sub>			20	mV
	ΔV2	+100μA <i<sub>OUT&lt;+3mA</i<sub>			20	mV
	ΔV3	+100μA <i<sub>OUT&lt;+3mA</i<sub>			20	mV
	–ΔV2	-3mA <i<sub>OUT&lt;-100μA</i<sub>			20	mV
	<b>–</b> ΔV3	-3mA <i<sub>OUT&lt;-100μA</i<sub>			20	mV
	–∆V4	-3mA <i<sub>OUT&lt;-100μA</i<sub>	_		20	mV
R <sub>1</sub> +R <sub>2</sub>	R	0.5V applied across R <sub>1</sub> +R <sub>2</sub>	33	40	47	kΩ

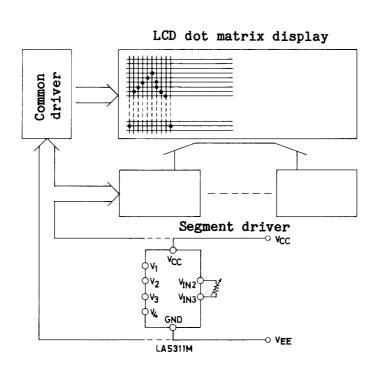
# **Equivalent Circuit**



# **Pin Assingment**



**Sample Application Circuit** 



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