MAXIM

Low-Voltage, SOT23 µP Supervisors with Power Fail In/Out, Manual Reset, and Watchdog Timer

MAX6701–MAX6708

General Description

The MAX6701–MAX6708 microprocessor (µP) supervisory circuits reduce the complexity and components required to monitor power-supply functions in µP systems. These devices significantly improve system reliability and accuracy compared to separate ICs or discrete components.

The MAX6701–MAX6708 family provides four functions: a reset output during power-up, power-down and brownout conditions; an independent watchdog output that goes low if the watchdog input has not been toggled within 1.6s; a 0.62V threshold detector for power-fail warning; and an active-low manual reset input.

The MAX6701–MAX6708 family offers several pinout options to accommodate a variety of multivoltage microprocessor supervision applications.

The MAX6701/MAX6702/MAX6703 monitor three supply voltages (one fixed threshold and two adjustable) to drive a single reset output and include a manual reset input and a watchdog timer with an independent output. The MAX6704 monitors a single supply voltage to drive complementary reset outputs and includes an independent adjustable power fail in/power fail out comparator, a manual reset input, and a reset based watchdog timer. The MAX6705/MAX6706/MAX6707 monitor a single supply voltage to drive a single reset output and include an independent adjustable power fail in/power fail out comparator, a manual reset input, and a watchdog timer with an independent output. The MAX6708 is the same as the MAX6704 but without the watchdog timer function.

Applications

Computers

Controllers

Intelligent Instruments

Automotive Systems

Critical µP Power Monitoring

White Goods

Networking

Telecommunications

Typical Operating Circuit appears at end of data sheet.

Features

♦ **Small 8-Pin SOT23 Package**

♦ **Precision Monitoring of +5.0V, +3.3V, +3.0V, +2.5V Supply Voltages**

♦ **140ms Reset Timeout Delay**

♦ **Power Fail Input with Independent Output; Monitors Inputs Down to 0.62V (MAX6704–MAX6708)**

♦ **Dual Adjustable Reset Input for Triple Voltage Monitoring (MAX6701/MAX6702/MAX6703)**

♦ **1.6s Watchdog Timeout Period (MAX6701–MAX6707)**

♦ **Independent Watchdog Output (MAX6701/MAX6702/MAX6703/MAX6705/ MAX6706/MAX6707)**

♦ **Manual Reset Input**

♦ **Four Reset Output Stage Options**
   Active-Low Push-Pull (MAX6701, MAX6705)
   Active-Low Open-Drain (MAX6703, MAX6707)
   Active-High Push-Pull (MAX6702, MAX6706)
   Dual Active-Low/High Push-Pull (MAX6704, MAX6708)

♦ **Guaranteed Reset Valid to V_{CC} = 1V**

♦ **Immune to Short Negative V_{CC} Transients**
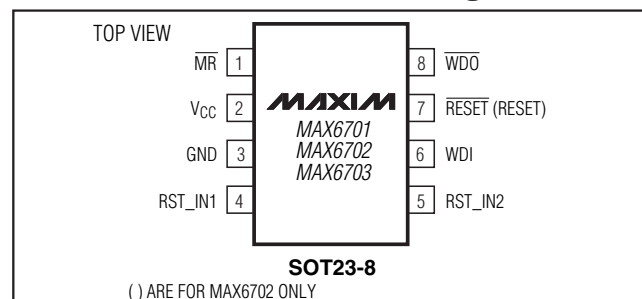
♦ **Low Cost, Few External Components**

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX6701_KA-T	-40°C to +125°C	SOT23-8
MAX6702_ KA-T	-40°C to +125°C	SOT23-8
MAX6703_ KA-T	-40°C to +125°C	SOT23-8

Insert the desired suffix letter (from the Threshold Suffix Guide table) into the blank to complete the part number. All devices must be ordered in increments of 2500 pieces. Sample stock is typically held on standard versions only. Contact factory for availability.

Pin Configurations

TOP VIEW

$\overline{MR}$	1	8 $\overline{WDO}$
V_{CC}	2	7 $\overline{RESET}$ (RESET)
GND	3	6 WDI
RST_IN1	4	5 RST_IN2

MAX6701 MAX6702 MAX6703

SOT23-8

( ) ARE FOR MAX6702 ONLY

For price, delivery, and to place orders, please contact Maxim Distribution at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

Low-Voltage, SOT23 µP Supervisors with Power Fail In/Out, Manual Reset, and Watchdog Timer

ABSOLUTE MAXIMUM RATINGS

V_CC ....-0.3V to +6.0V
Open-Drain $\overline{RESET}$, $\overline{WDO}$, $\overline{PFO}$ ....-0.3V to +6.0V
Push-Pull RESET, $\overline{RESET}$, $\overline{WDO}$, $\overline{PFO}$ ....-0.3V to (V_CC + 0.3V)
$\overline{MR}$, WDI, PFI, RST_IN1, RST_IN2 ....-0.3V to (V_CC + 0.3V)
Input Current (V_CC) 20mA
Output Current ($\overline{RESET}$, RESET, $\overline{PFO}$, $\overline{WDO}$) 20mA

Continuous Power Dissipation (T_A = +70°C)
    8-Pin SOT23 (derate 8.9mW/°C above +70°C) 714mW
Operating Temperature Range ....-40°C to +125°C
Junction Temperature ....+150°C
Storage Temperature Range ....-65°C to +150°C
Lead Temperature (soldering, 10s) ....+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V_CC = +4.25V to +5.5V for L/M versions, V_CC = +2.55V to +3.6V for the T/S/R versions, V_CC = +2.1V to +2.75V for the Z/Y versions. T_A = -40°C to +125°C, unless otherwise specified. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Operating Voltage Range	V_CC	T_A = 0°C to +125°C		1.0		5.5	V
		T_A = -40°C to +125°C		1.2		5.5	
Supply Current $\overline{MR}$ Unconnected	I_CC	V_CC < 5.5V, no load			12	25	µA
		V_CC < 3.6V, no load			9	20	
		V_CC < 3.6V, no load (MAX6708 only)			6	20	
V_CC Reset Threshold (V_CC falling)	V_TH	MAX670_L	T_A = -40°C to +85°C	4.50	4.63	4.75	V
			T_A = -40°C to +125°C	4.47		4.78	
		MAX670_M	T_A = -40°C to +85°C	4.25	4.38	4.50	
			T_A = -40°C to +125°C	4.22		4.53	
		MAX670_T	T_A = -40°C to +85°C	3.00	3.08	3.15	
			T_A = -40°C to +125°C	2.97		3.17	
		MAX670_S	T_A = -40°C to +85°C	2.85	2.93	3.00	
			T_A = -40°C to +125°C	2.83		3.02	
		MAX670_R	T_A = -40°C to +85°C	2.55	2.63	2.70	
			T_A = -40°C to +125°C	2.53		2.72	
		MAX670_Z	T_A = -40°C to +85°C	2.25	2.32	2.38	
			T_A = -40°C to +125°C	2.24		2.40	
		MAX670_Y	T_A = -40°C to +85°C	2.12	2.19	2.25	
			T_A = -40°C to +125°C	2.11		2.27	
Reset Threshold Temperature Coefficient	ΔV_TH				60		ppm/°C
V_CC to Reset Output Delay		V_CC falling at 10mV/µs			12		µs
Reset Timeout Period	t_RP	T_A = -40°C to +85°C		140	200	280	ms
		T_A = -40°C to +125°C		120		300	
PFI, RST_IN1, RST_IN2 Threshold		V_CC = 1.8V to 5.5V	T_A = -40°C to +85°C	602	618	634	mV
			T_A = -40°C to +125°C	593		642	
PFI, RST_IN1, RST_IN2 Hysteresis					6		mV

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ELECTRICAL CHARACTERISTICS (continued)

(V_{CC} = +4.25V to +5.5V for L/M versions, V_{CC} = +2.55V to +3.6V for the T/S/R versions, V_{CC} = +2.1V to +2.75V for the Z/Y versions. T_A = -40°C to +125°C, unless otherwise specified. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
PFI, RST_IN1, RST_IN2 Leakage Current		(Note 2)	T_A = -40°C to +85°C	-50		+50	nA
			T_A = -40°C to +125°C	-200		+200	
PFI to $\overline{PFO}$ Delay	t_{PF}				1		µs
MR Input Voltage	V_{IL}					0.3 x V_{CC}	V
	V_{IH}			0.7 x V_{CC}			
$\overline{MR}$ Minimum Input Pulse				1			µs
$\overline{MR}$ Glitch Rejection					100		ns
$\overline{MR}$ to Reset Delay	t_{MD}				200		ns
$\overline{MR}$ Pullup Resistance				25	50	75	kΩ
Watchdog Timeout Period	t_{WD}	T_A = -40°C to +85°C		1.12	1.6	2.4	s
		T_A = -40°C to +125°C		0.96		2.52	
WDI Pulse Width	t_{WDI}	(Note 2)		50			ns
WDI Input Voltage	V_{IL}					0.3 x V_{CC}	V
	V_{IH}			0.7 x V_{CC}			
WDI Input Current	I_{WDI}	WDI = 0 or V_{CC}		-1		+1	µA
$\overline{RESET}$, $\overline{WDO}$ Output Low (Push-Pull or Open-Drain)	V_{OL}	$V_{CC} \geq$ 1.0V, I_{SINK} = 50µA, output asserted (T_A = 0°C to +125°C)				0.3	V
		$V_{CC} \geq$ 1.2V, I_{SINK} = 100µA, output asserted				0.3	
		$V_{CC} \geq$ 2.55V, I_{SINK} = 1.2mA, output asserted				0.3	
		$V_{CC} \geq$ 4.25V, I_{SINK} = 3.2mA, output asserted				0.4	
PFO Output Low (Push-Pull or Open-Drain)	V_{OL}	$V_{CC} \geq$ 1.80V, I_{SINK} = 200µA, output asserted				0.3	V
		$V_{CC} \geq$ 2.55V, I_{SINK} = 1.2mA, output asserted				0.3	
		$V_{CC} \geq$ 4.25V, I_{SINK} = 3.2mA, output asserted				0.4	
$\overline{RESET}$, $\overline{WDO}$, PFO Output High (Push-Pull Only)	V_{OH}	$V_{CC} \geq$ 2.7V, I_{SINK} = 500µA, output not asserted		0.8 x V_{CC}			V
		$V_{CC} \geq$ 4.75V, I_{SINK} = 800µA, output not asserted		0.8 x V_{CC}			
$\overline{RESET}$, $\overline{WDO}$, $\overline{PFO}$ Output Open-Drain Leakage Current	I_{LKG}	$V_{CC} > V_{TH}$, output not asserted				1.0	µA
RESET Output High (Push-Pull Only)	V_{OH}	$V_{CC} \geq$ 1.0V, I_{SOURCE} = 1µA, reset asserted (T_A = 0°C to +125°C)		0.8 x V_{CC}			V
		$V_{CC} \geq$ 1.2V, I_{SOURCE} = 50µA, reset asserted		0.8 x V_{CC}			
		$V_{CC} \geq$ 2.55V, I_{SOURCE} = 500µA, reset asserted		0.8 x V_{CC}			
		$V_{CC} \geq$ 4.25V, I_{SOURCE} = 800µA, reset asserted		0.8 x V_{CC}			

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MAX6701–MAX6708

ELECTRICAL CHARACTERISTICS (continued)

(V_{CC} = +4.25V to +5.5V for L/M versions, V_{CC} = +2.55V to +3.6V for the T/S/R versions, V_{CC} = +2.1V to +2.75V for the Z/Y versions. T_A = -40°C to +125°C, unless otherwise specified. Typical values are at T_A = +25°C.) (Note 1)
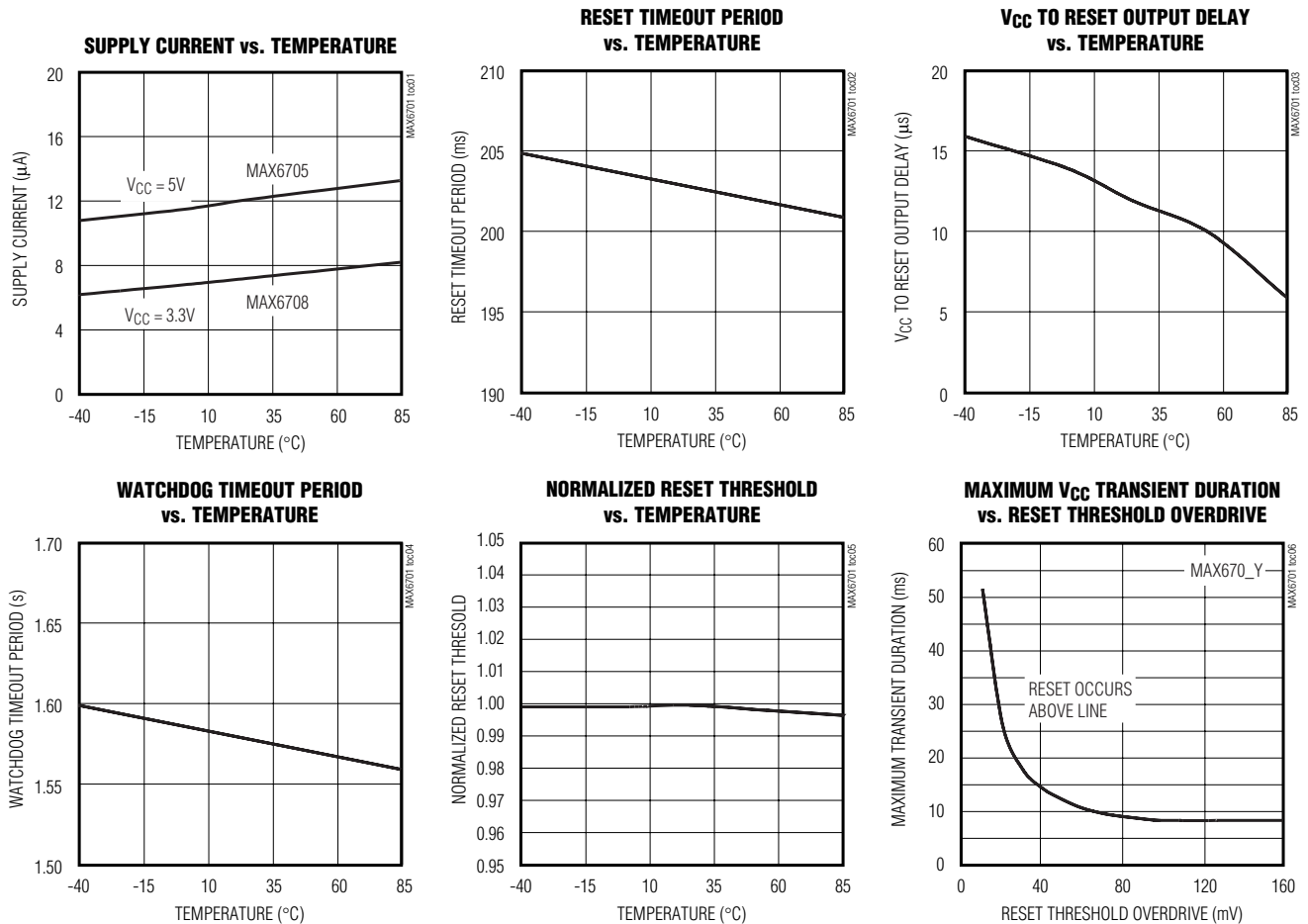
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
RESET Output Low (Push-Pull Only)	V_{OL}	$V_{CC} \geq 2.7V$, I_{SINK} = 1.2mA, reset not asserted			0.3	V
		$V_{CC} \geq 4.75V$, I_{SINK} = 3.2mA, reset not asserted			0.4	

Note 1: Over temperature limits are guaranteed by design and not production tested. Devices are tested at T_A = +25°C.
Note 2: Guaranteed by design. Not production tested.

Typical Operating Characteristics

(T_A = +25°C, unless otherwise noted.)

SUPPLY CURRENT vs. TEMPERATURE

RESET TIMEOUT PERIOD vs. TEMPERATURE

V$_{CC}$ TO RESET OUTPUT DELAY vs. TEMPERATURE

WATCHDOG TIMEOUT PERIOD vs. TEMPERATURE

NORMALIZED RESET THRESHOLD vs. TEMPERATURE

MAXIMUM V$_{CC}$ TRANSIENT DURATION vs. RESET THRESHOLD OVERDRIVE

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_____*Typical Operating Characteristics (continued)*

(T_A = +25°C, unless otherwise noted.)

OUTPUT VOLTAGE LOW vs. SINK CURRENT

$V_{CC} \geq 4.25V$

V_{CC} - V_{OH} vs. SOURCE CURRENT

$V_{CC} \geq 4.25V$
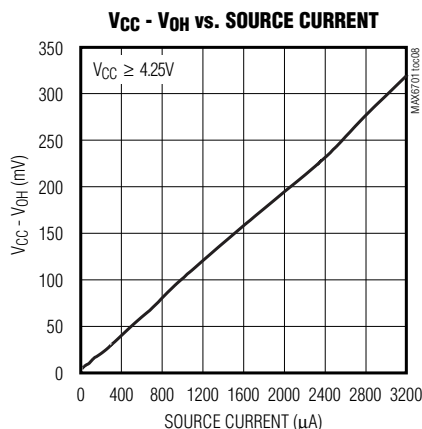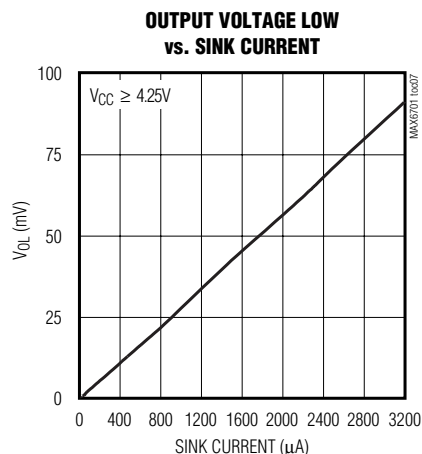
_____*Pin Description*

MAX6701 MAX6702 MAX6703	MAX6704	MAX6705 MAX6706 MAX6707	MAX6708	NAME	FUNCTION
1	1	1	1	$\overline{MR}$	Active-Low, Manual Reset Input, Internal 50kΩ Pullup to V_{CC}. Pull low to force a reset. Reset remains active as long as $\overline{MR}$ is low and for the reset timeout period after $\overline{MR}$ goes high. Leave unconnected or connect to V_{CC} if unused.
2	2	2	2	V_{CC}	Supply Voltage for MAX6701–MAX6708 and Input for Primary Reset Threshold Monitor. Push-pull outputs are powered by V_{CC}.
3	3	3	3	GND	Ground
—	4	4	4	PFI	Power Fail Voltage Monitor Input. High-impedance input for internal power fail comparator. Connect this pin to an external resistive divider network to set the reset threshold voltage; 0.62V (typ) threshold. Connect to GND or V_{CC} when not used.
—	5	5	5	$\overline{PFO}$	Power Fail Monitor Output. Open-drain or push-pull active-low. $\overline{PFO}$ goes low when PFI is less than 0.62V.
6	6	6	—	WDI	Watchdog Input. If WDI remains high or low for longer than the watchdog timeout period, the internal watchdog timer runs out and $\overline{WDO}$ is asserted on the MAX6704 (resets are asserted for the reset timeout period). The internal watchdog timer clears whenever reset is asserted, or WDI sees a rising or falling edge. The watchdog function cannot be disabled.
—	—	—	6	N.C.	No Connection

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MAX6701–MAX6708

MAX6701 MAX6702 MAX6703	MAX6704	MAX6705 MAX6706 MAX6707	MAX6708	NAME	FUNCTION
7	7	7	7	$\overline{RESET}$	Active-Low Reset Output (Open-Drain or Push-Pull). $\overline{RESET}$ changes from high to low when the V_{CC} input drops below the selected reset threshold (or RST_IN1/RST_IN2 for the MAX6701/MAX6702/MAX6703), $\overline{MR}$ is pulled low, or the watchdog triggers a reset (MAX6704 only). $\overline{RESET}$ remains low for the reset timeout period after the reset conditions are terminated.
8	—	8	—	$\overline{WDO}$	Active-Low Watchdog Output (Open-Drain or Push-Pull). $\overline{WDO}$ is asserted whenever the watchdog times out and does not deassert until the watchdog is cleared. $\overline{WDO}$ also asserts when V_{CC} or RST_IN_ are below reset thresholds or $\overline{MR}$ is pulled low. $\overline{WDO}$ deasserts after a valid WDI transition (without a reset timeout period).
7*	8	7*	8	RESET	Active-High Reset Output (Push-Pull). RESET changes from low to high when the V_{CC} input drops below the selected reset threshold (or RST_IN1/RST_IN2 for MAX6701/MAX6702/MAX6703), $\overline{MR}$ is pulled low, or the watchdog triggers a reset (MAX6704 only). RESET remains high for the reset timeout period after the reset conditions are terminated. *RESET active-high for the MAX6702/MAX6706.
4	—	—	—	RST_IN1	Input for User Adjustable V_{CC2} Monitor. High-impedance input for second internal reset comparator. Connect this pin to an external resistive-divider network to set the reset threshold voltage; 0.62V (typ) threshold. Connect to V_{CC} when not used. Reset is asserted when either V_{CC}, RST_IN1, or RST_IN2 are below threshold.
5	—	—	—	RST_IN2	Input for User Adjustable V_{CC3} Monitor. High-impedance input for third internal reset comparator. Connect this pin to an external resistive-divider network to set the reset threshold voltage; 0.62V (typ) threshold. Connect to V_{CC} when not used. Reset is asserted when either V_{CC}, RST_IN1, or RST_IN2 are below threshold.

_____*Detailed Description*

Reset Output

A microprocessor's (µP's) reset input starts the µP in a known state. The MAX6701–MAX6708 assert reset during power-up and prevent code execution errors during power-down or brownout conditions.

On power-up, once V_{CC} reaches 1V, $\overline{RESET}$ is a guaranteed logic low of 0.4V or less. As V_{CC} rises, $\overline{RESET}$ stays low. After V_{CC} rises above the reset threshold, an internal timer holds $\overline{RESET}$ low for about 200ms. $\overline{RESET}$ pulses low whenever V_{CC} dips below the reset threshold, including brownout conditions. If a brownout occurs in the middle of a previously initiated reset pulse, the pulse continues for at least another 140ms. On power-down, once V_{CC} falls below the reset threshold, $\overline{RESET}$ stays low and is guaranteed to be 0.4V or less, until V_{CC} drops below 1V.
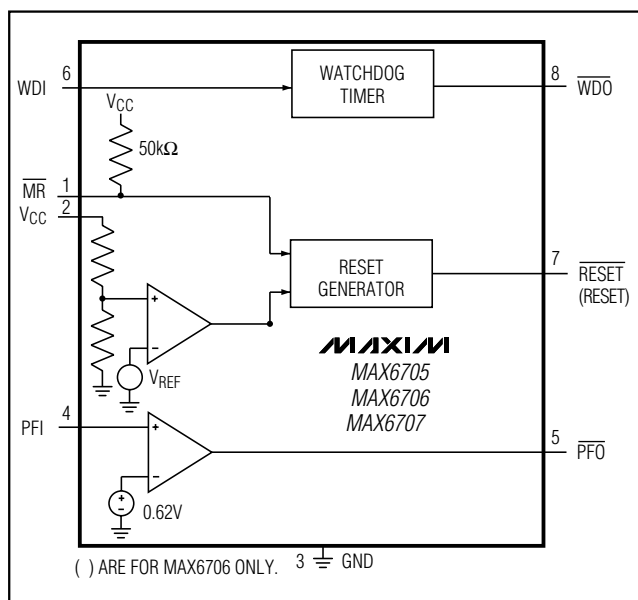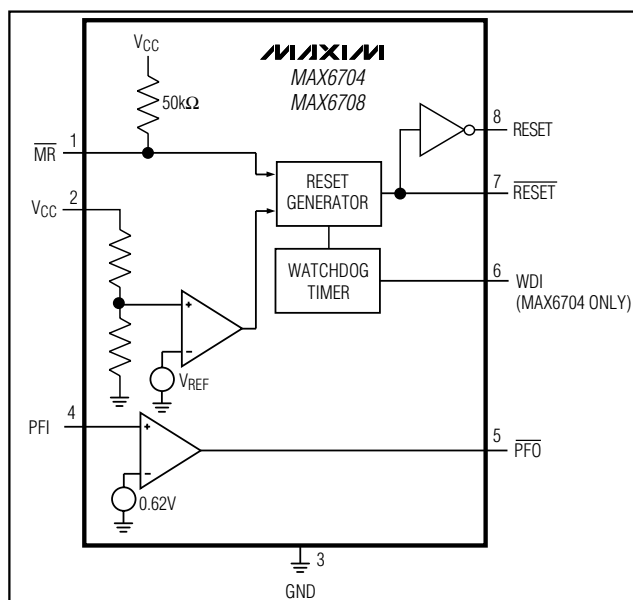
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Figure 1. MAX6705/MAX6706/MAX6707 Block Diagram

Figure 2. MAX6704/MAX6708 Block Diagram

The MAX6702/MAX6704/MAX6706/MAX6708 active-high RESET output is simply the complement of the RESET output, and is guaranteed to be valid with V$_{CC}$ down to 1V. (See Figure 4.)

Watchdog Timer

The MAX6701–MAX6707 watchdog circuit monitors the µP's activity. If the µP does not toggle the watchdog input (WDI) within 1.6s, WDO goes low. When RESET is asserted, the watchdog timer stays cleared and does not count. As soon as reset is released, the timer will start counting. WDO deasserts after a valid transition is detected at WDI. Pulses as short as 50ns can be detected.

Typically, WDO will be connected to the nonmaskable interrupt (NMI) input of a µP. When V$_{CC}$ drops below the reset threshold, WDO will go low whether or not the watchdog timer has timed out yet. Normally this would trigger an NMI, but RESET goes low simultaneously, and thus overrides the NMI.

The MAX6704 watchdog circuit does not have an independent watchdog output (WDO). If the µP does not toggle the watchdog input within 1.6s, the MAX6704 will assert a reset output pulse for the reset timeout period. (See Figure 5.)

Manual Reset

The manual reset input (MR) allows reset to be triggered by a pushbutton switch. The switch is effectively debounced by the reset pulse width. MR is CMOS logic compatible, so it can be driven by an external logic line. MR can be used to force a watchdog timeout to generate a reset pulse in the MAX6701–MAX6708. Simply connect WDO to MR.

Power-Fail Comparator

The uncommitted power-fail comparator can be used for various purposes because its noninverting input and output are externally available. The inverting input is internally connected to a 0.62V reference. To build an early warning circuit for power failure, connect the PFI pin to a voltage divider (see *Typical Operating Circuit*). Choose the voltage-divider ratio so that the voltage at PFI falls below 0.62V just before the regulator drops out. Use PFO to interrupt the µP so it can prepare for an orderly power-down. The low input current at this pin allows for large resistor values in the divider.

Reset-In Information

The MAX6701/MAX6702/MAX6703 include two adjustable reset inputs for monitoring up to a total of three system voltages (including V$_{CC}$). The thresholds for the monitored RST_IN supplies are externally set with resistor-divider networks (Figure 6). The reset output is asserted if any of the monitored supplies (V$_{CC}$, RST_IN1, or RST_IN2) go below its specified threshold and remain asserted for the reset timeout period after all supplies are above their thresholds.
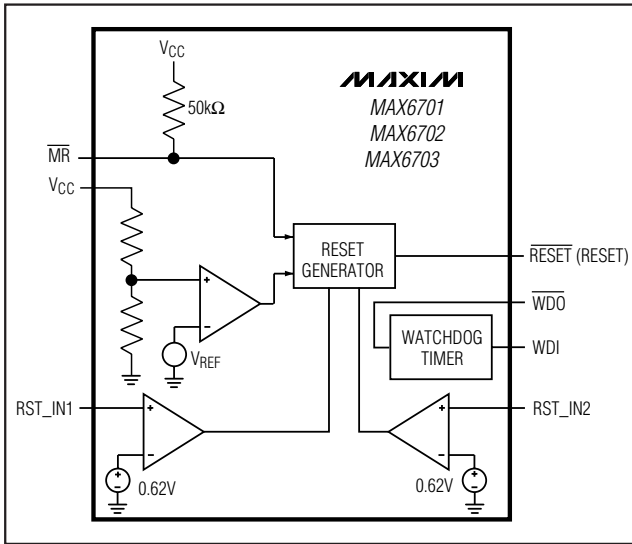
MAX6701–MAX6708

Figure 3. MAX6701/MAX6702/MAX6703 Block Diagram

_____Applications Information

Ensuring a Valid $\overline{RESET}$ Output Down to $V_{CC} = 0$

When V_{CC} falls below 1V, the MAX6701–MAX6708 $\overline{RESET}$ output no longer sinks current—it becomes an open circuit. High-impedance CMOS logic inputs can drift to undetermined voltages if left undriven. If a pull-down resistor is added to the $\overline{RESET}$ pin as shown in Figure 7, any stray charge or leakage currents will be drained to ground, holding $\overline{RESET}$ low. A resistor value (R1) is not critical; 100kΩ is large enough not to load $\overline{RESET}$ and small enough to pull $\overline{RESET}$ to ground. This application works for push-pull output only (not for open-drain resets).

Monitoring Other System Voltages

Other systems can be monitored by connecting a voltage divider to PFI and adjusting the ratio appropriately. In noisy systems, a capacitor between PFI and GND will reduce the power-fail circuit's sensitivity to high-frequency noise on the line being monitored. Reset can be asserted on other voltages in addition to the V_{CC} supply line. Connect $\overline{PFO}$ to $\overline{MR}$ to initiate a reset output pulse when PFI drops below 0.62V. Figure 8 shows the MAX6701–MAX6708 configured to assert a reset output when the secondary supply falls below the reset threshold.
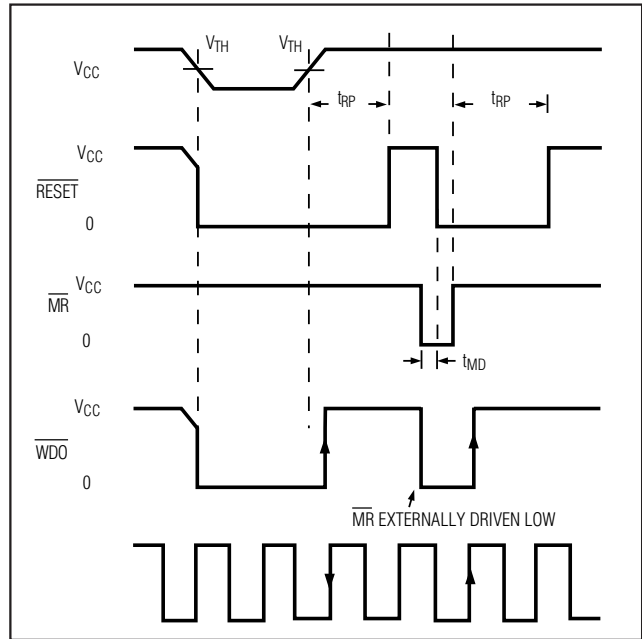

Figure 4. MAX6705/MAX6706/MAX6707 $\overline{RESET}$, $\overline{MR}$, $\overline{WDO}$, and WDI Timing. The MAX6706 RESET output is the inverse of the $\overline{RESET}$ shown.
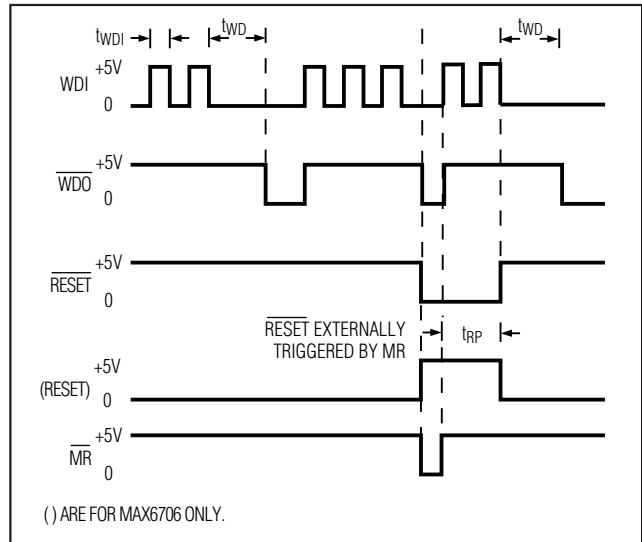
Figure 5. MAX6705/MAX6706/MAX6707 Watchdog Timing

/MAXIM/

$$V_{EXT\text{-}TH} = \left(1 + \frac{R1}{R2}\right) \times 0.62V \qquad R1 = R2 \times \left(\frac{V_{EXT\text{-}TH}}{0.62} - 1\right)$$

WHERE $V_{EXT\text{-}TH}$ IS THE EXTERNAL VOLTAGE TRIP LEVEL.
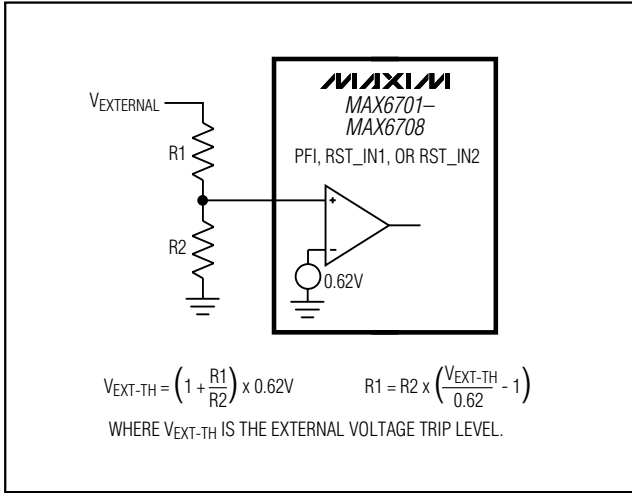
Figure 6. Calculating Adjustable Voltage Thresholds
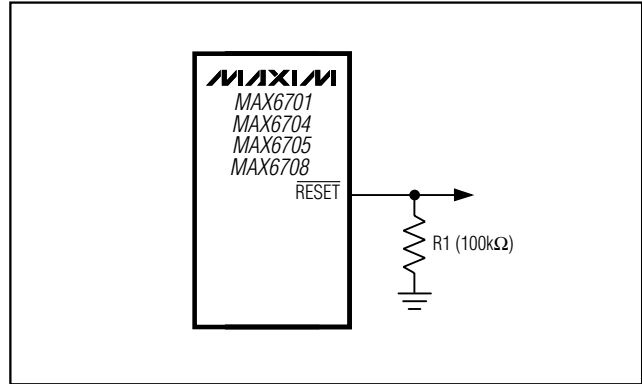
Figure 7. $\overline{RESET}$ Valid to Ground Circuit

() ARE FOR MAX6704/MAX6706/MAX6708 ONLY
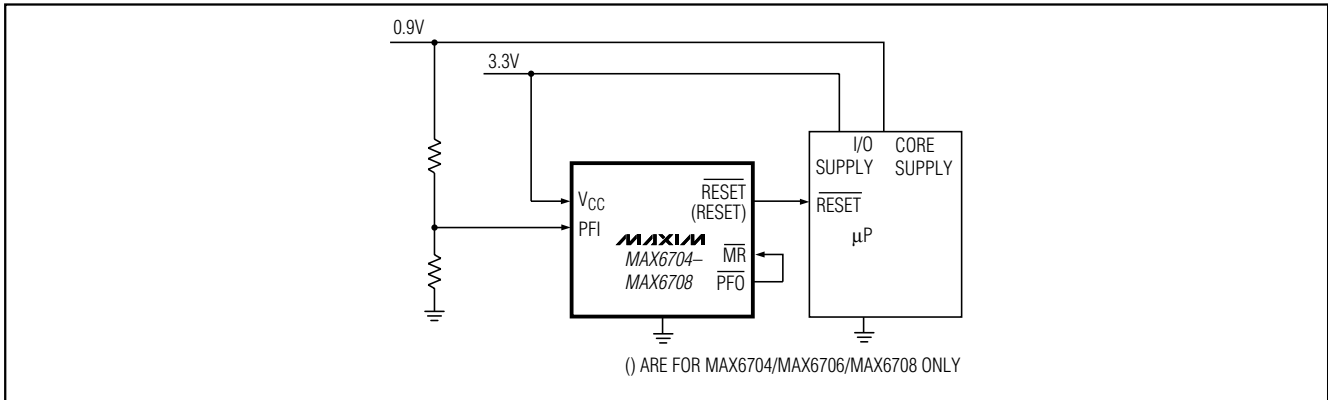
Figure 8. Monitoring Other System Voltages

_____Threshold Suffix Guide

SUFFIX	RESET THRESHOLD (V)
L	4.63
M	4.38
T	3.08
S	2.93
R	2.63
Z	2.32
Y	2.19

Bold indicates standard version.

__Ordering Information (continued)

PART	TEMP. RANGE	PIN-PACKAGE
MAX6704_KA-T	-40°C to +125°C	SOT23-8
MAX6705_KA-T	-40°C to +125°C	SOT23-8
MAX6706_KA-T	-40°C to +125°C	SOT23-8
MAX6707_KA-T	-40°C to +125°C	SOT23-8
MAX6708_KA-T	-40°C to +125°C	SOT23-8

Low-Voltage, SOT23 µP Supervisors with Power Fail In/Out, Manual Reset, and Watchdog Timer

Pin Configurations (continued)

SOT23-8

SOT23-8

( ) ARE FOR MAX6706 ONLY

SOT23-8
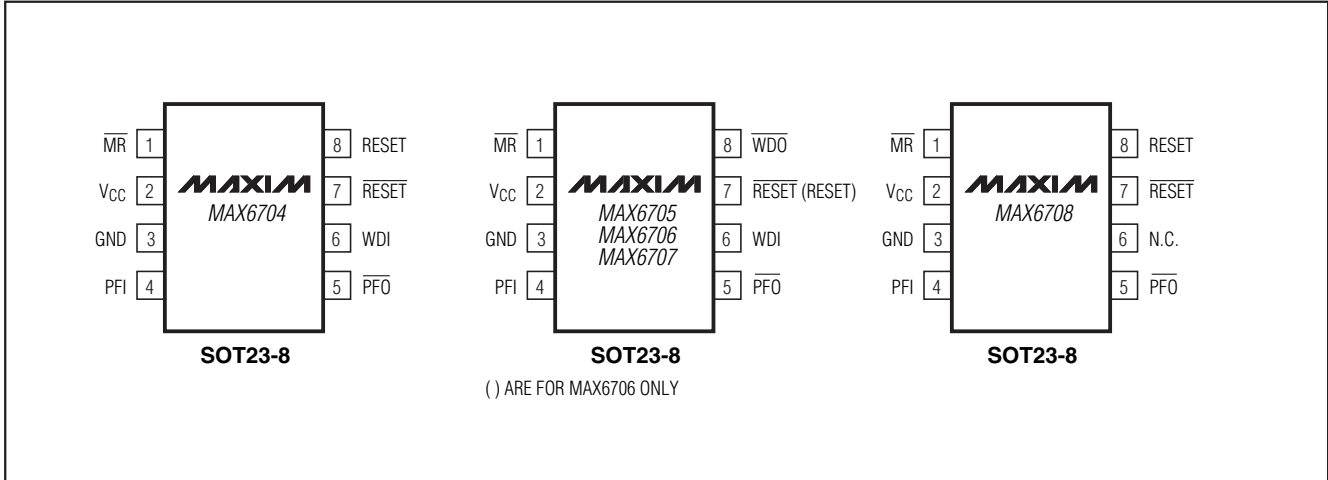
Selector Guide

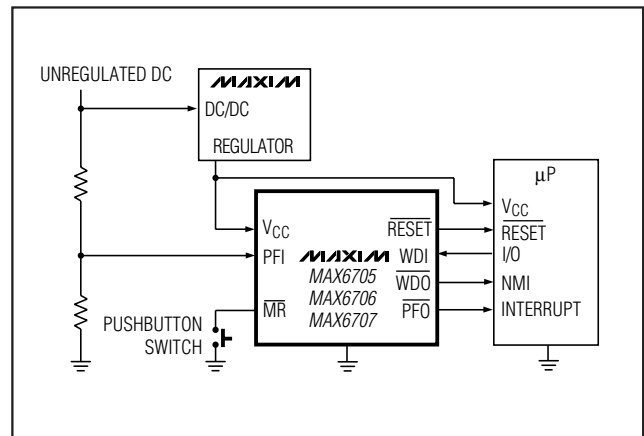| PART	$\overline{RESET}$ PP-LOW	RESET PP-HIGH	$\overline{RESET}$ OD-LOW	WDI	$\overline{WDO}$	PFI, $\overline{PFO}$	RST_IN1, RST_IN2
MAX6701	✔			✔	✔ PP		✔
MAX6702		✔		✔	✔ PP		✔
MAX6703			✔	✔	✔ OD		✔
MAX6704	✔	✔		✔		✔ PP	
MAX6705	✔			✔	✔ PP	✔ PP	
MAX6706		✔		✔	✔ PP	✔ PP	
MAX6707			✔	✔	✔ OD	✔ OD	
MAX6708	✔	✔				✔ PP	

PP = Push-Pull OD = Open-Drain

Chip Information

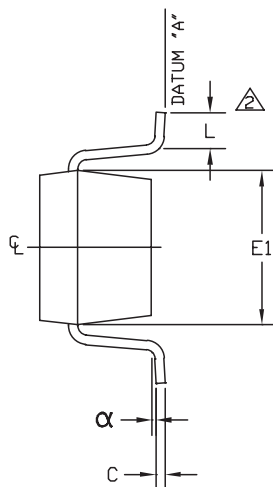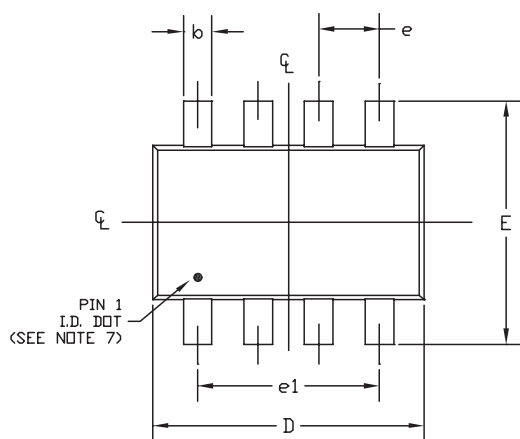TRANSISTOR COUNT: 716
PROCESS: BiCMOS

Typical Operating Circuit

МАХІМ

Package Information

SYMBOL	MIN	MAX
A	0.90	1.45
A1	0.00	0.15
A2	0.90	1.30
b	0.28	0.45
C	0.09	0.20
D	2.80	3.00
E	2.60	3.00
E1	1.50	1.75
L	0.10	0.60
e	0.65 ref	
e1	1.95 ref	
α	0°	10°
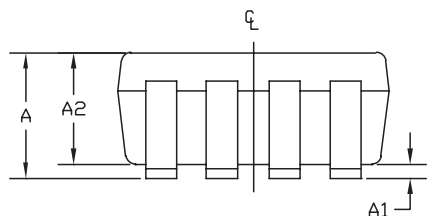
NOTE:
1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. FOOT LENGTH MEASURED REFERENCE TO FLAT FOOT SURFACE PARALLEL TO DATUM "A".
3. PACKAGE OUTLINE EXCLUSIVE OF MOLD FLASH & METAL BURR.
4. PACKAGE OUTLINE INCLUSIVE OF SOLDER PLATING.
5. EIAJ REF. NUMBER" SC-74 (6 LEAD VERSION)
6. COPLANARITY 4 MILS. MAX.
7. PIN 1 I.D. DOT IS 0.3 MM Ø MIN. LOCATED ABOVE PIN 1.

MAXIM
PROPRIETARY INFORMATION
TITLE:
PACKAGE OUTLINE, SOT 23, 8L

APPROVAL	DOCUMENT CONTROL NO.	REV	1/1
	21-0078	B	

SOT23, 8L.EPS

MAX6701–MAX6708

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