# **ESD** Protection Diodes

# In Ultra Small SOD–923 Package

The ESD9X Series is designed to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space is at a premium.

## **Specification Features:**

- Small Body Outline Dimensions: 0.039" x 0.024" (1.0 mm x 0.60 mm)
- Low Body Height: 0.017" (0.43 mm) Max
- Stand-off Voltage: 3.3 V 12 V
- Low Leakage
- Response Time is Typically < 1 ns
- ESD Rating of Class 3 (> 16 kV) per Human Body Model
- IEC61000-4-2 Level 4 ESD Protection
- These are Pb–Free Devices

#### **Mechanical Characteristics:**

CASE: Void-free, transfer-molded, thermosetting plastic Epoxy Meets UL 94 V-0 LEAD FINISH: 100% Matte Sn (Tin) **MOUNTING POSITION:** Any **QUALIFIED MAX REFLOW TEMPERATURE: 260°C** Device Meets MSL 1 Requirements

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
IEC 61000–4–2 (ESD) Contact		±30	kV
ESD Voltage Per Human Body Model Per Machine Model		16 400	kV V
Total Power Dissipation on FR–5 Board (Note 1) @ $T_A = 25^{\circ}C$	PD	150	mW
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	–55 to +150	°C
Lead Solder Temperature – Maximum (10 Second Duration)	ΤL	260	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1.  $FR-5 = 1.0 \times 0.75 \times 0.62$  in.

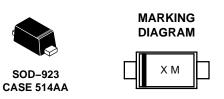


## **ON Semiconductor®**

http://onsemi.com



PIN 1. CATHODE 2. ANODE



<sup>=</sup> Specific Device Code = Date Code

## **ORDERING INFORMATION**

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Device	Package	Shipping <sup>†</sup>
ESD9XxxST5G	SOD-923	8000/Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

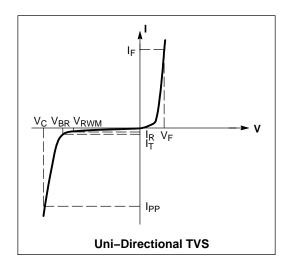
## **DEVICE MARKING INFORMATION**

See specific marking information in the device marking column of the table on page 2 of this data sheet.

### **ELECTRICAL CHARACTERISTICS**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

Symbol	Parameter					
I <sub>PP</sub>	Maximum Reverse Peak Pulse Current					
V <sub>C</sub>	Clamping Voltage @ IPP					
V <sub>RWM</sub>	V <sub>RWM</sub> Working Peak Reverse Voltage					
I <sub>R</sub>	Maximum Reverse Leakage Current @ V <sub>RWM</sub>					
V <sub>BR</sub>	Breakdown Voltage @ I <sub>T</sub>					
Ι <sub>Τ</sub>	Test Current					
١ <sub>F</sub>	Forward Current					
V <sub>F</sub>	Forward Voltage @ I <sub>F</sub>					
P <sub>pk</sub>	Peak Power Dissipation					
С	Max. Capacitance $@V_R = 0$ and f = 1 MHz					



#### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted, V<sub>F</sub> = 0.9 V Max. @ I<sub>F</sub> = 10 mA for all types)

	Device	V <sub>RWM</sub> (V)	I <sub>R</sub> (μΑ) @ V <sub>RWM</sub>	V <sub>BR</sub> (V) @ I <sub>T</sub> (Note 2)	ŀŗ	Max I <sub>PP</sub> (A) (Note 3)	V <sub>C</sub> (V) @ Max I <sub>PP</sub> (Note 3)	<b>P<sub>pk</sub> (W)</b> (8 x 20 μs)	C (pF)
Device*	Marking	Мах	Max	Min	mA		Max	Тур	Тур
ESD9X3.3ST5G	А	3.3	2.5	5.0	1.0	9.8	10.4	102	80
ESD9X5.0ST5G	В	5.0	1.0	6.2	1.0	8.7	12.3	107	65
ESD9X12ST5G	С	12	1.0	13.5	1.0	5.9	23.7	140	30

\*Other voltages available upon request.
2. V<sub>BR</sub> is measured with a pulse test current I<sub>T</sub> at an ambient temperature of 25°C.
3. Surge current waveform per Figure 3.

## **TYPICAL CHARACTERISTICS**

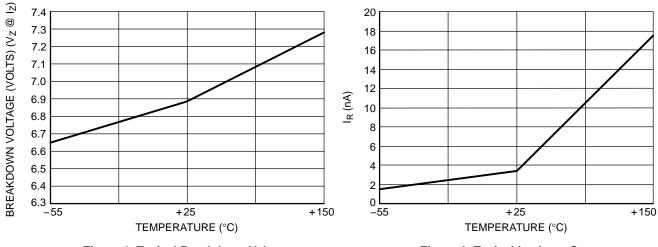




Figure 2. Typical Leakage Current versus Temperature

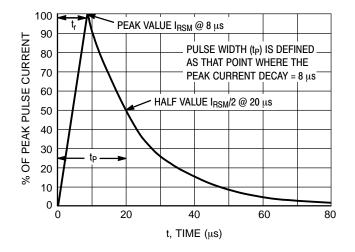
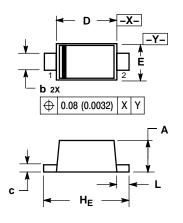


Figure 3. 8 X 20 µs Pulse Waveform

#### PACKAGE DIMENSIONS

SOD-923 CASE 514AA-01 ISSUE B



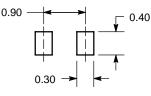
NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M. 1982.

2. CONTROLLING DIMENSION: MILLIMETERS.

 MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

	MIL	LIMETE	RS	INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.36	0.40	0.43	0.014	0.016	0.017	
b	0.15	0.20	0.25	0.006	0.008	0.010	
С	0.07	0.12	0.17	0.003	0.005	0.007	
D	0.75	0.80	0.85	0.030	0.031	0.033	
Е	0.55	0.60	0.65	0.022	0.024	0.026	
HE	0.95	1.00	1.05	0.037	0.039	0.041	
L	0.05	0.10	0.15	0.002	0.004	0.006	

#### SOLDERING FOOTPRINT\*



DIMENSIONS: MILLIMETERS

#### SOD-923

\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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