

GENERAL PURPOSE AMPLIFIER

**RF2334** 

Typical Applications

- Broadband, Low Noise Gain Blocks
- IF or RF Buffer Amplifiers
- Driver Stage for Power Amplifiers
- Final PA for Low Power Applications Broadband Test Equipment

### **Product Description**

The RF2334 is a general purpose, low-cost RF amplifier IC. The device is manufactured on an advanced Gallium Arsenide Heterojunction Bipolar Transistor (HBT) process, and has been designed for use as an easily-cascadable 50 $\Omega$  gain block. Applications include IF and RF amplification in wireless voice and data communication products operating in frequency bands up to 4000MHz. The device is self-contained with  $50\Omega$  input and output impedances and requires only two external DC biasing elements to operate as specified. The RF2334 is available in a very small industry-standard SOT23 5-lead surface mount package, enabling compact designs which conserve board space.



🗌 Si BJT	🗹 GaAs HBT	GaAs MESFET
Si Bi-CMOS	SiGe HBT	Si CMOS



Functional Block Diagram







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#### Package Style: SOT 5 Lead

#### Features

- DC to 4000 MHz Operation
- Internally matched Input and Output
- 16dB Small Signal Gain
- 5dB Noise Figure
- 50mW Linear Output Power
- Single Positive Power Supply

Ordering Information			
RF2334 RF2334 PCBA	General Purpose Amplifier Fully Assembled Evaluation	Board	
RF Micro Devices, Inc. 7625 Thorndike Road Greensboro, NC 27409, USA		Tel (336) 664 1233 Fax (336) 664 0454 http://www.rfmd.com	

#### **Absolute Maximum Ratings**

Parameter	Rating	Unit
Supply Current	120	mA
Input RF Power	+13	dBm
Operating Ambient Temperature	-40 to +75	°C
Storage Temperature	-60 to +150	°C



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Baramotor	Specification		Unit	Condition		
Falameter	Min.	Тур.	Max.	Unit	Condition	
Overall					T=25°C, I <sub>CC</sub> =65mA	
Frequency Range		DC to 4000		MHz		
3dB Bandwidth		2.5		GHz		
Gain		19.4		dB	Freq=100MHz	
		18		dB	Freq=1000MHz	
		16		dB	Freq=2000MHz	
		14		dB	Freq=3000MHz	
		13			Freq=4000MHz	
Gain Flatness		±2		dB	100MHz to 2000MHz	
Noise Figure		4.8		dB	Freq=2000MHz	
Input VSWR		2.1:1			In a 50 $\Omega$ system, DC to 4000 MHz	
Output VSWR		1.8:1			In a 50 $\Omega$ system, DC to 4000 MHz	
Output IP <sub>3</sub>		+33		dBm	Freq=1000MHz±50kHz, P <sub>TONE</sub> =-10dBm	
Output P <sub>1dB</sub>		+18.5		dBm	Freq=1000MHz	
Reverse Isolation		20.5		dB	Freq=2000MHz	
Power Supply					With $22\Omega$ bias resistor	
Device Operating Voltage		4.8		V	At pin 5 with I <sub>CC</sub> =65mA	
Operating Current		65		mA		

Pin	Function	Description	Interface Schematic
1	GND	Ground connection. Keep traces physically short and connect immedi- ately to ground plane for best performance.	
2	GND	Same as pin 1.	
3	RF IN	RF input pin. This pin is NOT internally DC blocked. A DC blocking capacitor, suitable for the frequency of operation, should be used in most applications. DC coupling of the input is not allowed, because this will override the internal feedback loop and cause temperature instability.	
4	GND	Same as pin 1.	
5	RF OUT	RF output and bias pin. Biasing is accomplished with an external series resistor and choke inductor to $V_{CC}$ . The resistor is selected to set the DC current into this pin to a desired level. The resistor value is determined by the following equation: $R = \frac{(V_{SUPPLY} - V_{DEVICE})}{I_{CC}}$ Care should also be taken in the resistor selection to <b>ensure that the current into the part never exceeds 120 mA over the planned operating temperature</b> . This means that a resistor between the supply and this pin is always required, even if a supply near 4.9V is available, to provide DC feedback to prevent thermal runaway. Because DC is present on this pin, a DC blocking capacitor, suitable for the frequency of operation, should be used in most applications. The supply side of the bias network should also be well bypassed.	

# Evaluation Board Schematic





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## Evaluation Board Layout Board Size 1" x 1"





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