

PRELIMINARY

Low Dropout Voltage Regulator with Reset

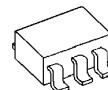
■ GENERAL DESCRIPTION

The NJM2801 is a low dropout voltage regulator with reset function.

It provides up to 150mA of logic supply, and the reset function monitors either input or output voltage of the regulator with 2% accuracy.

It is suitable for local power supply and reset for small micro controller and other logic chips.

■ PACKAGE OUTLINE



NJM2801F**

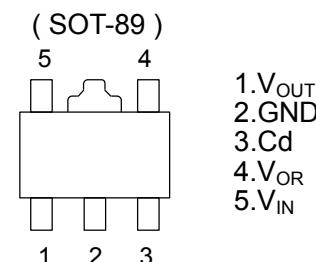
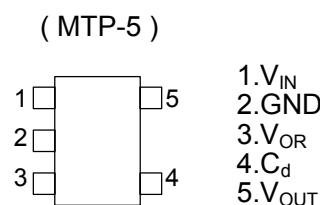


NJM2801U**

■ FEATURES

■ PIN CONFIGURATION

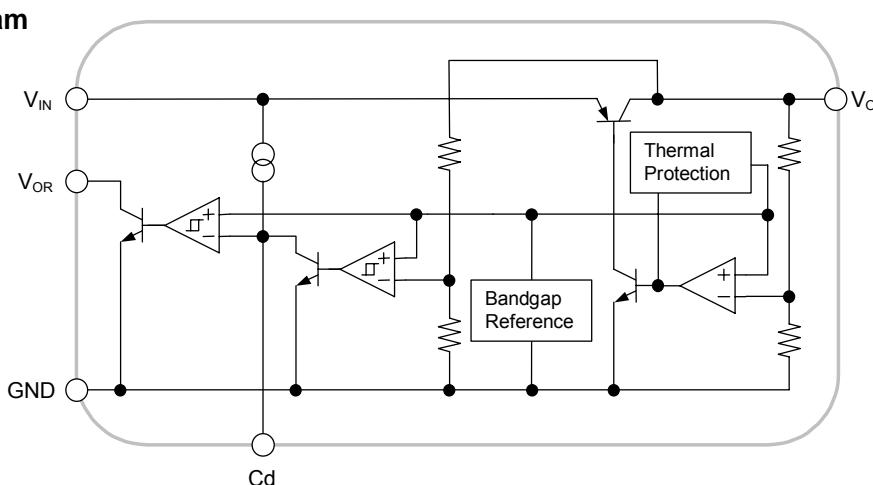
- Output Voltage Accuracy $V_o = \pm 2.2\%$
 - Reset Output Voltage $V_{ORH} = 3.3V(\text{typ.})$
 - Reset Hold Time $t_d = 10mS \pm 2.5mS$
 - Ripple Rejection $RR=60dB (\text{typ.})$
 - Quiescent Current $I_Q=300\mu A(\text{max.})$
 - Open Collector Output
 - Bipolar Technology
 - Output Voltage Monitor type
 - Package Outline SOT89(5Pin)/MTP5
 - Protection Circuit
 - 1. Current limit circuit
 - 2. Thermal overload protection circuit



■ Output Voltage/Reset Validated Voltage

PART NO	Output Voltage	Reset Validated Voltage
NJM2801-0543	5.0V	4.3V

■ Block Diagram



■ Absolute Maximum Ratings

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Input Voltage	V _{IN}	+14	V
Power Dissipation	P _D	200 (MTP5)	mW
		350 (SOT-89)	
Operating Temperature	To _{pr}	-40~+85	°C
Storage Temperature Range	T _{stg}	-40~+125	°C

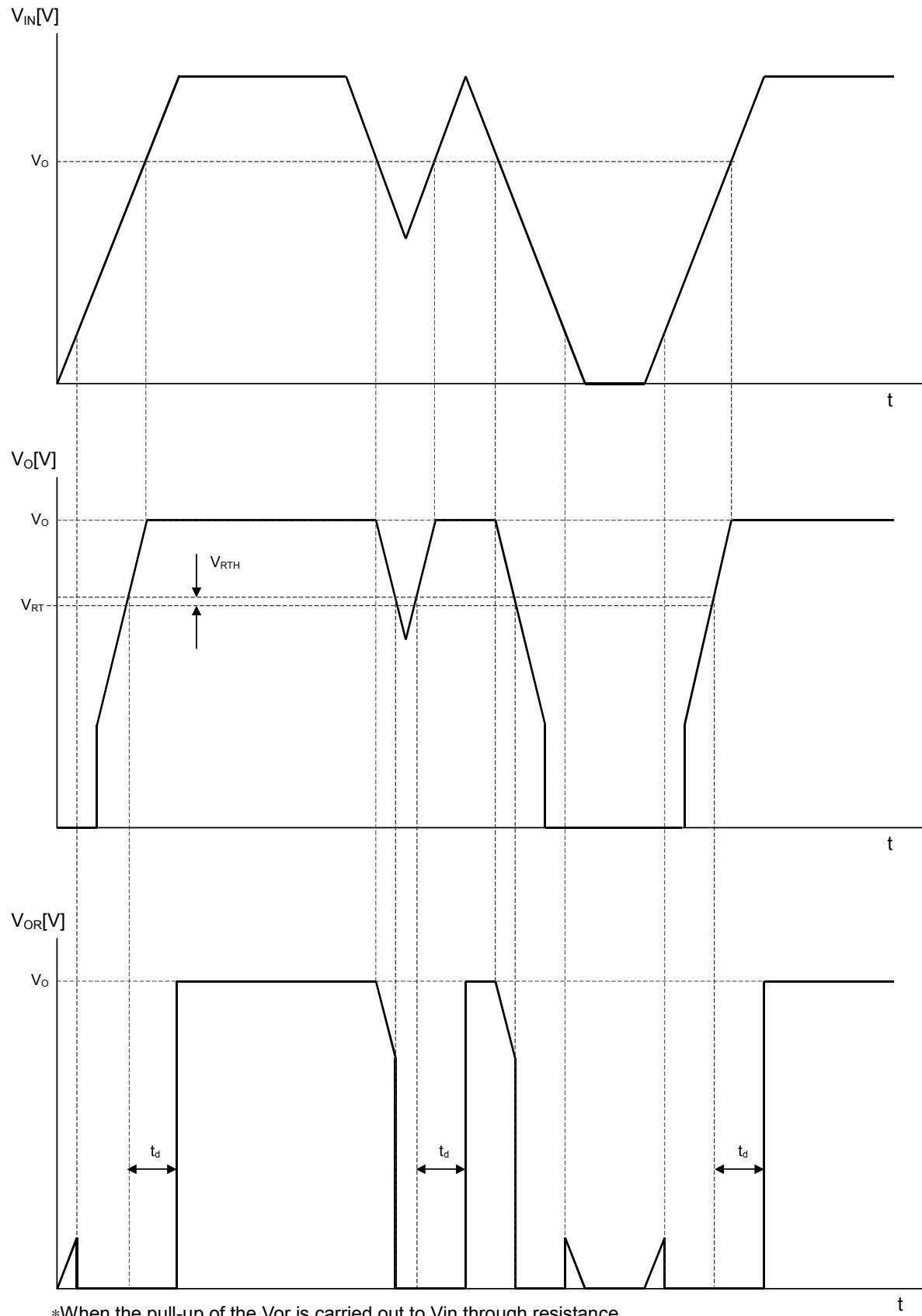
■ ELECTRICAL CHARACTERISTICS (V_{IN}=Vo+1V, C_{IN}=0.1μF, Co=1μF (Vo≤2.6V: Co=2.2μF) Ta=25°C)

PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Quiescent Current	I _Q	I _O =0mA	—	200	300	μA
Regulator Block						
Output Voltage	V _O	I _O =30mA	-2.2%	—	+2.2%	V
Output Current	I _O	V _O -0.3V	150	200	—	mA
Line Regulation	ΔV _O /ΔV _{IN}	V _{IN} =Vo+1V~Vo+6V, I _O =30mA	—	—	0.10	%/V
Load Regulation	ΔV _O /ΔI _O	I _O =0~100mA	—	—	0.03	%/mA
Dropout Voltage	ΔV _{L_O}	I _O =60mA	—	0.10	0.18	V
Ripple Rejection	RR	E _{IN} =200mVrms, f=1kHz, I _O =10mA, V _O =3V	—	60	—	dB
Output Voltage Temperature Coefficient	ΔV _O /ΔT	T _a =0~85°C, I _O =10mA	—	+50	—	ppm/°C
Output Noise Voltage	V _{NO}	f=10Hz~100kHz, I _O =10mA, V _O =3V	—	45	—	μVrms
Reset Block						
Voltage Detection	V _{RT}	V _{IN} =H→L	-2%	—	+2%	V
Hysteresis Voltage	V _{RTH}	V _{IN} =H→L→H	V _{RT} ×3%	V _{RT} ×5%	V _{RT} ×8%	mV
Low Level Output	R _{ORL}	V _{IN} =V _{RT} -0.5V, R _L =100kΩ	—	100	300	mV
Output Leak Current	I _{ORH}	V _{IN} =V _{RT} -0.5V	—	—	0.1	μA
On time Output Current	I _{ORL}	V _{IN} =V _{RT} -0.5V, R _L =0Ω	5	—	—	mA
Reset Output Delay	t _d	V _{IN} =(V _{RT} -0.5V)→(V _{RT} +0.5V), C _d =0.1μF	9	10	11	μS
Operation Voltage Limit	V _{OPL}	V _{ORL} =0.4V	—	0.9	—	V

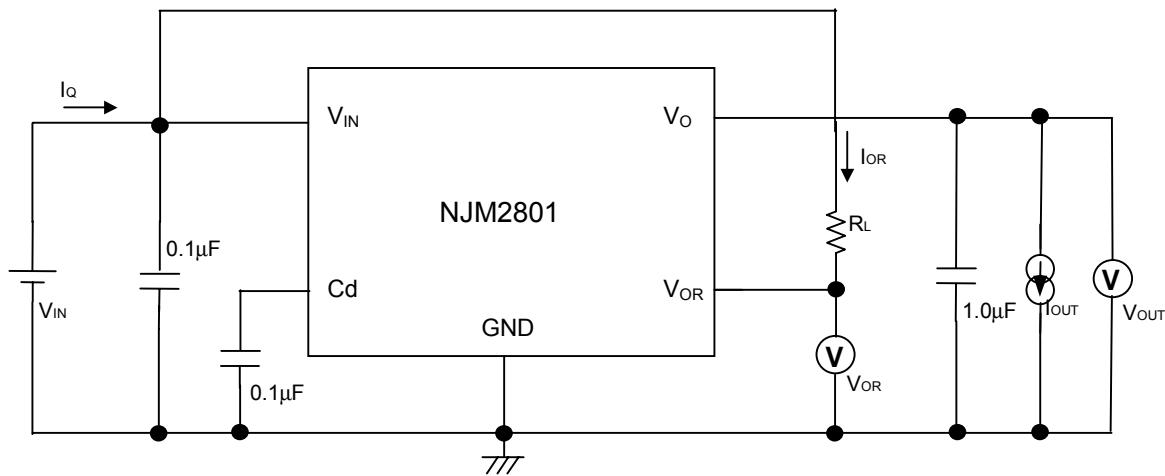
The above specification is a common specification for all output voltages.

Therefore, it may be different from individual specification for a specific output voltage.

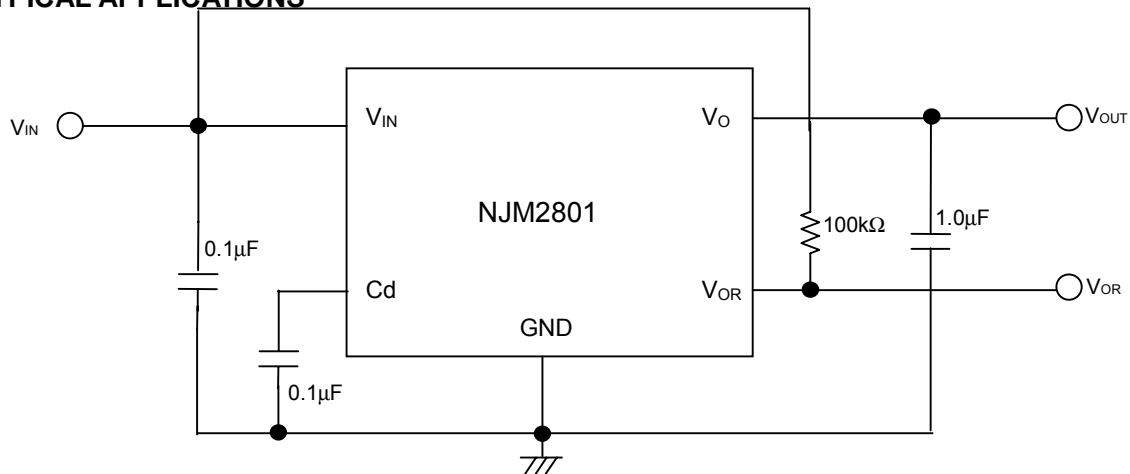
■ TIMING CHART



■ TEST CIRCUIT



■ TYPICAL APPLICATIONS



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