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# 2SK1698

Silicon N-Channel MOS FET

# HITACHI

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## Application

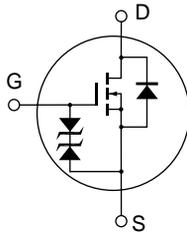
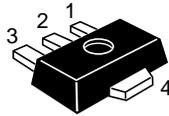
High speed power switching

## Features

- Low on-resistance
- High speed switching
- Low drive current
- 4 V gate drive device - - - can be driven from 5 V source.
- Suitable for DC – DC converter, motor drive, power switch, solenoid drive

## Outline

UPAK



1. Gate
2. Drain
3. Source
4. Drain

## 2SK1698

### Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{DSS}$	100	V
Gate to source voltage	$V_{GSS}$	±20	V
Drain current	$I_D$	0.3	A
Drain peak current	$I_{D(pulse)}^{*1}$	1.2	A
Body to drain diode reverse drain current	$I_{DR}$	0.3	A
Channel dissipation	$P_{ch}^{*2}$	1	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

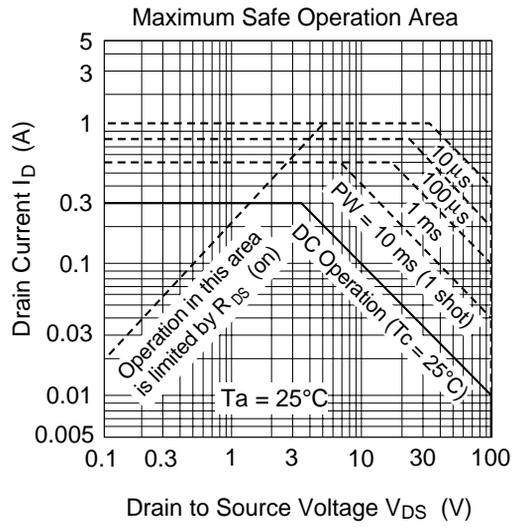
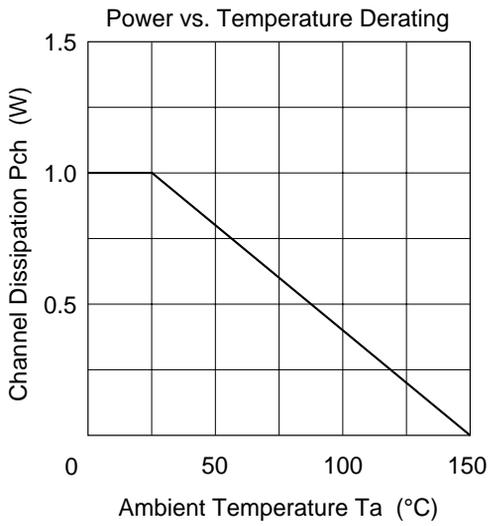
- Notes
1.  $PW \leq 10 \mu s$ , duty cycle  $\leq 1\%$
  2. When using the alumina ceramic board (12.5 × 20 × 0.7 mm)
  3. Marking is "FY".

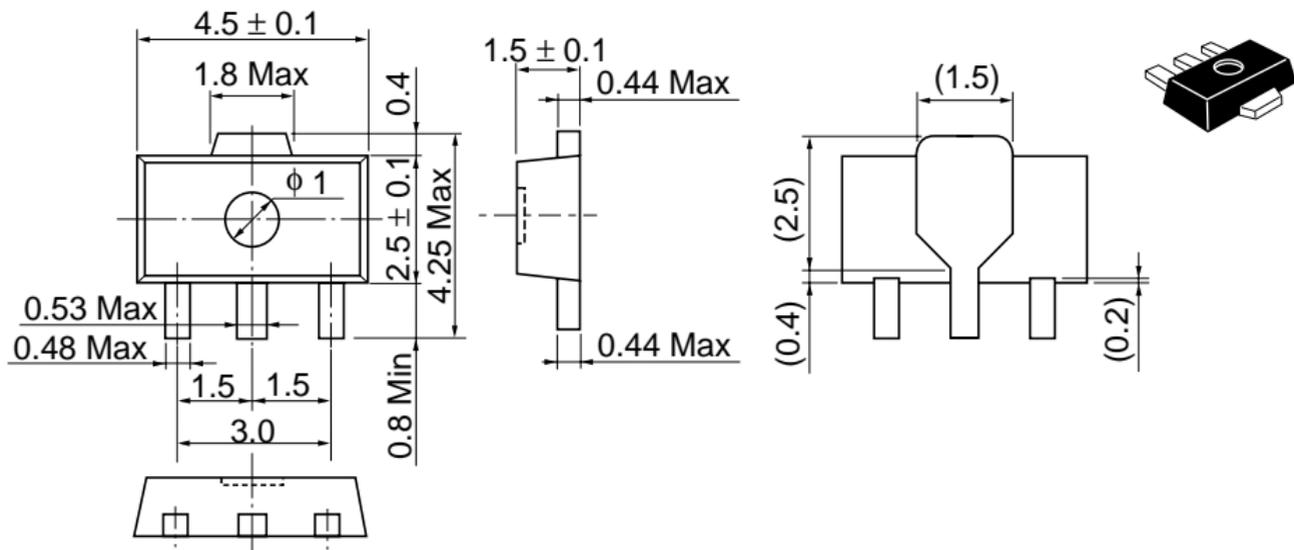
## Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	100	—	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	$\pm 20$	—	—	V	$I_G = \pm 100 \text{ }\mu\text{A}, V_{DS} = 0$
Gate to source leak current	$I_{GSS}$	—	—	$\pm 10$	$\mu\text{A}$	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	$I_{DSS}$	—	—	50	$\mu\text{A}$	$V_{DS} = 80 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	—	2.0	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	$R_{DS(on)}$	—	3.5	4.5	$\Omega$	$I_D = 0.2 \text{ A}, V_{GS} = 10 \text{ V}^{*1}$
		—	4.5	6.5	$\Omega$	$I_D = 0.2 \text{ A}, V_{GS} = 4 \text{ V}^{*1}$
Forward transfer admittance	$ y_{fs} $	0.22	0.35	—	S	$I_D = 0.2 \text{ A}, V_{DS} = 10 \text{ V}^{*1}$
Input capacitance	$C_{iss}$	—	35	—	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Output capacitance	$C_{oss}$	—	14	—	pF	$f = 1 \text{ MHz}$
Reverse transfer capacitance	$C_{rss}$	—	3.5	—	pF	
Turn-on delay time	$t_{d(on)}$	—	2	—	ns	$I_D = 0.2 \text{ A}, V_{GS} = 10 \text{ V},$
Rise time	$t_r$	—	4	—	ns	$R_L = 150 \text{ }\Omega$
Turn-off delay time	$t_{d(off)}$	—	17	—	ns	
Fall time	$t_f$	—	15	—	ns	
Body to drain diode forward voltage	$V_{DF}$	—	0.9	—	V	$I_F = 0.3 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	$t_{rr}$	—	80	—	ns	$I_F = 0.3 \text{ A}, V_{GS} = 0,$ $di_F/dt = 50 \text{ A}/\mu\text{s}$

Note 1. Pulse test

See characteristic curve of 2SK1337.





Hitachi Code	UPAK
JEDEC	—
EIAJ	Conforms
Weight (reference value)	0.050 g

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