

## HIGH RELIABILITY HYBRID DC-DC CONVERTERS WITH INTEGRAL EMI FILTER

#### **DESCRIPTION**

The DVEHF series of high reliability DC-DC converters is operable over the full military (-55 °C to +125 °C) temperature range with no power derating. Unique to the DVEHF series is a fault tolerant magnetic feedback circuit. Operating at a nominal fixed frequency of 450 kHz per stage, these regulated, isolated units utilize well-controlled undervoltage lockout circuitry to eliminate slow start-up problems.

These converters are designed and manufactured in a facility qualified to ISO9001 and certified to MIL-PRF-38534 and MIL-STD-883.

This product may incorporate one or more of the following U.S. patents:

5,784,266 5,790,389 5,963,438 5,999,433 6,005,780 6,084,792 6,118,673

#### **FEATURES**

- High Reliability
- Very Low Output Noise
- Wide Input Voltage Range: 15 to 50 Volts per MIL-STD-704
- Up to 10 Watts Output Power
- Fault Tolerant Magnetic Feedback Circuit
- NO Use of Optoisolators
- Undervoltage Lockout
- Indefinite Short Circuit Protection
- Current Limit Protection
- Industry Standard Pinout
- High Input Transient Voltage: 80 Volts for 1 sec per MIL-STD-704A
- Solder Seal Hermetic Package
- Custom Versions Available
- Additional Environmental Screening Available
- No External EMI Filter Required
- Meets MIL-STD-461C and MIL-STD-461D EMC Requirements
- Protects Against Conducted Susceptibility Specified in MIL-STD-461C, SC01 and CS02
- Non-flanged Version Available
- MIL-PRF-38534 Element Evaluated Components

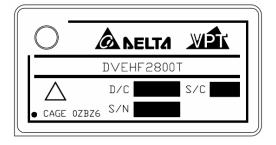


Figure 1 – DVEHF2800T DC-DC Converter (Not To Scale)



 $SPECIFICATIONS \ (T_{CASE} = -55^{\circ}C \ to \ +125^{\circ}C, \ V_{IN} = +28V \pm 5\%, \ Full \ Load^{5}, \ Unless \ Otherwise \ Specified)$ 

**ABSOLUTE MAXIMUM RATINGS** 

Input Voltage (Continuous)  $50 \ V_{DC}$ Input Voltage (Transient, 1 second) 80 Volts

**Output Power** 10 Watts Power Dissipation (Full Load, T<sub>CASE</sub> = +125°C) 5.0 Watts Junction Temperature Rise to Case

Storage Temperature

-65°C to +150°C

Lead Solder Temperature (10 seconds)

270°C

+15°C

Weight (Maximum) 50 Grams

Parameter		0 1141	DVEHF28512T			DVEHF28515T			Unito
		Conditions	Min	Тур	Max	Min	Тур	Max	Units
STATIC									
INPUT Voltage		Continuous	15	28	50	15	28	50	V
		Transient, 1 sec <sup>4</sup>	-	-	80	-	-	80	V
Current		Inhibited	-	3.5	6.0	-	3.5	6.0	mA
Odificilit		No Load	-	-	70	-	-	70	mA
Inhibit Pin Input <sup>4</sup>			0	-	1.5	0	-	1.5	V
Inhibit Pin Open Circuit	Voltage⁴		9.0	11.0	13.0	9.0	11.0	13.0	V
UVLO Turn On			8.0	-	12.0	8.0	-	12.0	V
UVLO Turn Off⁴			4.0	-	8.0	4.0	-	8.0	V
	$V_{\text{MAIN}}$	T <sub>CASE</sub> = 25°C	4.95	5.0	5.05	4.95	5.0	5.05	V
OUTPUT	$\pm V_{\text{AUX}}$		11.88	12.0	12.12	14.85	15.0	15.15	V
Voltage	$V_{\text{MAIN}}$	T <sub>CASE</sub> = -55°C to +125°C	4.925	5.0	5.075	4.925	5.0	5.075	V
	$\pm V_{AUX}$	1 <sub>CASE</sub> = -55 C t0 +125 C	11.4	12.0	12.6	14.25	15.0	15.75	V
	Total		0	-	10	0	-	10	W
Power⁴	$V_{\text{MAIN}}$		0.15	-	5.0	0.15	-	5.0	W
	$\pm V_{AUX}{}^6$	Either Output	0	-	3.5	0	-	3.5	W
Current <sup>3</sup> V <sub>MA</sub> ±V <sub>AU</sub>			0	-	1.0	0	-	1.0	Α
		Either Output <sup>6</sup>	0	-	0.29	0	-	0.23	Α
Dinnla Valtaga	$V_{MAIN}$	Full Load <sup>5</sup> , 20Hz to 10MHz	-	15	50	-	15	50	$mV_{p-p}$
Ripple Voltage	$\pm V_{\text{AUX}}$	Full Load , 20HZ to TOWINZ	-	15	50	-	15	50	$mV_{p-p}$
Line Degulation	$V_{MAIN}$	V <sub>IN</sub> = 15V to 50V	-	10	20	-	10	20	mV
Line Regulation	$\pm V_{\text{AUX}}$	V <sub>IN</sub> = 15V to 50V	-	15	50	-	15	50	mV
Load Regulation	V <sub>MAIN</sub>	No Load to Full Load⁵	-	5	20	-	5	20	mV
Load Regulation	$\pm V_{\text{AUX}}$	NO LOAG TO FUII LOAG	-	10	50	-	10	50	mV
Cross Regulation	±V <sub>AUX</sub>	+V <sub>OUT</sub> = 30%, -V <sub>OUT</sub> = 70% +V <sub>OUT</sub> = 70%, -V <sub>OUT</sub> = 30%	-	-	50	-	-	50	mV
EFFICIENCY		Full Load⁵	67	72	-	67	72	-	%
LOAD FALL T DOWER DIO	CIDATION	Overload <sup>4</sup>	-	-	7.5	-	-	7.5	W
LUAD FAULT POWER DIS	LOAD FAULT POWER DISSIPATION		-	-	7.5	-	-	7.5	W
CAPACITIVE LOAD⁴			-	-	500	-	-	500	μF
SWITCHING FREQUENCY			350	450	550	350	450	550	kHz
ISOLATION		500 V <sub>DC</sub> , T <sub>CASE</sub> = 25°C	100	-	-	100	-	-	ΜΩ
MTBF (MIL-HDBK-217F)		AIF @ T <sub>C</sub> = 55°C	-	350	-	-	350	-	kHrs



 $SPECIFICATIONS \ (T_{CASE} = -55^{\circ}C \ to \ +125^{\circ}C, \ V_{IN} = +28V \pm 5\%, \ Full \ Load^{5}, \ Unless \ Otherwise \ Specified)$ 

#### **ABSOLUTE MAXIMUM RATINGS**

Input Voltage (Continuous)  $50 V_{DC}$ Junction Temperature Rise to Case +15°C -65°C to +150°C Input Voltage (Transient, 1 second) 80 Volts Storage Temperature **Output Power** 10 Watts Lead Solder Temperature (10 seconds) 270°C Power Dissipation (Full Load,  $T_{CASE} = +125^{\circ}C$ ) 5.0 Watts Weight (Maximum) 50 Grams

Parameter		Conditions	DVEHF28512T			DVEHF28515T			Units
		Conditions	Min	Тур	Max	Min	Тур	Max	Office
DYNAMIC									
Load Step Output Transient	$V_{\text{MAIN}}$		-	200	500	-	200	500	$mV_{PK}$
Load Step Output Transient	$\pm V_{\text{AUX}}$	Half Load to Full Load	-	100	400	-	100	400	$mV_{PK}$
Load Step Recovery <sup>2</sup>	$V_{\text{MAIN}}$	Tiali Load to Full Load	-	200	500	-	200	500	μSec
	$\pm V_{\text{AUX}}$		-	200	500	-	200	500	μSec
Line Step Output Transient <sup>4</sup>	$V_{\text{MAIN}}$		-	200	600	-	200	600	$mV_{PK}$
Line Step Output Transient <sup>4</sup> ±V		$V_{IN} = 16V \text{ to } 40V$	-	50	100	-	50	100	$mV_{PK}$
Line Step Recovery <sup>2, 4</sup>	$V_{\text{MAIN}}$	V <sub>IN</sub> - 10V to 40V	-	200	500	-	200	500	μSec
Line Step Recovery	$\pm V_{\text{AUX}}$		-	200	500	-	200	500	μSec
Turn On Delay			-	20	30	-	20	30	mSec
Turn On Overshoot	$V_{\text{MAIN}}$	V <sub>IN</sub> = 0V to 28V	-	-	25	-	-	25	$mV_{PK}$
Turri On Overshoot	$\pm V_{\text{AUX}}$		-	-	50	-	-	50	$mV_{PK}$

- Notes: 1. This note intentionally not used.
  - 2. Time for output voltage to settle within 1% of its nominal value.
  - 3. Derate linearly to 0 at 135°C.
  - 4. Verified by qualification testing.
  - 5. 5.0W on  $V_{MAIN}$  and 2.5W on  $\pm V_{AUX}$ .
  - 6. Up to 70% of the total auxiliary power or current can be drawn from either of the auxiliary outputs.



### **BLOCK DIAGRAM**

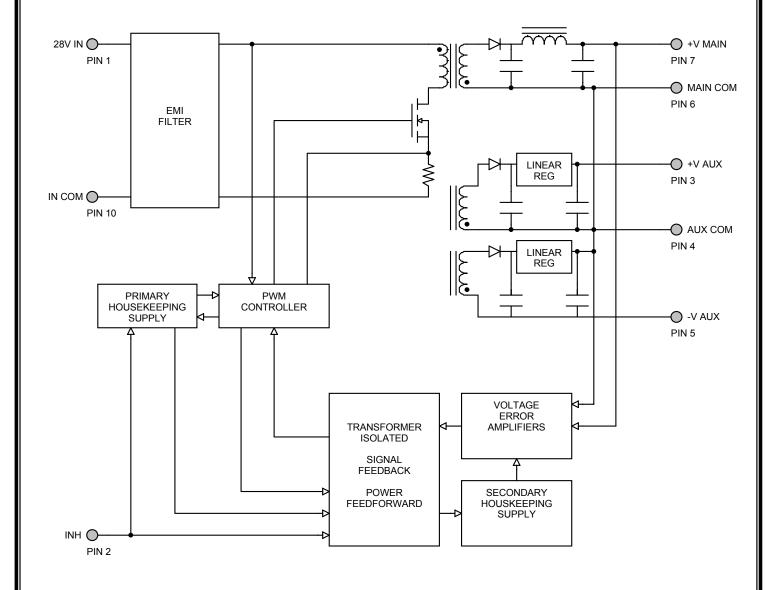


Figure 2



### CONNECTION DIAGRAM

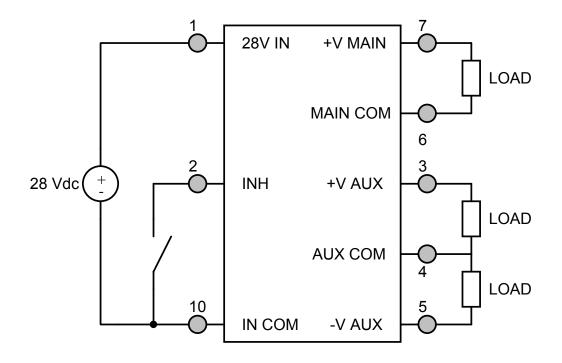
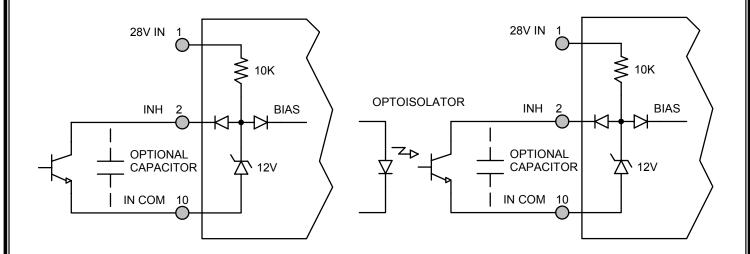


Figure 3

#### INHIBIT DRIVE CONNECTION DIAGRAMS



**Figure 4** – Internal Inhibit Circuit and Recommended Drive (Shown with optional capacitor for turn-on delay)

Figure 5 – Isolated Inhibit Drive (Shown with optional capacitor for turn-on delay)



EFFICIENCY PERFORMANCE CURVES (T<sub>CASE</sub> = 25°C)

V <sub>IN</sub> = 15V	V <sub>IN</sub> = 28V	V <sub>IN</sub> = 50V
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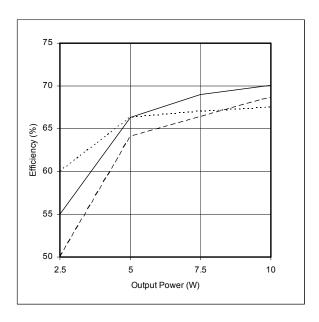


Figure 7 – DVEHF28512T Efficiency (%) vs. Output Power (W)

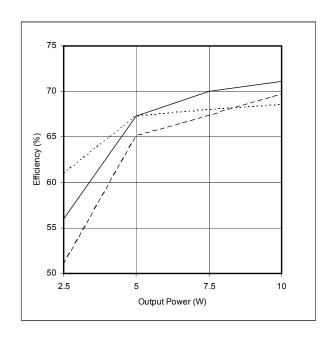


Figure 8 – DVEHF28515T Efficiency (%) vs. Output Power (W)



#### EMI PERFORMANCE CURVES

 $(T_{CASE} = 25^{\circ}C, V_{IN} = +28V \pm 5\%, Full Load, Unless Otherwise Specified)$ 

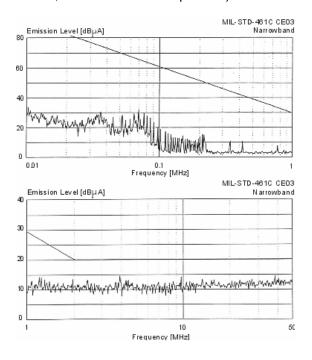


Figure 9 – MIL-STD-461C DVEHF2800T

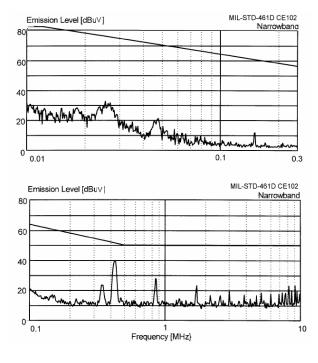
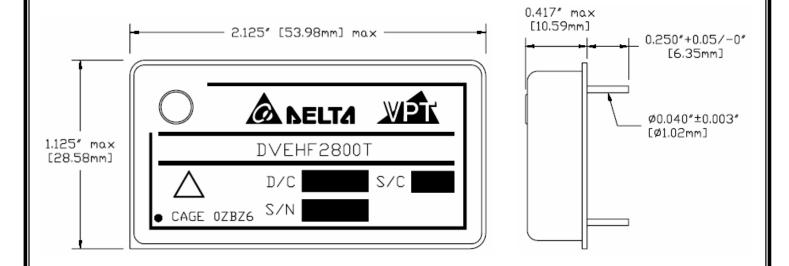


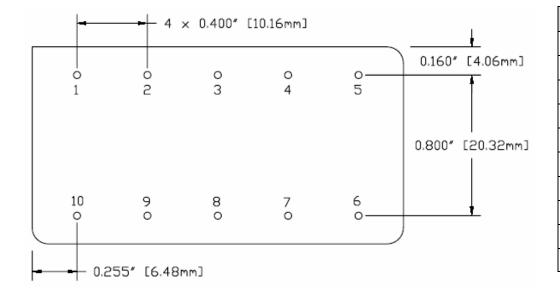
Figure 10 – MIL-STD-461D DVEHF2800T



### PACKAGE SPECIFICATIONS (NON-FLANGED, SOLDER SEAL)



#### TOP VIEW SIDE VIEW



PIN	FUNCTION
1	28V IN
2	INHIBIT
3	+V AUX
4	AUX COM
5	-V AUX
6	MAIN COM
7	+V MAIN
8	CASE
9	N/C
10	IN COM

#### **BOTTOM VIEW**

**Figure 11** – Non-Flanged, Solder Seal Tin Plated Package and Pinout (Dimensional Limits are ±0.005" Unless Otherwise Stated)



### PACKAGE PIN DESCRIPTION

Pin	Function	Description			
1	28V IN	Positive Input Voltage Connection			
2	INHIBIT	Logic Low = Disabled Output. Connecting the inhibit pin to input common causes converter shutdown.  Logic High = Enabled Output. Unconnected or open collector TTL.			
3	+V AUX	Positive Auxiliary Output Voltage Connection			
4	AUX COM	Auxiliary Output Common Connection			
5	-V AUX	Negative Auxiliary Output Voltage Connection			
6	MAIN COM	Main Output Common Connection			
7	+V MAIN	Positive Main Output Voltage Connection			
8	CASE	Case Connection			
9	N/C	No Connection			
10	IN COM	Input Common Connection			

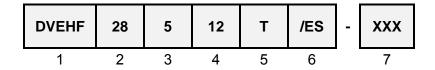


ENVIRONMENTAL SCREENING (100% Tested Per MIL-STD-883 as referenced to MIL-PRF-38534)

Screening	MIL-STD-883	Standard (No Suffix)	Extended /ES
Non- Destructive Bond Pull	Method 2023	•	•
Internal Visual	Method 2017, 2032 Internal Procedure	•	•
Temperature Cycling	Method 1010, -55°C to 125°C		•
Constant Acceleration	Method 2001, 500g, Y1 Direction		•
Burn-In	96 hours at +125°C 24 hours at +125°C	•	•
Final Electrical	100% at 25°C	•	•
Hermeticity	Method 1014, Fine Leak, Condition A Method 1014, Gross Leak, Condition C Dip (1 x 10 <sup>-3</sup> )	•	•
External Visual	Method 2009	•	•



#### ORDERING INFORMATION



(1) (2) (3)

Product Series	Nominal Input Voltage		Main Output Voltage		i Wain Chithlit Volta		Auxiliary Output Voltages	
DVEHF	28	28 Volts	5	+ 5 Volts	12 15	± 12 Volts ± 15 Volts		

(5) (6) (7)

Number o	of Outputs	Screenir	ng Code <sup>1</sup>	Additional Screening Code		
Т	Triple	None /ES	Standard Extended	Contact Sales		

Notes: 1. VPT Inc. reserves the right to ship higher screened products to meet lower screened orders at our sole discretion unless specifically forbidden by customer contract.

Please contact your sales representative or the VPT Inc. Sales Department for more information concerning additional environmental screening and testing, different input voltage, output voltage, power requirement, source inspection, and/or special element evaluation for space or other higher quality applications.

#### CONTACT INFORMATION

To request a quotation or place orders please contact your sales representative or the VPT Inc. Sales Department at:

Phone: (425) 353-3010 Fax: (425) 353-4030 E-mail: vptsales@vpt-inc.com

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