

Epitaxial planar PNP silicon transistor

Description

• Dual chip digital transistor

Features

- Two SRA2207 chips in SOT-353 package
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process

Ordering Information

Type NO.	Marking	Package Code	
SUR534H	34H	SOT-353	

Outline Dimensions

1.95~2.25 0.65 Typ. • Equivalent Circuit 1.15~1.35 0.30 Max. 5 1• -R2 R2, Trz 1.90~2.10 2 3 4 \mathbf{R}_1 \mathbf{R}_2 0.65 Typ. 10KΩ 47KΩ Tr1 Tr2 10KΩ 47KΩ 0.85~0.95 **PIN Connections** 1. IN 1 0.25 Min. 2. COMMON 1,2 3. IN 2 0.19 Max. 0.10 Max. 4. OUT 2 5. OUT 1

SUR534H

(Ta=25°C)

Absolute Maximum Ratings [Tr1,Tr2]

Absolute Maximum Ratings [Tr1,Tr2]			(Ta=25°C)	
Characteristic	Symbol	Rating	Unit	
Output voltage	Vo	-50	V	
Input voltage	VI	-30, 6	V	
Output current	Io	-100	mA	
Power dissipation	P _D *	200	mW	
Junction temperature	Tյ	150	°C	
Storage temperature range	T _{stg}	-55 ~ 150	°C	

*: Total rating

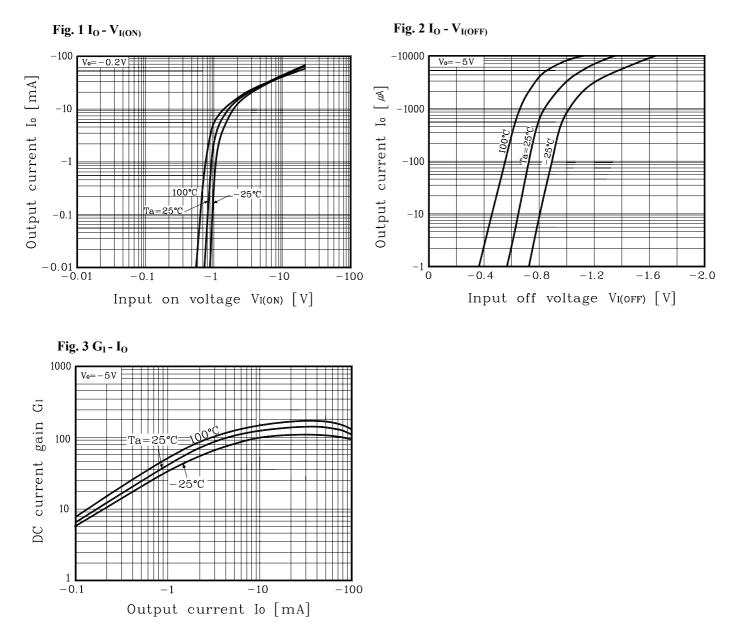
Electrical Characteristics [Tr1,Tr2]

Characteristic Symbol **Test Condition** Min. Typ. Max. Unit -500 Output cut-off current $V_0 = -50V, V_I = 0$ $I_{\text{O(OFF)}}$ nA _ -DC current gain G_{I} $V_0 = -5V$, $I_0 = -10mA$ 80 --150 Output voltage I_0 =-10mA, I_I =-0.5mA -0.1 -0.3 V V_{O(ON)} _ Input voltage (ON) V_0 =-0.2V, I_0 =-5mA -1.8 V -V_{I(ON)} _ Input voltage (OFF) V₀=-5V, I₀=-0.1mA V -0.5 $V_{I(OFF)}$ -_ f_{T}^{*} --Transition frequency V_0 =-10V, I_0 =-5mA, f=1MHz 200 MHz V_{I} =-5V, I_{O} =0 Input current -0.88 I_{I} mΑ -Input resistor (Input to base) 7 13 KΩ R_1 10 -KΩ Input resistor (Base to common) R_2 33 47 61 -

* : Characteristic of transistor only

SUR534H

Electrical Characteristic Curves [Tr1,Tr2]



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