

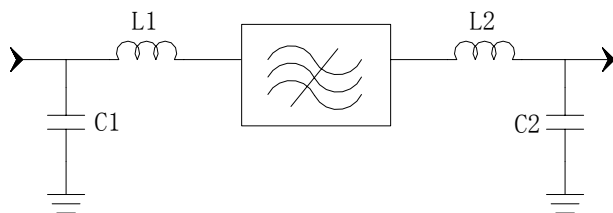
### Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	151.7	151.8	151.9
Insertion Loss	dB	-	29.5	31
3 dB Bandwidth	MHz	8.1	8.15	-
30 dB Bandwidth	MHz	-	8.84	8.9
40 dB Bandwidth	MHz	-	8.9	9.2
Group delay Variation ( $f_0 \pm 4.05\text{MHz}$ )	nsec	-	100	200
Passband Variation	dB	-	0.8	1.2
Absolute Delay	usec	-	3.54	4
Ultimate Rejection	dB	48	50	-
Substrate Material		112 LiTaO <sub>3</sub>		
Ambient Temperature	°C	25		
Package Size		DIP3512 (35.2x12.7x5.2mm <sup>3</sup> )		

**Notes:**

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

### Matching Configuration



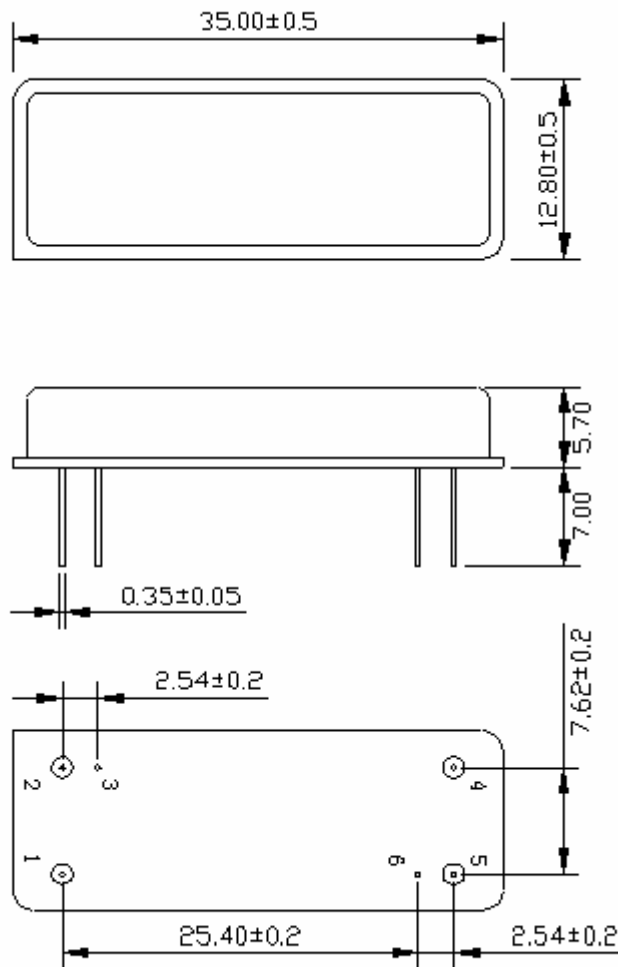
**L1=27nH    L2=18nH**  
**C1=39pF    C2=47pF**


**Source/Load Impedance=50 ohm**

Notes - Component values may change depending on board layout.

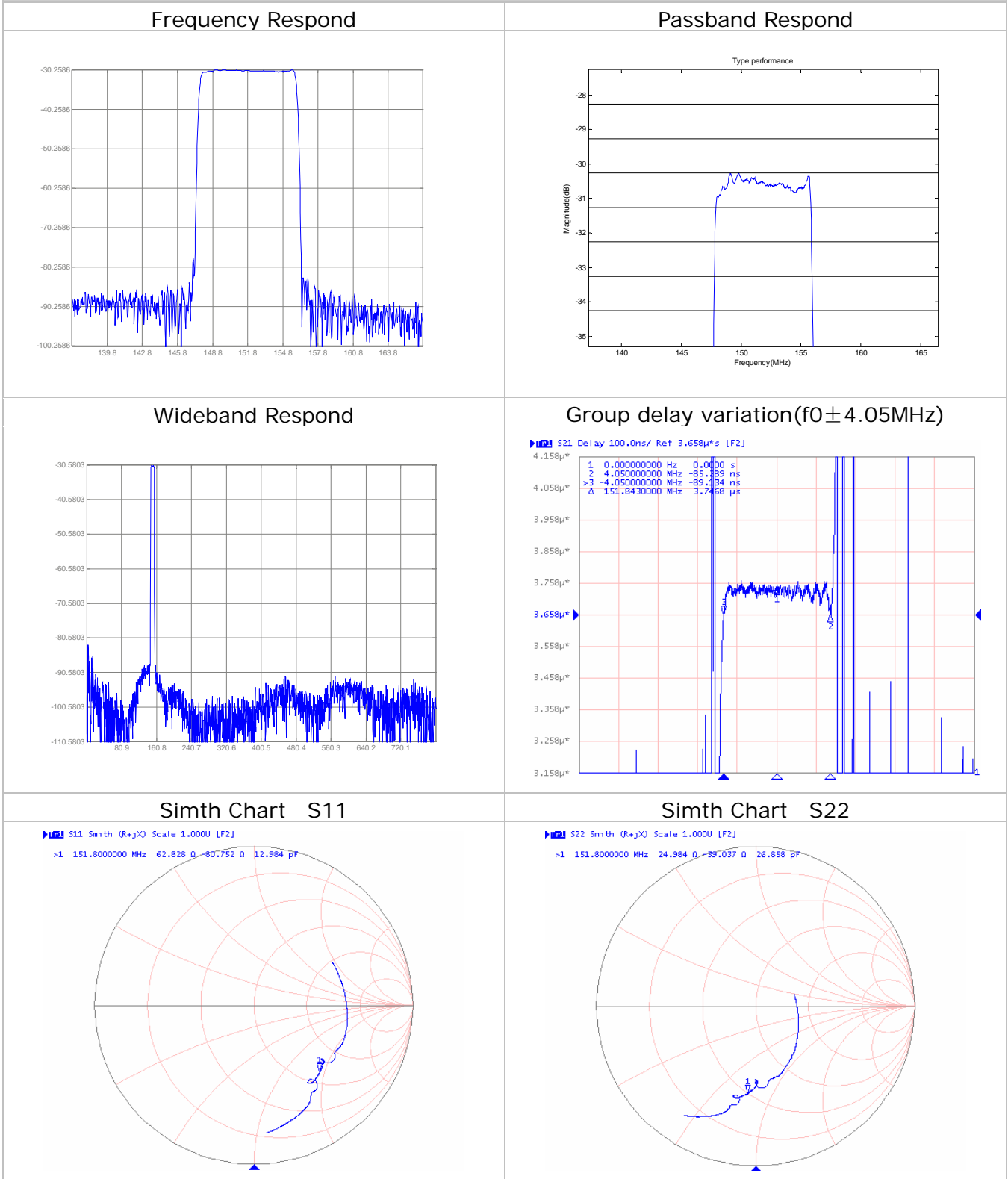
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*Package Dimension*



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*Typical Performance*



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