

Electronics

Digital Attenuator, 5-Bit, 31 dB 400 - 2500 MHz

Features

- 5 Bits, 1 dB Steps
- Excellent Accuracy
- Single Positive Control (+3 V to +5 V)
- Lead-Free QSOP-16 (SSOP-16) Package
- 100% Matte Tin Plating over Copper
- Halogen-Free "Green" Mold Compound
- 260°C Reflow Compatible
- RoHS* Compliant Version of MAATSS0002

Description

M/A-COM's MAADSS0010 is a 1 dB step GaAs MMIC digital attenuator with 31 dB attenuation range in a lead-free QSOP-16 (SSOP-16) package. It requires external DC blocking capacitors on the RF ports, positive supply voltage and five individual bit control voltages.

The MAADSS0010 is particularly suited where high attenuation accuracy, low insertion loss and low intermodulation products are required. Typical applications include base stations, wireless data, and wireless local loop gain level control circuits.

The MAADSS0010 is fabricated using M/A-COM's GaAs 1.0 micron process. The process features full chip passivation for increased performance and reliability.

Ordering Information¹

Part Number	Package	
MAADSS0010	Bulk Packaging	
MAADSS0010TR	1000 piece reel	
MAADSS0010SMB	Sample Test Board	

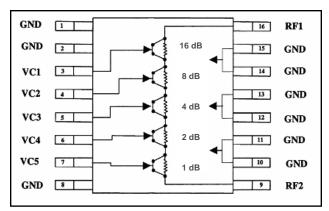
1. Reference Application Note M513 for reel size information.

RoHS Compliant



MAADSS0010 V2

Functional Schematic



Pin Configuration

Pin No.	Function	Pin No.	Function	
1	GND	9	RF2	
2	GND	10	GND	
3	VC1	11	GND	
4	VC2	12	GND	
5	5 VC3 13		GND	
6	6 VC4 14		GND	
7	VC5	15	GND	
8	GND	16	RF1	

Absolute Maximum Ratings ^{2,3}

Parameter	Absolute Maximum	
Input Power	+34 dBm	
Voltage	+7 V	
Operating Temperature	-40°C to +85°C	
Storage Temperature	-65°C to +125°C	

2. Exceeding any one or combination of these limits may cause permanent damage to this device.

M/A-COM does not recommend sustained operation near these survivability limits.

* Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

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Electrical Specifications: $T_A = 25^{\circ}C$, $Z_0 = 50 \Omega$, Vs = +5 V, Vc = 0 V / 5 V

Parameter	Conditions	Units	Min.	Тур.	Max.		
Insertion Loss (reference state)	0.8 - 1.5 GHz	dB	_	1.8	1.9		
	1.5 - 2.0 GHz	dB	_	2.2	2.3		
	2.0 - 2.5 GHz	dB	—	2.7	2.9		
Accuracy	0.8 - 1.5 GHz ± (0.3 dB + 4% of Attenuation)						
	1.5 - 2.0 GHz	E	\pm (0.3 dB + 3% of Attenuation)				
	2.0 - 2.5 GHz	E	\pm (0.4 dB + 3% of Attenuation)				
Step Change	0.8 - 1.5 GHz	dB	0.4	1.0	1.4		
	1.5 - 2.0 GHz	dB	0.4	1.0	1.6		
	2.0 - 2.5 GHz	dB	0.4	1.0	1.8		
Attenuation Range	0.8 - 2.5 GHz	dB	—	31	—		
VSWR	VR 0.8 - 2.5 GHz		_	—	2.0:1		
Trise, Tfall 10% to 90% RF, 90% to 10% RF		μS	—	2	—		
Ton, Toff	Ton, Toff 50% Control to 10% / 90% RF		_	2	—		
Transients In Band		mV	_	120	_		
P1dB	+3V, 0.8 - 2.5 GHz	dBm	15	21	_		
	+5V, 0.8 - 2.5 GHz	dBm	18	27	—		
IP3	Two tones, Pin <u><</u> +5 dBm/tone						
	+3 V, 0.8 - 2.5 GHz	dBm	34	_	—		
	+5 V, 0.8 - 2.5 GHz	dBm	43	—	—		
Control Current	Vc	μA	_		40		
	Vs	μA	—	_	200		

Truth Table ⁴

Control Inputs					
VC5	VC4	VC3	VC2	VC1	Attenuation (dB)
1	1	1	1	1	Reference
0	1	1	1	1	1 dB
1	0	1	1	1	2 dB
1	1	0	1	1	4 dB
1	1	1	0	1	8 dB
1	1	1	1	0	16 dB

4. $0 = 0.0 V \pm 0.2 V$ $1 = Vs = 5.0 V \pm 0.2 V$

Operating Instructions

The MAADSS0010 is designed to operate with 5 V logic levels. The difference between +3 V and +5 V operation is minimal for small signal performance. IIP3, however, is a strong function of voltage. +3 V is the minimum voltage at which the product will reliably operate.

The MAADSS0010 requires a parallel interface that allows the user to enter a 5 bit digital word. Each state increments the attenuation by 1.0 dB giving a total range of 31 dB.

The MAADSS0010 is not internally DC blocked. This means that the device requires DC blocking capacitors on the RF1 and RF2 ports. M/A-COM recommends 0.1 uF to allow for the entire frequency range to be utilized. Higher frequency applications can use smaller value capacitors as DC blocks.

For application information concerning this and other M/A-COM products, please visit our website at www.macom.com, where information including soldering profiles, reliability procedures, and S-parameter data can be found.

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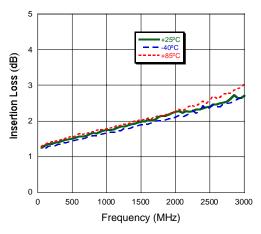




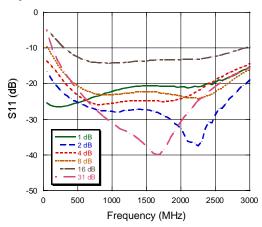
MAADSS0010 V2

Typical Performance Curves

Insertion Loss

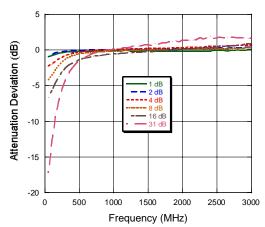


Input Return Loss

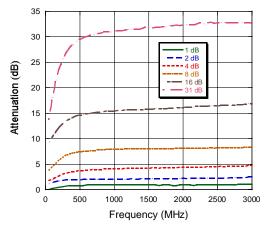


Attenuation Accuracy

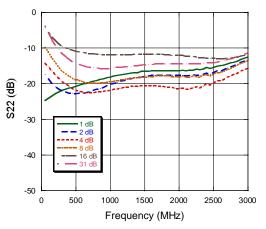
3



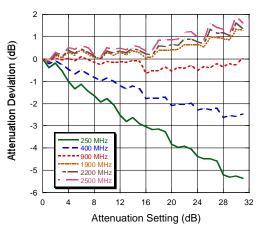
Attenuation at Major Bits



Output Return Loss



Attenuation Accuracy vs. Setting



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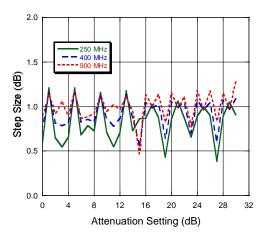
RoHS Compliant



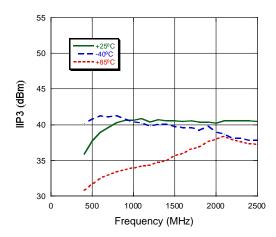
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Typical Performance Curves

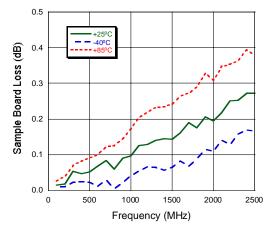
Step Size (low frequency)



IIP3 at 3 V



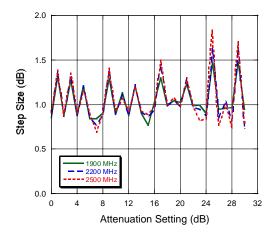
Sample Board Loss



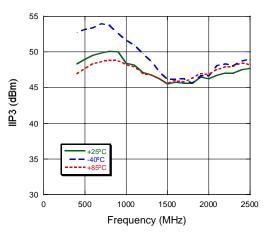
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Step Size (high frequency)







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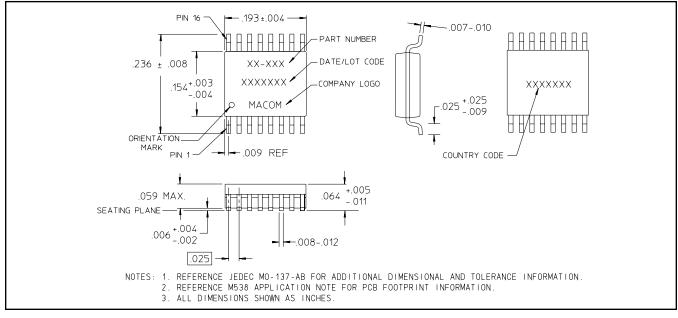
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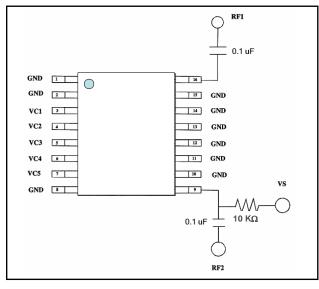
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Lead-Free QSOP-16 (SSOP-16)[†]



⁺ Reference Application Note M538 for lead-free solder reflow recommendations.

Recommended Configuration



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