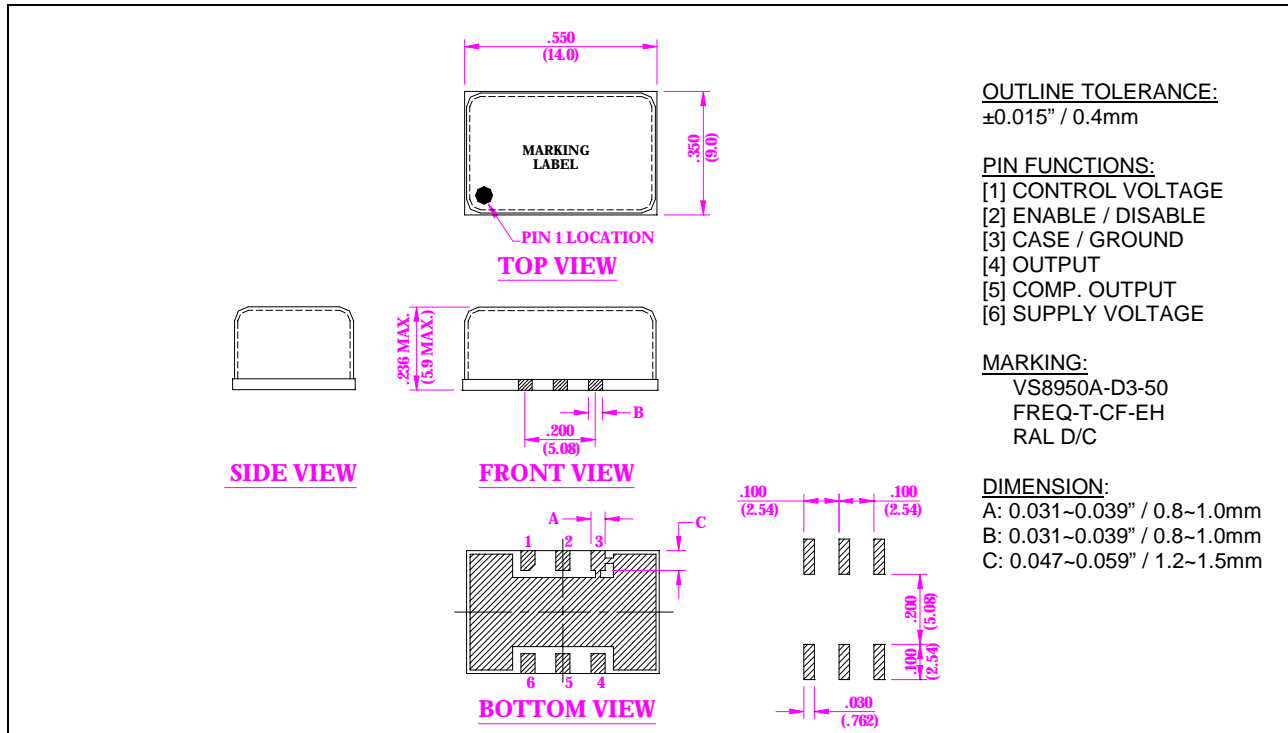


VS8950A-D3-FREQ.-CF-EH

SAW BASED VCO

- APPROVALS
- MECHANICAL SPECIFICATION



ELECTRICAL SPECIFICATION

| PARAMETER | SYMBOL | CONDITIONS | VALUE | UNIT |
|--------------------------------------|--------------|---|--|-------|
| Frequency, nom | fo | - | 622.080; 666.5143 | MHz |
| Supply voltage, nom. | Vcc | Vcc±10% | +3.3 | V |
| Supply current, max. | Is | Vcc=+3.3VDC, Vc=+1.65VDC, Ta=+25°C, 50Ω to Vcc-2.0VDC load | 80.0 | mA |
| PECL output level | VOH / VOL | load=50Ω to Vcc-2.0VDC | +2.275 / +1.68 | V |
| Duty cycle | DC | load=50Ω to Vcc-2.0VDC / 50%Vcc | 45...55 | % |
| Rise- / fall time | tr / tf | 20%~80% Vout, 80%~20% Vout, max | 550.0 | ps |
| Jitter, rms, max. | J | 1σ, Fj=(12kHz...20MHz) | 1.0 | ps |
| Control voltage range | Vc | DC | 0...+3.3 | V |
| Absolute pull range, min. | APR | min. guaranteed frequency shift from fo over variations in temp., aging, power supply & load. | ±50.0 | ppm |
| Transfer gain, min/typ/max | Kv | Over the control voltage range | 150/200/300 | ppm/V |
| Freq. stability vs. temperature, typ | Δf/fc (Ta) | Ta=-40°C...+85°C, (ref. to fo) | ±150.0 | ppm |
| Settability | Vfo | Ta=+25°C ±1°C | +1.65 ± 0.35 | V |
| Linearity, max. | Δf/V | Positive slope | ± 20 | % |
| Input impedance, min. | Zin | - | 10.0 | KΩ |
| Modulation freq. bandwidth, min. | MBW(-3dB) | Vcc=+3.3VDC, Vc=+1.65VDC, Ta=+25°C, 50Ω to Vcc-2.0VDC load | 10.0 | KHz |
| Enable | En | Pin 2=High, Vcc-0.25V (min.) or "Open" | Enabled | - |
| Disable | Dis | Pin 2=Low, Vee+1.4V (max.) | Pin 5 will assume a fixed level of logic "1", and pin 6 will assume a fixed level of logic "0" | - |
| Operating temperature | Ta | - | -40...+85 | °C |
| Storage temperature | T(stg) | - | -55...+105 | °C |
| Absolute voltage ranges | Vcc, Vc(abs) | Non-destructive, DC | -0.5...+6.0 | V |

10/5/04 marketing-rfq, vcxo, WEB Spec

Note; For other Frequencies and options please consult with factory.