HZM5.6ZFA

Silicon Epitaxial Planar Zener Diode for Surge Absorb

HITACHI

ADE-208-796 (Z) Rev 0 May. 1999

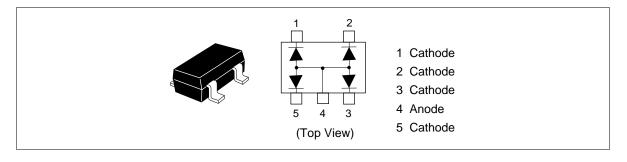
Features

- HZM5.6ZFA has four devices, and can absorb external + and -surge.
- Low capacitance (C=8.5pF max) and can protect ESD of signal line.
- MPAK-5 Package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HZM5.6ZFA	56Z	MPAK-5

Outline





HZM5.6ZFA

Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol	Value	Unit
Power dissipation	Pd *1	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note 1. Four device total, See Fig.2.

Electrical Characteristics (Ta = 25° C) *1

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Zener voltage	V _z	5.31	_	5.92	V	I _Z = 5 mA, 40ms pulse
Reverse current	I _R	_	_	0.5	μΑ	V _R = 2.5V
Capacitance	С	_	8.0	8.5	pF	$V_R = 0V, f = 1 MHz$
Dynamic resistance	r _d	_	_	80	Ω	$I_Z = 5 \text{ mA}$
ESD-Capability *2	_	8	_	_	kV	C =150pF, R = 330 Ω , Both forward and reverse direction 10 pulse

Notes 1. Per one device.

2. Failure criterion ; IR > $0.5\mu A$ at VR = 2.5V.

Main Characteristic

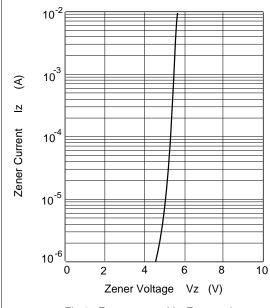


Fig.1 Zener current Vs. Zener voltage

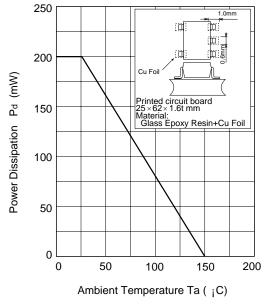


Fig.2 Power Dissipation Vs. Ambient Temperature

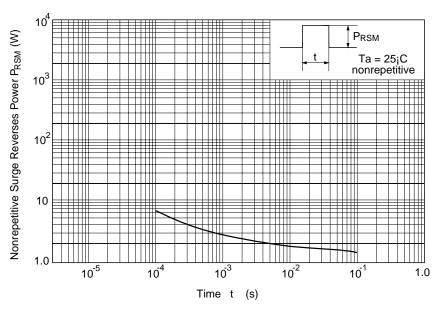
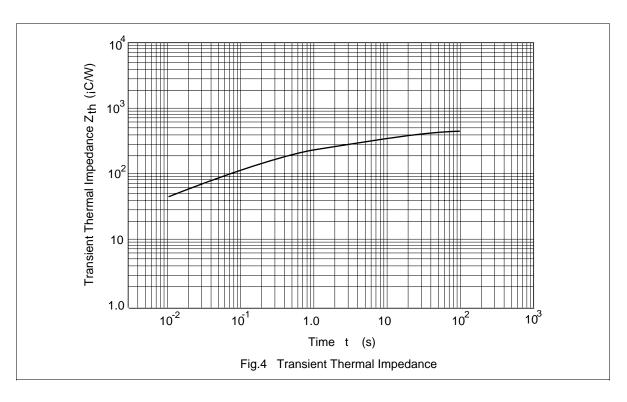


Fig.3 Surge Reverse Power Ratings

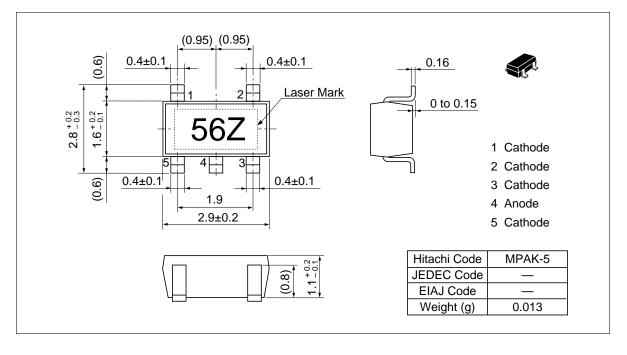
HZM5.6ZFA

Main Characteristic



Package Dimensions

Unit: mm



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