

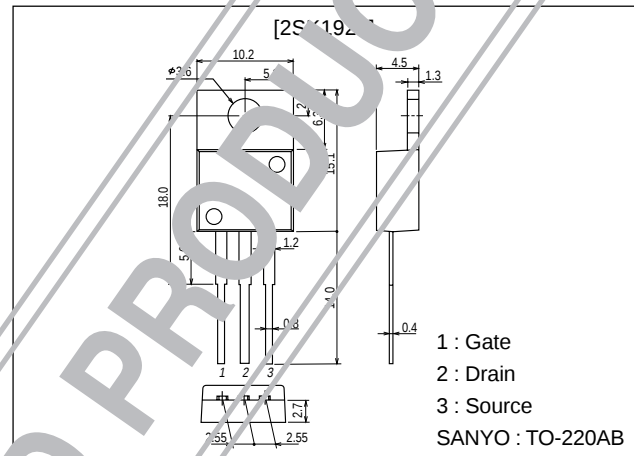
SANYO**2SK1922****Ultrahigh-Speed Switching Applications****Features**

- Low ON resistance.
- Ultrahigh-speed switching.
- High-speed diode ($t_{rr}=100\text{ns}$).

Package Dimensions

unit:mm

2052C

**Specifications****Absolute Maximum Ratings** at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DS}		600	V
Gate-to-Source Voltage	V_{GS}		± 30	V
Drain Current (DC)	I_D		2	A
Drain Current (pulse)	I_{DP}		8	A
Allowable Power Dissipation	P_D	$T_c=25^\circ\text{C}$	1.75	W
Channel Temperature	T_{ch}		50	W
Storage Temperature	T_{stg}		150	$^\circ\text{C}$
			-55 to +150	$^\circ\text{C}$

Electrical Characteristics at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DS}$	$I_D=10\text{mA}$, $V_{GS}=0$	600			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=480\text{V}$, $V_{GS}=0$			1.0	mA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 30\text{V}$, $V_{DS}=0$			± 100	nA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=10\text{V}$, $I_D=1\text{mA}$	2.0		3.0	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=10\text{V}$, $I_D=1\text{A}$	0.8	1.5		S
Static Drain-to-Source On-state Resistance	$R_{DS(on)}$	$I_D=1\text{A}$, $V_{GS}=10\text{V}$		3.2	4.3	Ω

(Note) Be careful in handling the 2SK1922 because it has no protection diode between gate and source.

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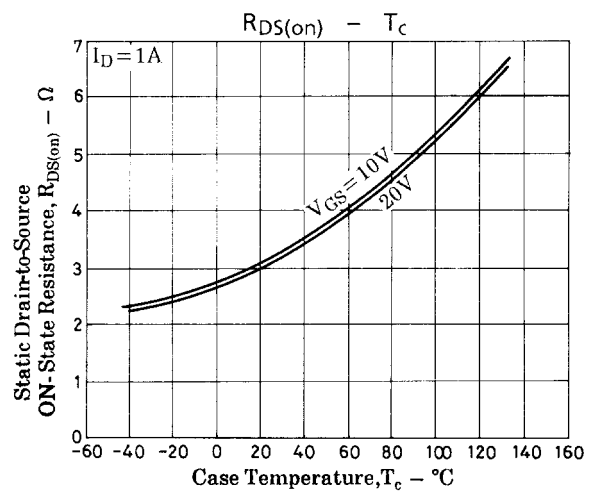
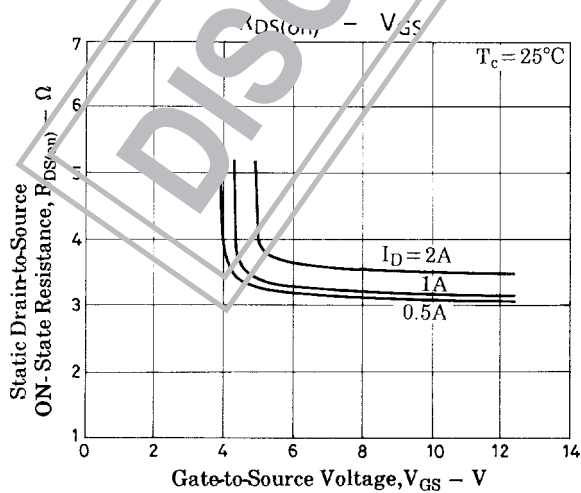
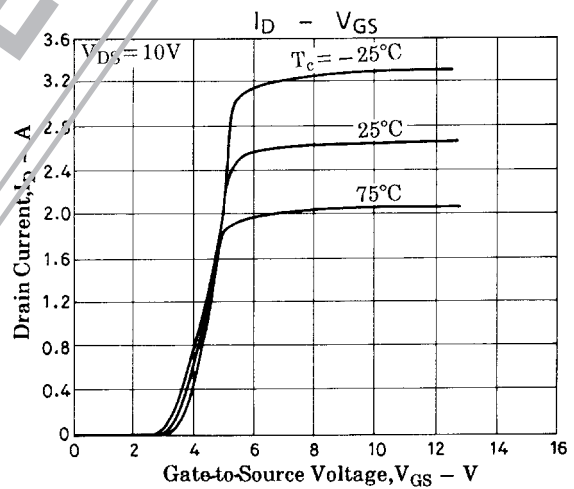
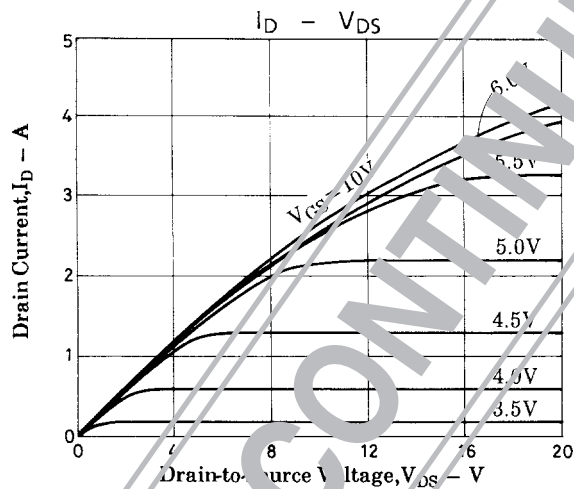
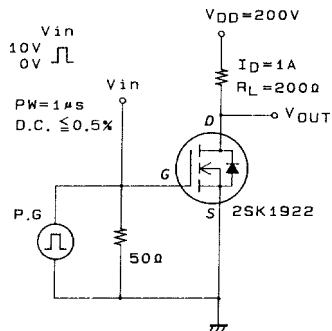
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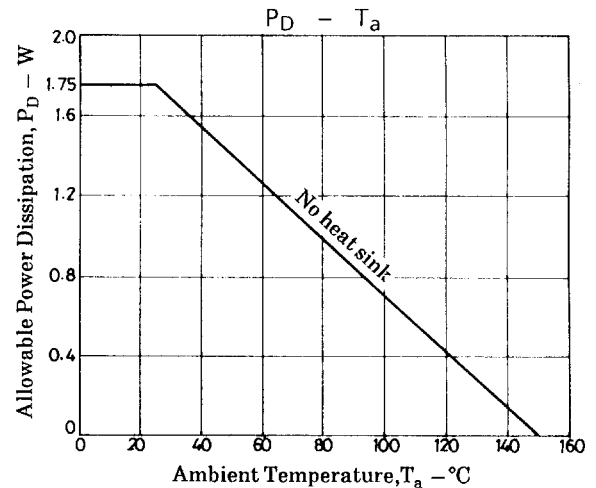
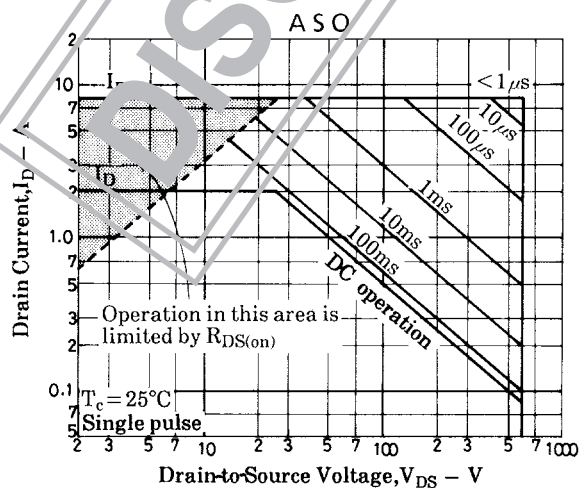
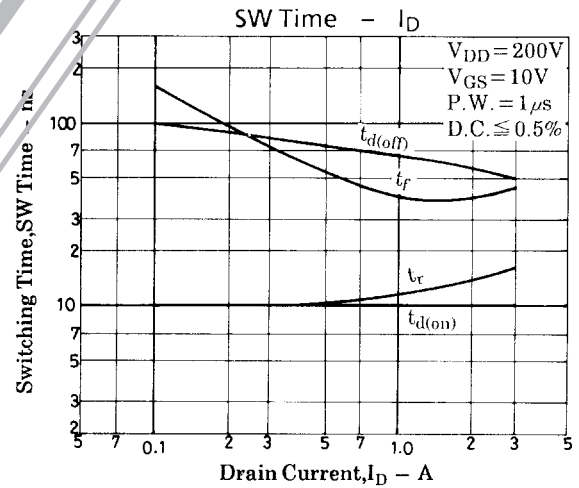
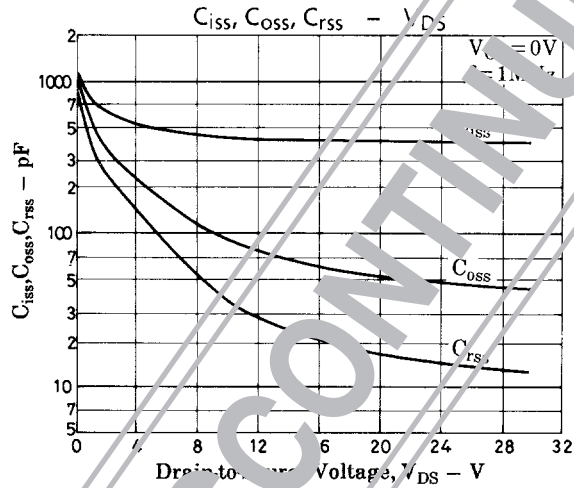
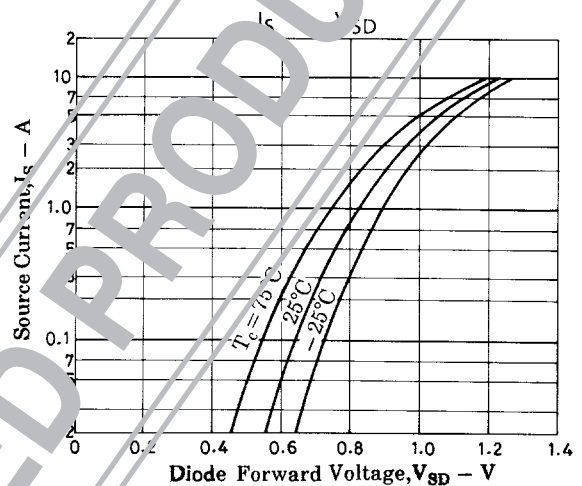
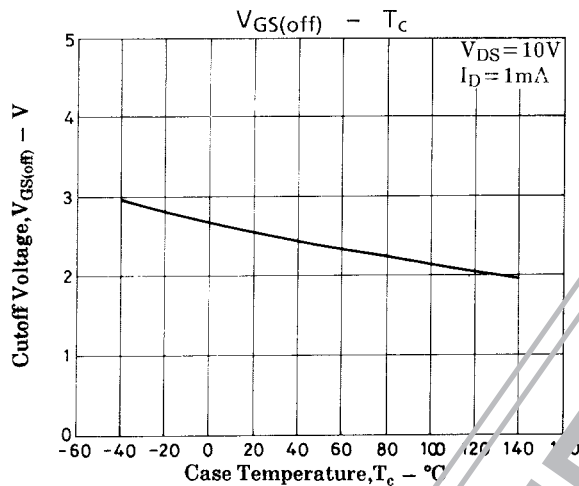
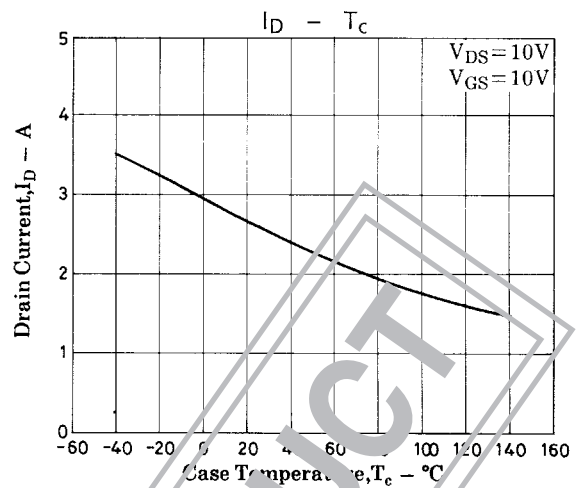
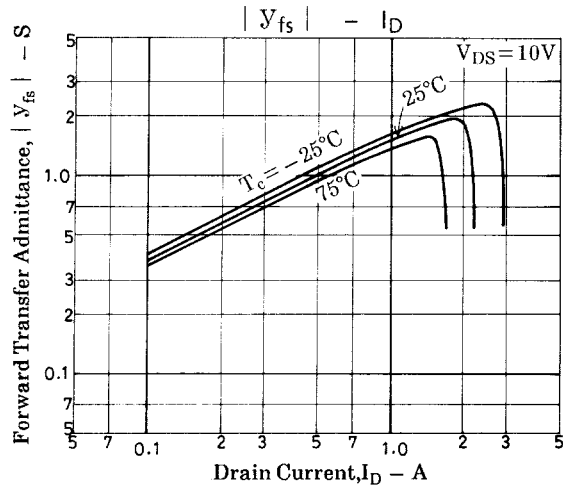
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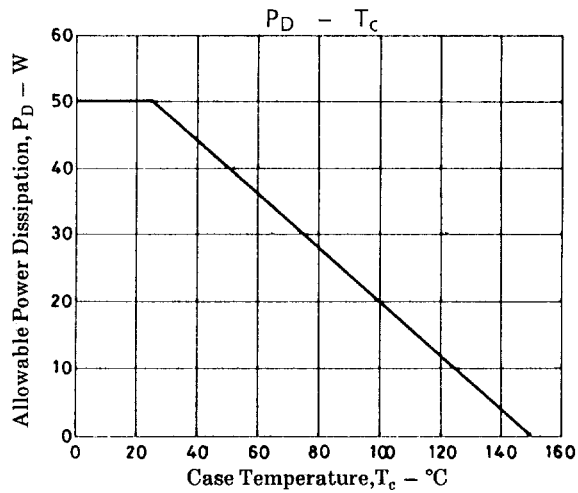
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Parameter	Symbol	Conditions	Ratings	Unit
Input Capacitance	C_{iss}	$V_{DS}=20V, f=1MHz$	400	pF
Output Capacitance	C_{oss}	$V_{DS}=20V, f=1MHz$	55	pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS}=20V, f=1MHz$	15	pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.	10	ns
Rise Time	t_r	See specified Test Circuit.	12	ns
Turn-OFF Delay Time	$t_{d(off)}$	See specified Test Circuit.	65	ns
Fall Time	t_f	See specified Test Circuit.	40	ns
Diode Forward Voltage	V_{SD}	$I_S=2A, V_{GS}=0$	1.5	V
Diode Reverse Recovery Time	t_{rr}	$I_S=2A, di/dt=100A/\mu s$	100	ns

Switching Time Test Circuit







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