

# **LB1831M**

# Low-Saturation Bidirectional Motor Driver for Low-Voltage Applications

#### Overview

The LB1831M is a dual low-saturation bidirectional motor driver IC for use in low-voltage applications. It is especially suited for use in compact low-voltage motors in portable equipment such as printer, FDD, camera.

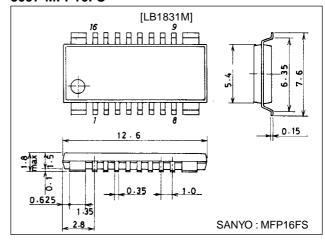
#### **Features**

- Capable of being operated from a low voltage (2.5V min)
- Low saturation voltage.
  (Upper transistor+lower transistor residual voltage 1.0V max at 400mA).
- Parallel connection avaiable.
- (Upper transistor+lower transistor residual voltage 0.5V max at 400mA).
- (Upper transistor+lower transistor residual voltage 1.0V max at 800mA).
- Logic power supply and motor power supply are separate.
- On-chip braking function.
- On-chip spark killer diodes.
- Possible to increase the internal allowable power dissipation because the package is compact (MFP-16FS) and heat can be radiated easily to the outside.

### **Package Dimensions**

unit:mm

#### 3097-MFP16FS



## **Specifications**

#### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max		-0.3 to +10	V
	V <sub>S</sub> max		-0.3 to +10	V
Output supply voltage	Vout		V <sub>S</sub> +V <sub>SF</sub>	V
Input supply voltage	V <sub>IN</sub>		-0.3 to +10	V
GND pin flow-out current	I <sub>GND</sub>	per channel	1.0	Α
Allowable power dissipation	Pd max1	IC only	900	mW
	Pd max2	Mounted on specified board (20×30×1.5mm³ glass epoxy)	1200	mW
Operating temperature	Topr		–20 to +75	°C
Storage temperature	Tstg		-40 to +125	°C

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# Allowable Operating Conditions at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	Vcc		2.5 to 9.0	V
	V <sub>S</sub>		1.8 to 9.0	V
Input high-level voltage	V <sub>IH</sub>		1.8 to 9.0	V
Input low-level voltage	V <sub>IL</sub>		-0.3 to 0.7	V

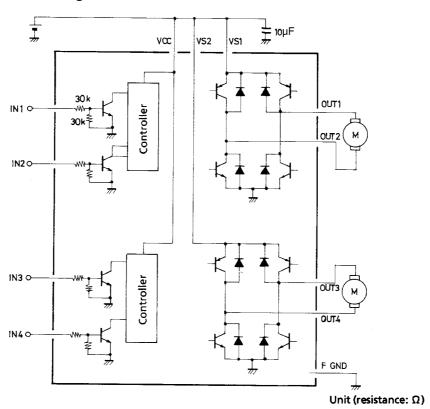
# Electrical Characteristics at Ta = 25 $^{\bullet}C, \, V_{CC} \text{=-}3V$

Parameter	Symbol	Conditions	Ratings			Unit	
Faranietei	Symbol	Conditions	min	typ	max	UIIIL	
Supply current	Icc	V <sub>IN</sub> 1, 2, 3, 4=0V, I <sub>CC</sub> +I <sub>S</sub>		0.1	10	μA	
	I <sub>CC1</sub>	V <sub>IN</sub> 1=3V, V <sub>IN</sub> 2, 3, 4=0V, I <sub>CC</sub> +I <sub>S</sub>		10	18	mA	
	I <sub>CC2</sub>	V <sub>IN</sub> 1, 2=3V, V <sub>IN</sub> 3, 4=0V, I <sub>CC</sub> +I <sub>S</sub>		20	35	mA	
Output saturation voltage (upper+lower)	V <sub>OUT</sub> 1	I <sub>OUT</sub> =200mA		0.35	0.50	V	
	V <sub>OUT</sub> 2	I <sub>OUT</sub> =400mA		0.75	1.0	V	
	V <sub>OUT</sub> 3	I <sub>OUT</sub> =400mA, parallel connection		0.4	0.55	V	
	V <sub>OUT</sub> 4	I <sub>OUT</sub> =800mA, parallel connection		0.8	1.1	V	
Output sastain voltage	V <sub>O(sus)</sub>	I <sub>OUT</sub> =400mA	9			V	
Input current	I <sub>IN</sub>	V <sub>IN</sub> =2V, V <sub>CC</sub> =6V			80	μA	
[Spark Killer Diode]							
Reverse current	I <sub>S(leak)</sub>	V <sub>CC</sub> 1, 2=9V			30	μA	
Forward voltage	V <sub>SF</sub>	I <sub>OUT</sub> =500mA			1.7	V	

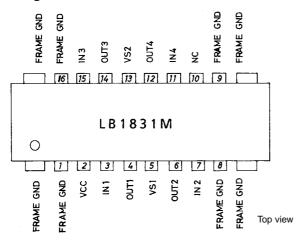
#### **Truth Table**

IN 1/3	IN 2/4	OUT 1/3	OUT 2/4	Mode
Н	L	Н	L	Forward
L	Н	L	Н	Reverse
Н	Н	L	L	Brake
L	L	OFF	OFF	Standby

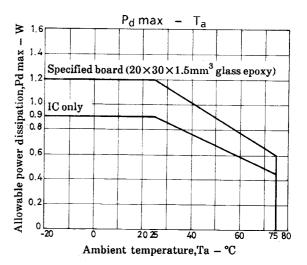
# **Equivalent Circuit Block Diagram**



#### **Pin Assignment**







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