International **TOR** Rectifier

Data Sheet No. PD10053-A

Series PVD33N

Microelectronic Power IC HEXFET® Power MOSFET Photovoltaic Relay Single-Pole, Normally-Open 0-300V DC, 240mA

General Description

The PVD33 Series DC Relay (PVD) is a single-pole, normally open, solid-state replacement for electromechanical relays used for general purpose switching of analog signals. It utilizes International Rectifier's HEXFET power MOSFET as the output switch, driven by an integrated circuit photovoltaic generator of novel construction. The output switch is controlled by radiation from a GaAIAs light emitting diode (LED), which is optically isolated from the photovoltaic generator.

The PVD33 Series overcomes the limitations of both conventional electromechanical and reed relays by offering the solid state advantages of long life, fast operating speed, low pick up power, bounce-free operation, low thermal offset voltages and miniature package. These advantages allow product improvement and design innovations in many applications such as process control, multiplexing, automatic test equipment and data acquisition.

The PVD33 can switch analog signals from thermocouple level to 300 Volts peak DC. Signal frequencies into the RF range are easily controlled and switching rates up to 500Hz are achievable. The extremely small thermally generated offset voltages allow increased measurement accuracies.

These relays are packaged in 8-pin, molded DIP packages and available with either through-hole or surface-mount ("gull-wing") leads, in plastic shipping tubes.

Applications

- Process Control
- Data Acquisition
- Test Equipment
- Multiplexing and Scanning

Features

- Bounce-Free Operation
- 10¹⁰ Off-State Resistance
- 1,000 V/µsec dv/dt
- 5 mA Input Sensitivity
- 4,000 V_{RMS} I/O Isolation
- Solid-State Reliability
- UL Recognition pending
- ESD Tolerance: 4000V Human Body Model 500V Machine Model



Part Identification

PVD2352N PVD3354N

through-hole

PVD2352NS PVD3354NS surface-mount

(gull-wing)

(HEXFET is the registered trademark for International Rectifier Power MOSFETs)

Electrical Specifications (-40°C \leq T_A \leq +85°C unless otherwise specified)

| INPUT CHARACTERISTICS | PVD2352N | PVD3354N | Units |
|--|-----------|----------|---------|
| Minimum Control Current (see figures 1 and 2) | | | DC |
| For 250mA Continuous Load Current | 2 | | mA@25°C |
| For 240mA Continuous Load Current | 5 | | mA@40°C |
| For 200mA Continuous Load Current | 5 | | mA@85°C |
| Maximum Control Current for Off-State Resistance at 25°C | 10 | | μA(DC) |
| Control Current Range (Caution: current limit input LED. See figure 6) | 2.0 to 25 | | mA(DC) |
| Maximum Reverse Voltage | 7.0 | | V(DC) |

| OUTPUT CHARACTERISTICS | PVD2352N | PVD3354N | Units |
|---|--------------------------|---------------------------|---------------------|
| Operating Voltage Range | 200 | 300 | V _(peak) |
| Maxiumum Load Current 40°C I LED 5mA | 240 | | mA(DC) |
| Response Time @25°C (see figures 7 and 8) | | | |
| Max. T(on) @ 12mA Control, 50 mA Load, 100 VDC | 100 | | μs |
| Max. T(off) @ 12mA Control, 50 mA Load, 100 VDC | 70 | | μs |
| Max. On-state Resistance 25°C (Pulsed) (fig. 4) 50 mA Load, 5mA Control | 6 | | Ω |
| Min. Off-state Resistance 25°C (see figure 5) | 10 ⁸ @ 160VDC | 10 ¹⁰ @ 240VDC | Ω |
| Max. Thermal Offset Voltage @ 5.0mA Control | 0.2 | | µvolts |
| Min. Off-State dv/dt | 1000 | | V/µs |
| Typical Output Capacitance (see figure 9) | 10 | | pF @ 50VDC |

| GENERAL CHARACTERISTICS | | (PVD2352N and PVD3354N) | Units |
|---|-----------|----------------------------------|------------------|
| Dielectric Strength: Input-Output | | 4000 | V _{RMS} |
| Insulation Resistance: Input-Output @ 90V _{DC} | | 10 ¹² @ 25°C - 50% RH | Ω |
| Maximum Capacitance: Input-Output | | 1.0 | pF |
| Max. Pin Soldering Temperature (1.6mm below seating plane, 10 seconds max.) | | +260 | |
| Ambient Temperature Range: | Operating | -40 to +85 | °C |
| | Storage | -40 to +100 | |

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Figure 2. Typical Control Current Requirements



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Figure 5. Normalized Off-State Leakage

Figure 6. Input Characteristics (Current Controlled)



Figure 7. Typical Delay Times



Figure 8. Delay Time Definitions

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Figure 9. Typical Output Capacitance

Wiring Diagram



Case Outline

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Case Outlines



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