Power Transistor (80V, 0.5A) 2SD1782K

Features

1) Low $V_{CE(sat)}$. $V_{CE(sat)} = 0.2V (Typ.)$

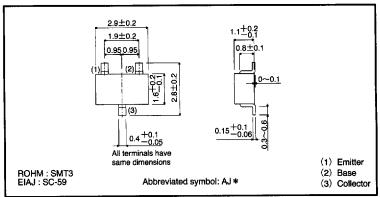
 $(I_C/I_B = 0.5A/50mA)$

2) High breakdown voltage.BV_{CEO} = 80V

3) Complements the 2SB1198K.

Structure

Epitaxial planar type NPN silicon transistor External dimensions (Units: mm)



* Denotes hre

● Absolute maximum ratings (Ta = 25°C)

| Parameter | Symbol | Limits | Unit |
|-----------------------------|--------|----------------|------|
| Collector-base voltage | Vсво | 80 | ٧ |
| Collector-emitter voltage | VCEO | 80 | V |
| Emitter-base voltage | VEBO | 5 | ٧ |
| Collector current | lc | 0.5 | Α |
| Collector power dissipation | Pc | 0.2 | W |
| Junction temperature | Tj | 150 | °C |
| Storage temperature | Tstg | −55~150 | ဗ |

●Electrical characteristics (Ta = 25°C)

| Parameter | Symbol | Min. | Тур. | Max. | Unit | Conditions |
|--------------------------------------|-------------|------|------|------|------|---------------------------|
| Collector-base breakdown voltage | ВУсво | 80 | _ | | ٧ | Ic=50 μA |
| Collector-emitter breakdown voltage | BVceo | 80 | _ | _ | ٧ | lc=2mA |
| Emitter-base breakdown voltage | ВУЕВО | 5 | _ | _ | ٧ | IE=50 μA |
| Collector cutoff current | Ісво | 1 | _ | 0.5 | μΑ | V _{CB} =50V |
| Emitter cutoff current | JEBO | | _ | 0.5 | μΑ | V _{EB} =4V |
| Collector-emitter saturation voltage | VCE(sat) | | 0.2 | 0.5 | ٧ | Ic/IB=500mA/50mA |
| DC current transfer ratio | hre | 120 | | 390 | _ | Vc=3V, lc=100mA |
| Transition frequency | fr | _ | 180 | | MHz | Vc=10V, l=-50mA, f=100MHz |
| Output capacitance | Cob | | 7.5 | _ | pF | Vcb=10V, le=0A, f=1MHz |

Packaging specifications and hre

| | | Package name | Taping |
|----------|-----------------|------------------------------|--------|
| | | Symbol | T146 |
| Туре | h _{FE} | Basic ordering unit (pieces) | 3000 |
| 2SD1782K | QR | | 0 |

hre values are classified as follows:

| Item | Q | R |
|------|---------|---------|
| hre | 120~270 | 180~390 |

●Electrical characteristic curves

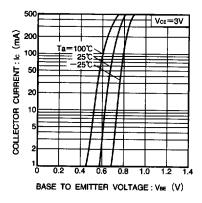


Fig.1 Grounded emitter propagation characteristics

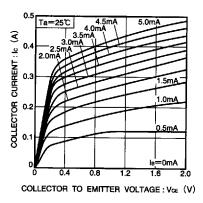
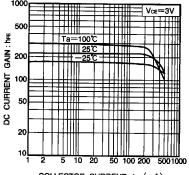


Fig.2 Grounded emitter output characteristics



COLLECTOR CURRENT : lc (mA)

Fig.3 DC current gain vs. collector current

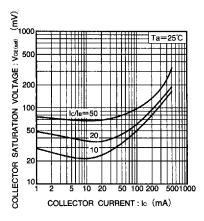


Fig.4 Collector-emitter saturation voltage vs. collector current (I)

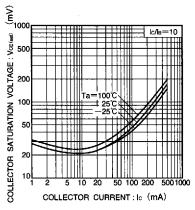


Fig.5 Collector-emitter saturation voltage vs. collector current (${\mathbb I}$)

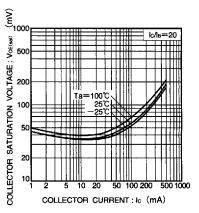


Fig.6 Collector-emitter saturation voltage vs. collector current (III)

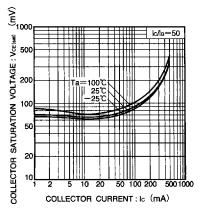


Fig.7 Collector-emitter saturation voltage vs. collector current (IV)

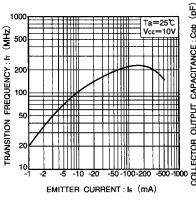


Fig.8 Gain bandwidth product vs. emitter current

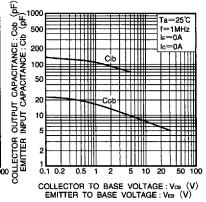
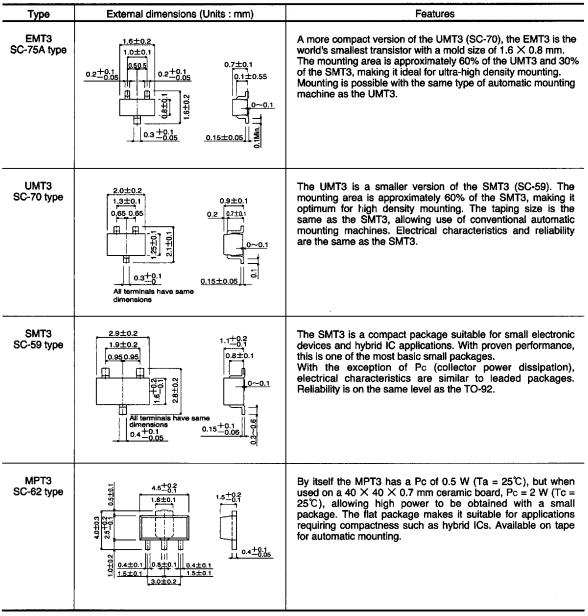


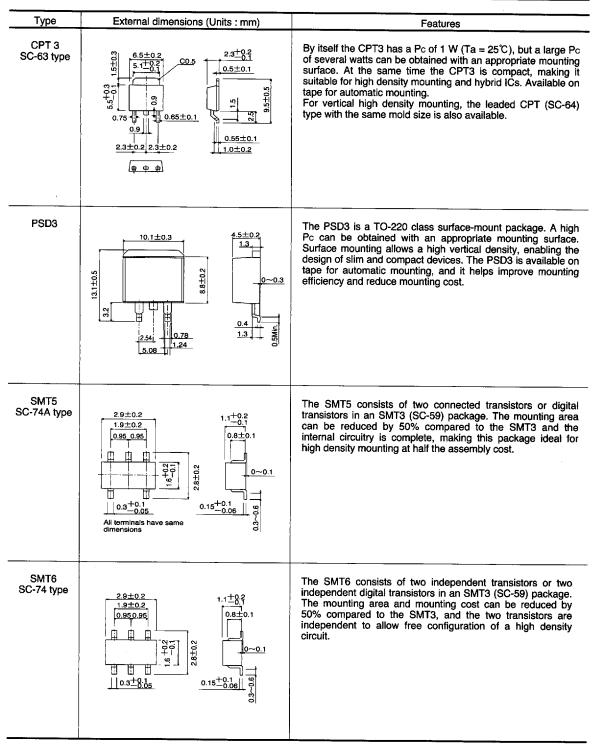
Fig.9 Collector output capacitance vs. collector-base voltage Emitter input capacitance vs. emitter-base voltage

Packages

ROHM has been manufacturing transistors since 1975. In the development of products, we constantly strive to anticipate the needs of our customers. Regarding packages, the demands of the market for compactness, low power consumption, low power dissipation and automatic mounting support are becoming ever greater, and we are strengthening our product development system to meet these needs.

Types and features of surface-mount packages

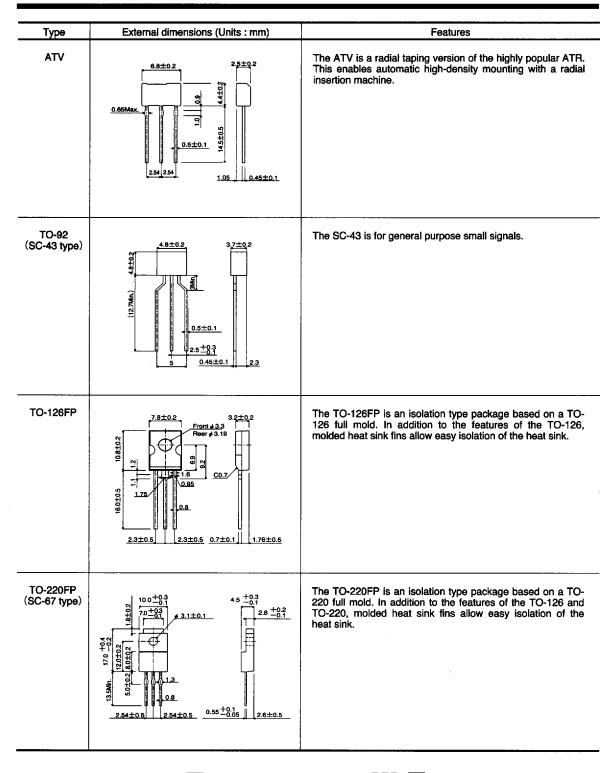




| Туре | External dimensions (Units : mm) | Features |
|---------------------|---|--|
| UMT5 SC-88A type | 2.0±0.2 1,3±0.1 0,85 0,85 0,7 0,005 All terminals have same dimensions | The UMT5 consists of two connected transistors or digital transistors in a UMT3 (SC-70) package. The mounting area can be reduced by 50% compared to the UMT3 and the internal circuitry is completed, making this package ideal for high density mounting at half the assembly cost. |
| UMT6 SC-88 type | 2.0±0.2 1,3±0.1 0.65 0.65 0.7 0.7 0.7 0.7 0.7 0.7 0.7 0.7 | The UMT6 consists of two independent transistors or two independent digital transistors in a UMT (SC-70) package. The mounting area and mounting cost can be reduced by 50% compared to the UMT3, and the two transistors are independent to allow free configuration of a high density circuit. |

●Types and features of leaded packages

| Туре | External dimensions (Units : mm) | Features |
|---------------------|---|---|
| SPT (SC-72 type) | 2±0,2 0.45±0.15 0.45±0.15 0.5 0.45±0.15 0.5 0.45±0.15 | The SPT is a smaller version of the conventional TO-92 type. The body size (3×4×2 mm³) has been reduced to 1/4 that of the TO-92 (5×5×4 mm³). The SPT is available on tape for automatic insertion, and less space is occupied on the printed circuit board than the TO-92. Reliability is the same as the TO-92. |
| FTR | 0.65±0.1 2.4±0.2 0.55±0.1 0.55±0.1 0.45±0.1 0.45±0.1 | SIL type with a height of 3.4 mm and a lead pitch of 2.54 mm. |
| FTL | 0.65Max 2.4±0.2 0.65Max 2.4±0.2 0.5±0.1 0.5±0.1 | The FTL is a radial taping version of the highly popular FTR. This enables automatic high-density mounting with a radial insertion machine. |
| ATR (SC-71 type) | 0.65Max | SC-71type with a height of 4.4 mm and a Pc=1W type. |



| Type | External dimensions (Units : mm) | Features |
|----------|----------------------------------|--|
| TO-220FN | \$3.2±0.2 | The TO-220FN features the same performance as the TO-220FP with approximately 2 mm less height, allowing the design of slimmer devices. Furthermore, the elimination of support pins in the fin (collector electrode) solves short-circuiting problems with neighboring components and the chassis. To make the height to the installation hole the same as the TO-220FP, it can be replaced as is from the TO-220FP. |