

STD790A

MEDIUM CURRENT, HIGH PERFORMANCE, LOW VOLTAGE PNP TRANSISTOR

Type	Marking		
STD790A	D790A		

- VERY LOW COLLECTOR TO EMITTER SATURATION VOLTAGE
- DC CURRENT GAIN, h_{FE} > 100
- 3 A CONTINUOUS COLLECTOR CURRENT
- 60 V BREAKDOWN VOLTAGE (V(BR)CER)
- SURFACE MOUNTING DPAK (TO-252) POWER PACKAGE IN TAPE & REEL (Suffix "T4")

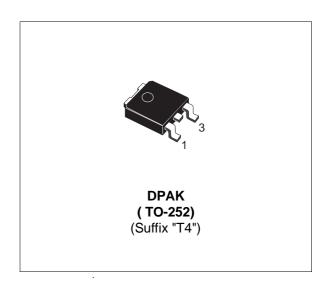
APPLICATIONS

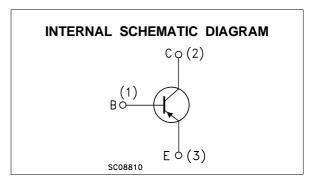
- SWITCHING REGULATOR IN BATTERY CHARGER APPLICATIONS
- SUITABLE FOR AUTOMOTIVE APPLICATIONS (V(BR)CER > 60V)
- VOLTAGE REGULATION IN BIAS SUPPLY CIRCUITS
- HEAVY LOAD DRIVER

DESCRIPTION

The device is manufactured in low voltage PNP Planar Technology by using a "Base Island" layout.

The resulting Transistor shows exceptional high gain performance coupled with very low saturation voltage.





ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage (I _E = 0)	-60	V
V_{CER}	Collector-Emitter Voltage (R _{BE} = 47Ω)	-60	V
V _В	Emitter-Base Voltage (Ic = 0)	-5	V
Ic	Collector Current	-3	Α
I _{CM}	Collector Peak Current (t _p < 5 ms)	-6	Α
P _{tot}	Total Dissipation at T _C = 25 °C	15	W
T _{stg}	Storage Temperature	-65 to 150	°C
Tj	Max. Operating Junction Temperature	150	°C

March 2003 1/6

THERMAL DATA

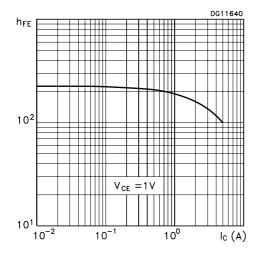
R _{thj-case} • Thermal Resistance Junction-Case	Max	8.33	°C/W]
--	-----	------	------	---

ELECTRICAL CHARACTERISTICS ($T_{case} = 25$ ^{o}C unless otherwise specified)

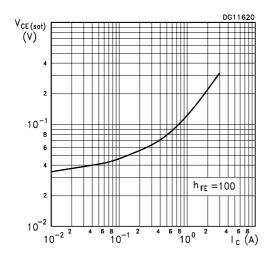
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit	
I _{CBO}	Collector Cut-off Current (I _E = 0)	$V_{CB} = -30 \text{ V}$ $V_{CB} = -30 \text{ V}$ $T_j = 100 ^{\circ}\text{C}$			-0.1 -10	μA μA	
I _{EBO}	Emitter Cut-off Current (I _C = 0)	V _{EB} = -4 V			-1	μΑ	
V _{(BR)CER*}	Collector-Emitter Breakdown Voltage (R _{BE} = 47Ω)	I _C = -10 mA	-60			V	
V _{(BR)CBO}	Collector-Base Breakdown Voltage (I _E = 0)	I _C = -100 μA	-60			V	
V _{(BR)EBO}	Emitter-Base Breakdown Voltage (I _C = 0)	I _E = -100 μA	-5			V	
V _{CE(sat)} *	Collector-Emitter	$I_{C} = -0.5A$ $I_{B} = -5mA$			-0.15	V	
, ,	Saturation Voltage	$I_C = -1A$ $I_B = -10mA$			-0.3	V	
		$I_C = -2A$ $I_B = -20mA$			-0.5	V	
		$I_{C} = -3A$ $I_{B} = -30 \text{mA}$ $I_{C} = -3A$ $I_{B} = -30 \text{mA}$ $T_{j} = 100 ^{\circ}\text{C}$			-0.7 -0.9	V V	
V _{BE(sat)*}	Base-Emitter Saturation Voltage	$I_{C} = -1 \text{ A}$ $I_{B} = -10 \text{ mA}$		-0.8	-1.0	V	
V _{BE(on)}	Base-Emitter Turn-On Voltage	$I_C = -1 A$ $V_{CE} = -2 V$		-0.8	-1	V	
h _{FE} *	DC Current Gain	$I_C = -10 \text{ mA}$ $V_{CE} = -2 \text{ V}$	100	200	300		
		$I_C = -500 \text{ mA}$ $V_{CE} = -2 \text{ V}$	100	200	300		
		$I_C = -1 A$ $V_{CE} = -2 V$	100	400			
		$I_C = -2 A$ $V_{CE} = -1 V$ $I_C = -3 A$ $V_{CE} = -1 V$	100 90	160 130			
f _T	Transition Frequency	$I_{C} = -50 \text{ mA}$ $V_{CE} = -5V$ $f = 50 \text{MHz}$	100	100		MHz	
t _d t _r t _s	RESISTIVE LOAD Delay Time RiseTime StorageTime Fall Time	$I_{C} = -3 \text{ A}$ $I_{B1} = -I_{B2} = -60 \text{ mA}$ $V_{CC} = -20 \text{ V}$ (see figure 1)		180 160 250 80	220 210 300 100	ns ns ns	

^{*} Pulsed: Pulse duration = 300 μs, duty cycle ≤ 1.5 %

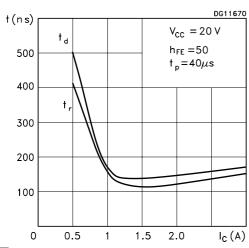
DC Current Gain



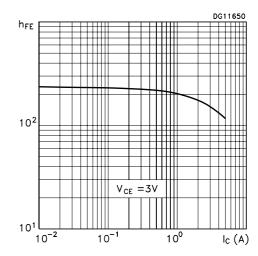
Collector-Emitter Saturation Voltage



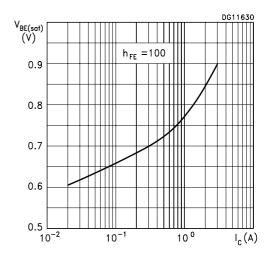
Switching Times Resistive Load



DC Current Gain



Base-Emitter Saturation Voltage



Switching Times Resistive Load

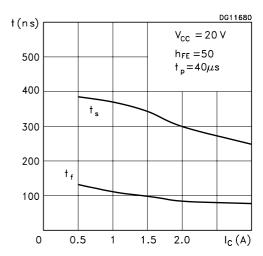
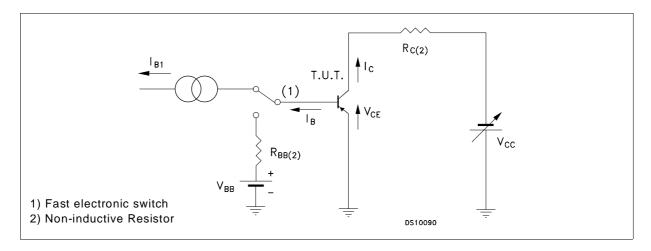
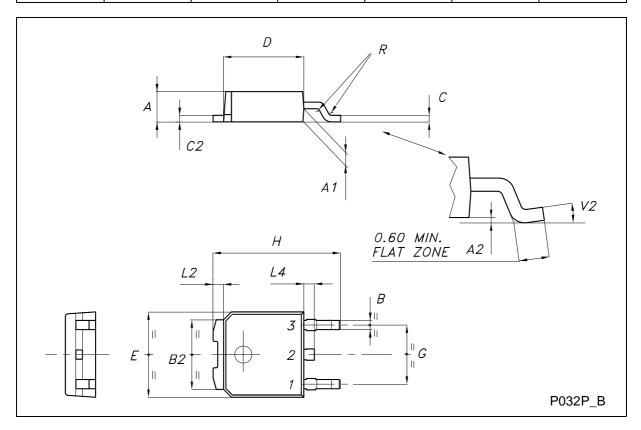


Figure 1: Resistive Load Switching Test Circuit.



TO-252 (DPAK) MECHANICAL DATA

DIM.	mm			inch			
Dilwi.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
А	2.20		2.40	0.087		0.094	
A1	0.90		1.10	0.035		0.043	
A2	0.03		0.23	0.001		0.009	
В	0.64		0.90	0.025		0.035	
B2	5.20		5.40	0.204		0.213	
С	0.45		0.60	0.018		0.024	
C2	0.48		0.60	0.019		0.024	
D	6.00		6.20	0.236		0.244	
Е	6.40		6.60	0.252		0.260	
G	4.40		4.60	0.173		0.181	
Н	9.35		10.10	0.368		0.398	
L2		0.8			0.031		
L4	0.60		1.00	0.024		0.039	
V2	0°		8°	0°		0°	



Information furnished is believed to be accurate and reliable. However, STMicroelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of STMicroelectronics. Specification mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. STMicroelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of STMicroelectronics.

The ST logo is a trademark of STMicroelectronics

© 2003 STMicroelectronics – Printed in Italy – All Rights Reserved STMicroelectronics GROUP OF COMPANIES

Australia - Brazil - Canada - China - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States.

http://www.st.com