

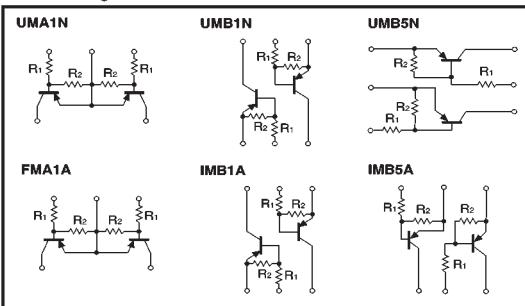
General purpose (dual digital transistors)

UMA1N / UMB1N / UMB5N / FMA1A / IMB1A / IMB5A

●Features

- 1) Two DTA124E chips in a UMT or SMT package.

●Circuit diagrams



●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	V _I (off)	—	—	-0.5	V	V _{CC} =-5V, I _O =-100 μA
	V _I (on)	-3	—	—	V	V _O =-0.2V, I _O =-5mA
Output voltage	V _O (on)	—	-0.1	-0.3	V	I _O /I _C =-0.5mA/-10mA
Input current	I _I	—	—	-0.36	mA	V _I =-5V
Output current	I _O (off)	—	—	-0.5	μA	V _{CC} =-50V, V _I =0V
DC current gain	G _I	56	—	—	—	V _O =-5V, I _O =-5mA
Input resistance	R _I	15.4	22	28.6	kΩ	—
Resistance ratio	R ₂ /R ₁	0.8	1	1.2	—	—

(96-384-A124E)

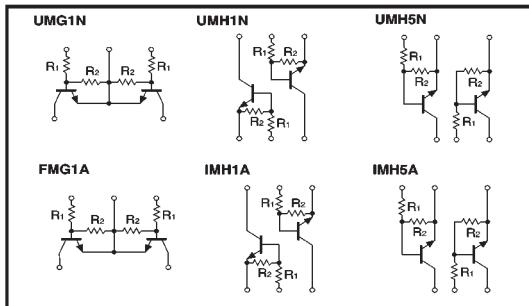
General purpose (dual digital transistors)

UMG1N / UMH1N / UMH5N / FMG1A / IMH1A / IMH5A

●Features

- 1) Two DTC124E chips in a UMT or SMT package.

●Circuit diagrams



●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	V _I (off)	—	—	0.5	V	V _{CC} =5V, I _O =100 μA
	V _I (on)	3	—	—	V	V _O =0.2V, I _O =5mA
Output voltage	V _O (on)	—	0.1	0.3	V	I _O =10mA, I _I =0.5mA
Input current	I _I	—	—	0.36	mA	V _I =5V
Output current	I _O (off)	—	—	0.5	μA	V _{CC} =50V, V _I =0V
DC current gain	G _I	56	—	—	—	V _O =5V, I _O =5mA
Input resistance	R _I	15.4	22	28.6	kΩ	—
Resistance ratio	R ₂ /R ₁	0.8	1	1.2	—	—

(94S-789-C124E)