# Switching regulator for DC / DC converters

# **BA9700A / BA9700AF / BA9700AFV**

The BA9700A, BA9700AF and BA9700AFV are switching regulators that use a pulse width modulation (PWM) system. They use a transistor switch to stabilize the output voltage.

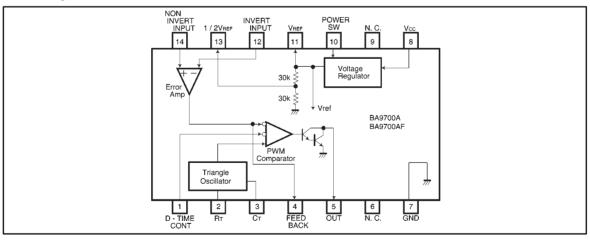
By the use of the transistor, power loss is decreased, fluctuation efficiency is improved, and the circuit is made more compact and single.

#### Features

- Output voltage can step up, step down, or invert at an arbitrary level.
- 2) Low current consumption. (typically 1.7mA)
- Wide oscillation frequency range. (typically 2.8k ~ 470kHz)
- 4) Built-in reference voltage current. (typically 2.57V)
- 5) Operates at low voltage. (minimally 3.55V)

- Dead time controller restricts operation when overloaded.
- 7) Power switch can isolate all circuits in the IC.
- 8) Best suited for battery operated equipment.
- 9) Compact 14-pin SSOP package (BA9700AFV).

#### Block diagram





# ●Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit
Power supply voltage		Vcc	24	٧
Power dissipation	BA9700A		600*1	
	BA9700AF	Pd	350*2	mW
	BA9700AFV		350*³	
Operating temperature		Topr	<b>−20~+80</b>	Ç
Storage temperature		Tstg	<b>−55∼+150</b>	Ç

<sup>\*1</sup> BA9700A: Reduced by 4.8 mW for each increase in Ta of 1℃ over 25℃.

<sup>\*2</sup> BA9700AF: Reduced by 4.4 mW for each increase in Ta of 1°C over 25°C. \*3 BA9700AFV: Reduced by 3.5 mW for each increase in Ta of 1°C over 25°C. \*2,\*3 When mounted on a 5.0×5.0×1.6 mm glass epoxy board.

•Electrical characteristics (unless otherwise noted, Ta = 25°C, Vcc = 5.0V, Rt = 10kΩ, and Ct = 330pF)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
〈Reference voltage section〉	Cymbol	IVIIII.	Typ.	IVICA.	Oilit	Conditions
Output voltage	V <sub>ref</sub>	2.48	2.57	2.66	v	I <sub>ref</sub> =0.1mA
					V	irer—U. IIIIA
1/2 V <sub>ref</sub> output voltage	1 / 2Vref	1.22	1.29	1.36	-	V 0.55V 0.0V
Line regulation	Line	_	3.00	12.0	mV	Vcc=3.55V~24V
Load regulation 1	Load1		1.00	5.00	mV	I <sub>ref</sub> =0.1mA~1.0mA
Load regulation 2	Load2	_	6.00	10.0	mV	I <sub>ref</sub> =0.1mA~8.0mA
Output voltage when power decreases	Vref	2.48	2.57	2.66	V	I <sub>ref</sub> =0.1mA, Vcc=3.55V
Output current when short-circuited	losc	_	10.0	30.0	mA	V <sub>ref</sub> =0V
〈Triangular wave oscillation section〉						
Oscillation frequency	fosc	_	230	-	kHz	Rτ=10kΩ, Cτ=330pF
Standard deviation of oscillation frequency	fosc	_	10	_	%	Rτ and Cτ are constant
Frequency variation (Vcc)	fdv	_	1	_	%	Vcc=3.55~24V
〈Error amplifier section〉						
Input offset voltage	Vio	-6.0	_	6.0	mV	Null Method
Input offset current	lю	-100	_	100	nA	Null Method
Maximum input voltage	Vice	1.60	1.90	_	V	Null Method
Open loop gain	Av	70.0	80.0	_	dB	Null Method
Common-mode rejection ratio	CMRR	70.0	80.0	_	dB	Null Method
Input bias current	Ів	_	180	500	nA	Null Method
〈PWM comparator section〉			I		I	
Threshold voltage 1	Vto	_	2.04	2.24	V	DUTY 0%
Threshold voltage 2	V <sub>1100</sub>	1.25	1.43	_	V	DUTY 100%
〈Output section〉				1	l	
Output transistor leakage current	Leak	_	_	10.0	μΑ	Vo=24V
Output saturation voltage	Vsat	_	1.70	2.10	v	lo=50mA
⟨Total device⟩		1			-	
Quiescent current	Icc	_	1.70	2.40	mA	RT=OPEN POWER SW=2.5V
Standby current	lccs	_	0	7.00	μΑ	POWER SW=0V
Operating voltage	Vcc	3.55	_	24.0	V	1 0,1211 011 01
Operating voltage	VCC	3,00		24.0	_ v	

# Timing chart

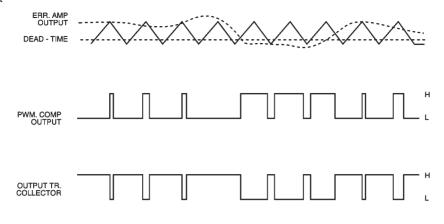


Fig.1

# Application examples

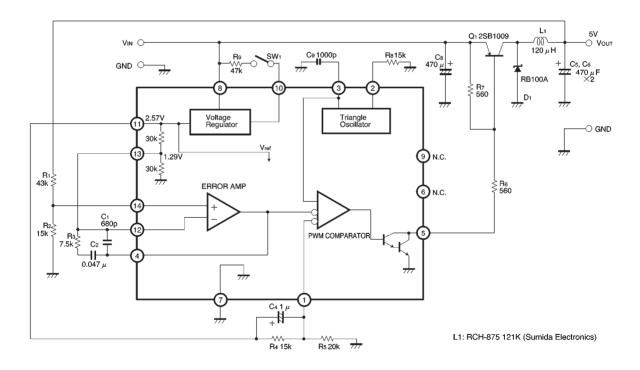


Fig.2 Step-down converter

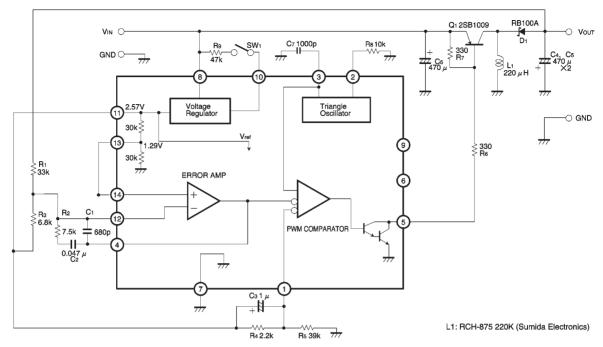


Fig.3 Inverting converter

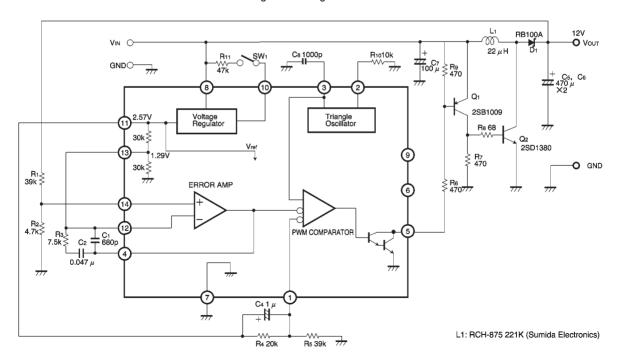
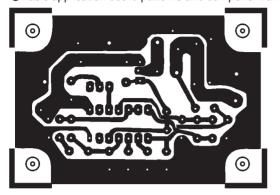


Fig.4 Step-up converter

Basic application board patterns and component arrangements



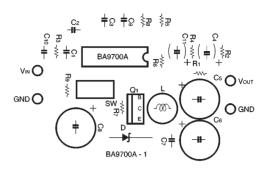
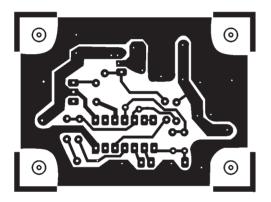


Fig.5 Step-down converter



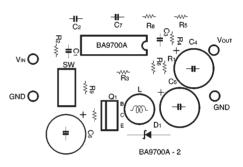
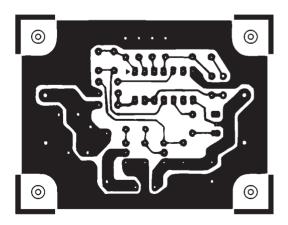


Fig.6 Inverting converter (BA9700A)



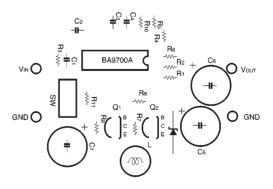


Fig.7 Step-up converter (BA9700A)

#### Electrical characteristic curves

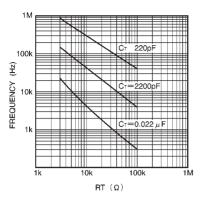


Fig.8 Oscillation frequency characteristic vs. triangular wave oscillator resistance (1)

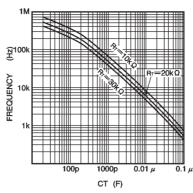


Fig.9 Oscillation frequency characteristic vs. triangular wave oscillator resistance (2)

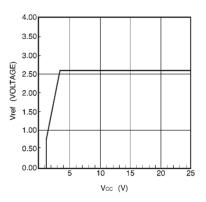


Fig.10 Output voltage vs. operating voltage for the reference voltage section

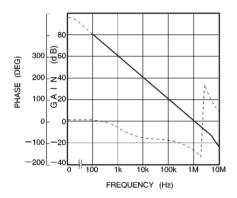


Fig.11 Phase and gain vs. frequency for the error amplifier

External dimensions (Units: mm)

