## General Description

The AAT4610 SmartSwitch ${ }^{\text {TM }}$ is a member of AnalogicTech's Application Specific Power MOSFET ${ }^{\text {TM }}$ (ASPM ${ }^{\text {TM }}$ ) product family. It is a Current Limited P-channel MOSFET power switch designed for high-side load-switching applications. This switch operates with inputs ranging from 2.7 V to 5.5 V , making it ideal for both 3 V and 5 V systems. An integrated current-limiting circuit protects the input supply against large currents which may cause the supply to fall out of regulation. The AAT4610 is also protected from thermal overload which limits power dissipation and junction temperatures. It can be used to control loads that require up to 1 A . Current limit threshold is programmed with a resistor from SET to ground. The quiescent supply current is typically a low $15 \mu \mathrm{~A}$ max. In shutdown mode, the supply current decreases to less than $1 \mu \mathrm{~A}$.

The AAT4610 is available in a 5 pin SOT-23 specified over -40 to $85^{\circ} \mathrm{C}$.

The AAT4600 Series is a family of adjustable and fixed SmartSwitch ${ }^{\text {TM }}$ with a range of current handling capabilities. Available are single versions in adj. current limit (AAT4600, AAT4601) as well as in fixed current limit (AAT4602, AAT4625). Dual versions (AAT4620, AAT4626) are also available.

## Features

## SmartSwitch ${ }^{\text {™ }}$

- 2.7 V to 5.5 V Input voltage range
- Programmable over current threshold
- Fast transient response:
- $<1 \mu \mathrm{~s}$ response to short circuit
- Low quiescent current
- $15 \mu A$ typical
- $1 \mu \mathrm{~A}$ max with Switch off
- $160 \mathrm{~m} \Omega$ typical $\mathrm{R}_{\mathrm{DS}(\mathrm{ON})}$
- Only 2.5 V needed for ON/OFF Control
- Undervoltage Lockout
- Thermal shutdown
- Temp range -40 to $85^{\circ} \mathrm{C}$
- 4kV ESD Rating
- UL Approved-File No. E217765
- 5 pin SOT-23 package


## Applications

- Peripheral ports
- Notebook computers
- Personal communication devices
- Hot swap supplies

7
UL Recognized Component

## Typical Application



## Pin Descriptions

| Pin \# | Symbol | Function |
| :---: | :---: | :--- |
| 1 | OUT | P-channel MOSFET drain. Connect $0.47 \mu$ F capacitor from OUT to GND. |
| 2 | GND | Ground connection |
| 3 | SET | Current-Limit Set Input. A resistor from SET to ground sets the current limit for <br> the switch. |
| 4 | $\overline{O N}$ | Enable Input. Two versions are available, active-high and active-low. See order- <br> ing information for details. |
| 5 | IN | P-channel MOSFET source. Connect $1 \mu \mathrm{~F}$ capacitor from IN to GND. |

## Pin Configuration

5-Pin SOT-23
(Top View)


Absolute Maximum Ratings ( $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ unless otherwise noted)

| Symbol | Description | Value | Units |
| :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\text {IN }}$ | IN to GND | -0.3 to 6 | V |
| $\mathrm{V}_{\mathrm{ON}}$ | $\overline{\mathrm{ON}}(\mathrm{ON})$ to GND | -0.3 to $\mathrm{V}_{1 \mathrm{~N}}+0.3$ | V |
| $\mathrm{V}_{\text {SET, }} \mathrm{V}_{\text {OUT }}$ | SET, OUT to GND | -0.3 to $\mathrm{V}_{\mathrm{IN}}+0.3$ | V |
| $I_{\text {MAX }}$ | Maximum Continuous Switch Current | 2 | A |
| $\mathrm{T}_{\mathrm{J}}$ | Operating Junction Temperature Range | -40 to 150 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\text {LEAD }}$ | Maximum Soldering Temperature (at Leads) | 300 | ${ }^{\circ} \mathrm{C}$ |
| $V_{\text {ESD }}$ | ESD Rating ${ }^{1}$ - HBM | 4000 | V |

Note: Stresses above those listed in Absolute Maximum Ratings may cause permanent damage to the device. Functional operation at conditions other than the operating conditions specified is not implied. Only one Absolute Maximum rating should be applied at any one time.

Note 1: Human body model is a 100 pF capacitor discharged through a $1.5 \mathrm{k} \Omega$ resistor into each pin.

## Thermal Characteristics

| Symbol | Description | Value | Units |
| :---: | :--- | :---: | :---: |
| $\Theta_{\mathrm{JA}}$ | Thermal Resistance $^{2}$ | 150 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| $\mathrm{P}_{\mathrm{D}}$ | Power Dissipation $^{2}$ | 667 | mW |

Note 2: Mounted on a demo board.

Electrical Characteristics $\left(\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=-40\right.$ to $85^{\circ} \mathrm{C}$ unless otherwise noted. Typical values are at $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ )


## Typical Characteristics

(Unless otherwise noted, $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ )

## Quiescent Current vs. Temperature



Output Current vs $\mathrm{V}_{\text {OUT }}$


Off-Supply Current vs. Temperature


## Quiescent Current


$\mathrm{R}_{\mathrm{DS}\left(\mathrm{O}^{( }\right)} \mathrm{vs}$. Temperature


Off-Switch Current vs. Temperature

(Unless otherwise noted, $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ )

Turn-ON vs. Temperature


Turn-On
$R_{L}=10 \Omega, C_{L}=0.47 \mu F$



Turn-OFF vs. Temperature


Turn-Off
$R_{L}=10 \Omega$


Short Circuit through $0.6 \Omega$

(Unless otherwise noted, $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ )



Current Limit v. Temperature


## Functional Block Diagram



## Applications Information

## Setting Current Limit

In most applications, the variation in $\mathrm{I}_{\text {LIM }}$ must be taken into account when determining $\mathrm{R}_{\text {SET. }}$. The $\mathrm{I}_{\mathrm{LIM}}$ variation is due to processing variations from part to part, as well as variations in the voltages at IN (pin 5) and OUT (pin 3), plus the operating temperature. See charts "Current Limit vs. Temperature", and "Output Current vs. $\mathrm{V}_{\text {Out }}$ ". Together these three factors add up to a $\pm 25 \%$ tolerance (see $\mathrm{I}_{\text {LIM }}$ specification in Electrical Characteristics section). In the figure below, a cold device with a statistically higher current limit, and a hot device with a statistically lower current limit, both with $R_{\text {SET }}$ equal to $10.5 \mathrm{k} \Omega$ are shown. While the chart, " $R_{\text {SET }}$ Vs. $I_{\text {LIM }}$ " indicates an $\mathrm{I}_{\text {LIM }}$ of 0.7 A with an $\mathrm{R}_{\text {SET }}$ of $10.5 \mathrm{k} \Omega$, this figure shows that the actual current limit will be at least 0.525 A , and no greater than 0.880A.

Current Limit Using $10.5 \mathrm{k} \Omega$


To determine $\mathrm{R}_{\mathrm{SET}}$, start with the maximum current drawn by the load, and multiply it by 1.33. (typical_I ${ }_{\text {LIM }}=$ minimum_I LIM $/ 0.75$ ) This is the typical current limit value. Next, refer to "R $\mathrm{RET}_{\text {VS. }}$ V. I LIM " and find the $R_{\text {SET }}$ that corresponds to the typical current limit value. Choose the largest resistor available
that is less than or equal to it. For greater precision, the value of $R_{\text {SET }}$ may also be calculated using the $l_{\text {LIM }} R_{\text {SET }}$ product found in the chart " $R_{\text {SET }}$ coefficient vs. $\mathrm{I}_{\text {LIM" }}$. The maximum current is derived by multiplying the typical current for the chosen $\mathrm{R}_{\text {SET }}$ in the chart by 1.25. A few standard resistor values are listed in the table "Current Limit $\mathrm{R}_{\text {SET }}$ Values".

## Current Limit $\mathbf{R}_{\text {SET }}$ Values

| $R_{\text {SET }}$ <br> (kS) | Current <br> Limit typ <br> $(\mathrm{mA})$ | Device will <br> not current <br> limit below: <br> $(\mathrm{mA})$ | Device always <br> current limits <br> below: <br> $(\mathrm{mA})$ |
| :---: | :---: | :---: | :---: |
| 40.2 | 200 | 150 | 250 |
| 30.9 | 250 | 188 | 313 |
| 24.9 | 300 | 225 | 375 |
| 22.1 | 350 | 263 | 438 |
| 19.6 | 400 | 300 | 500 |
| 17.8 | 450 | 338 | 563 |
| 16.2 | 500 | 375 | 625 |
| 14.7 | 550 | 413 | 688 |
| 13.0 | 600 | 450 | 750 |
| 10.5 | 700 | 525 | 875 |
| 8.87 | 800 | 600 | 1000 |
| 7.50 | 900 | 675 | 1125 |
| 6.81 | 1000 | 750 | 1250 |
| 6.04 | 1100 | 825 | 1375 |
| 5.49 | 1200 | 900 | 1500 |
| 4.99 | 1300 | 975 | 1625 |
| 4.64 | 1400 | 1050 | 1750 |

Example: A USB port requires 0.5 A . 0.5 A multiplied by 1.33 is 0.665 A . From the chart named " $\mathrm{R}_{\text {SET }}$ vs.
$\mathrm{l}_{\text {LIM }}$ ", $\mathrm{R}_{\text {SET }}$ should be less than $11 \mathrm{k} \Omega .10 .5 \mathrm{k} \Omega$ is a standard value that is a little less than $11 \mathrm{k} \Omega$ but very close. The chart reads approximately 0.700 A as a typical $\mathrm{I}_{\text {LIM }}$ value for $10.5 \mathrm{k} \Omega$. Multiplying 0.700 A by 0.75 and 1.25 shows that the AAT4610 will limit the load current to greater than 0.525A but less than 0.875 A .

## Operation in Current Limit

When a heavy load is applied to the output of the AAT4610, the load current is limited to the value of $\mathrm{I}_{\text {LIM }}$ determined by $\mathrm{R}_{\text {SET }}$. See the figure "Overload Operation". Since the load is demanding more current than $\mathrm{I}_{\text {LIM }}$, the voltage at the output drops. This causes the AAT4610 to dissipate a larger than normal quantity of power, and its die temperature to increase. When the die temperature exceeds an over temperature limit, the AAT4610 will shut down until is has cooled sufficiently, at which point it will startup again. The AAT4610 will continue to cycle on and off until the load is removed, power is removed, or until a logic high level is applied to ON (pin 4).

## Enable Input

In many systems, power planes are controlled by integrated circuits which run at lower voltages than the power plane itself. The enable input ON (pin 4) of the AAT4610 has low and high threshold voltages that accommodate this condition. The threshold voltages are compatible with 5 volt TTL, and 2.5 volt to 5 volt CMOS.

## Reverse Voltage

The AAT4610 is designed to control current flowing from IN to OUT. If a voltage is applied to OUT which is greater than the voltage on IN , large currents may flow. This could cause damage to the AAT4610.

## Overload Operation



## Ordering Information

| Package | Enable | Marking $^{1}$ | Part Number (Tape and Reel) |
| :---: | :---: | :---: | :---: |
| SOT-23-5 | $\overline{\mathrm{ON}}$ (active low) | AAXYY | AAT4610IGV-T1 |
| SOT-23-5 | ON (active high) | AWXYY | AAT4610IGV-1-T1 |

Note: Sample stock is generally held on all part numbers listed in BOLD.
Note 1: XYY = assembly and date code.

## Package Information

SOT-23-5


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