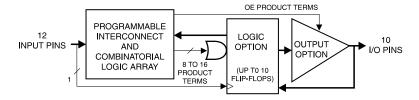
#### **Features**

- Industry Standard Architecture
  - Low-cost Easy-to-use Software Tools
- High-speed, Electrically-erasable Programmable Logic Devices
  - 7.5 ns Maximum Pin-to-pin Delay
- Several Power Saving Options

Device	I <sub>CC</sub> , Standby	I <sub>CC</sub> , Active
ATF22V10B	85 mA	90 mA
ATF22V10BQ	35 mA	40 mA
ATF22V10BQL	5 mA	20 mA

- CMOS and TTL Compatible Inputs and Outputs
  - Input and I/O Pull-up Resistors
- Advanced Flash Technology
  - Reprogrammable
  - 100% Tested
- High-reliability CMOS Process
  - 20-year Data Retention
  - 100 Erase/Write Cycles
  - 2,000V ESD Protection
  - 200 mA Latchup Immunity
- Full Military, Commercial, and Industrial Temperature Ranges
- Dual-in-line and Surface Mount Packages in Standard Pinouts
- PCI Compliant

#### **Logic Diagram**



# **Pin Configurations**

#### All Pinouts Top View

Pin Name	Function
CLK	Clock
IN	Logic Inputs
I/O	Bidirectional Buffers
*	No Internal Connection
V <sub>CC</sub>	+5V Supply

#### **TSSOP**

CLK/IN	1	24	□ vcc
IN 🗀	$_{2}\bigcirc$	23	□ I/O
IN 🗀	3	22	□ I/O
IN 🗀	4	21	□ I/O
IN 🗀	5	20	□ I/O
IN 🗀	6	19	□ I/O
IN 🗀	7	18	□ I/O
IN 🗀	8	17	□ I/O
IN 🗀	9	16	□ I/O
IN 🗀	10	15	□ I/O
IN 🗀	11	14	□ I/O
GND	12	13	□IN

#### DIP/SOIC

				_				_	_	
		7			Σ					
CLK/IN 🗆	1 2	4 □ VCC	z				8	Q	0	
IN 🗆	2 2	3 🗖 1/0	=	=	디	*	2	_	_	
IN 🗆	3 2	2 🗖 1/0	4	8	7	-	28	27	9	1
IN□	4 2	1 🗀 1/0	IN 🗆 5			0	N	N	9 25	Þ 1/0
IN□	5 2	0   1/0	IN ☐ 6						24	□ I/O
IN 🗆	6 1	9 🗖 1/0	IN 🗆 7						23	□ I/O
IN 🗆	7 1	в 🗖 1/0	* □ 8						22	□*
IN 🗆	8 1	7 🗖 1/0	IN □ 9						21	□ I/O
IN□	9 1	6 <b> </b> 1/O	IN 🗆 10						20	□ I/O
IN□	10 1	5 🗖 1/0	IN 🗆 11 🗸	~	4	ıo	(0	_	<sub>m</sub> 19	□ I/O
IN 🗆	11 1-	4 🗖 1/0	L	÷	÷	~	Ŧ	÷	8	]
GND [	12 1:	3 □ IN	Z	Z		*	z	0	0	
			_	_	GND		_	>	>	

LCC/PLCC



# Highperformance EE PLD

# ATF22V10BQ ATV22V10BQL

Rev. 0250K-03/01





# **Description**

The ATF22V10B is a high-performance CMOS (electrically-erasable) programmable logic device (PLD) which utilizes Atmel's proven electrically-erasable Flash memory technology. Speeds down to 7.5 ns and power dissipation as low as 10 mA are offered. All speed ranges are specified over the full  $5V \pm 10\%$  range for military and industrial

temperature ranges, and 5V  $\pm$  5% for commercial temperature ranges.

Several low-power options allow selection of the best solution for various types of power-limited applications. Each of these options significantly reduces total system power and enhances system reliability.

### **Absolute Maximum Ratings\***

Temperature Under Bias55°C to +125°C
Storage Temperature65°C to +150°C
Voltage on Any Pin with Respect to Ground2.0V to +7.0V <sup>(1)</sup>
Voltage on Input Pins with Respect to Ground During Programming2.0V to +14.0V <sup>(1)</sup>
Programming Voltage with Respect to Ground2.0V to +14.0V <sup>(1)</sup>

\*NOTICE:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Note:

Minimum voltage is -0.6V DC, which may undershoot to -2.0V for pulses of less than 20 ns.
 Maximum output pin voltage is V<sub>CC</sub> + 0.75V DC, which may overshoot to 7.0V for pulses of less than 20 ns.

# **DC and AC Operating Conditions**

	Commercial	Industrial	Military
Operating Temperature	0°C - 70°C (Ambient)	-40°C - 85°C (Ambient)	-55°C - 125°C (Case)
V <sub>CC</sub> Power Supply	5V ± 5%	5V ± 10%	5V ± 10%

Note: 1. The shaded devices are obsolete.

# **DC Characteristics**

Symbol	Parameter	Condition			Min	Тур	Max	Units
I <sub>IL</sub>	Input or I/O Low Leakage Current	$0 \le V_{IN} \le V_{IL}$ (Max)			-35	-100	μΑ	
I <sub>IH</sub>	Input or I/O High Leakage Current	$3.5 \le V_{IN} \le V_{CC}$					10	μΑ
	Power Supply Current,	V <sub>CC</sub> = Max,	<sub>CC</sub> = Max, Com.			85	120	mA
I <sub>cc</sub>	Standby	V <sub>IN</sub> = Max, Outputs Open	B-7	Ind., Mil.		85	140	mA
			B-10	Com./Ind.		85/85	120/140	mA
			D-10	Mil.		85	140	mA
			B-15	Com./Ind.		65/65	90/115	mA
		V <sub>CC</sub> = Max,	D-10	Mil.		65	115	mA
I <sub>CC</sub>	Power Supply Current, Standby	$V_{IN} = Max$ ,	B-25	Com.		65	90	mA
	,	Outputs Open	D-20	Ind., Mil.		65	115	mA
			BQ-15	Com.		35	55	mA
			DOI 00 05	Com.		5	10	mA
			BQL-20, -25	Ind., Mil.		5	15	mA
		V <sub>CC</sub> = Max, Outputs Open,	B-7	Com.		90	120	mA
				Mil., Ind.		90	145	mA
			B-10	Com./Ind.		90/90	120/145	mA
				Mil.		90	145	mA
			B-15	Com./Ind.		65/65	90/120	mA
I <sub>CC2</sub>	Clocked Power Supply Current			Mil.		65	120	mA
	Cupply Culton	f = 15 MHz	B-25	Com.		65	90	mA
				Ind., Mil.		65	120	mA
			BQ-15	Com.		40	60	mA
			DOI 00 05	Com.		20	50	mA
			BQL-20, -25	Ind., Mil.		20	70	mA
I <sub>OS</sub> <sup>(1)</sup>	Output Short Circuit Current	V <sub>OUT</sub> = 0.5V					-130	mA
V <sub>IL</sub>	Input Low Voltage				-0.5		0.8	V
V <sub>IH</sub>	Input High Voltage				2.0		V <sub>CC</sub> + 0.75	V
	0 1 11 1/1	$V_{IN} = V_{IH} \text{ or } V_{IL},$	I <sub>OL</sub> = 16 mA	Com., Ind.			0.5	V
$V_{OL}$	Output Low Voltage	$V_{CC} = Min$	I <sub>OL</sub> = 12 mA	Mil.			0.5	V
V <sub>OH</sub>	Output High Voltage	$V_{IN} = V_{IH} \text{ or } V_{IL},$ $V_{CC} = \text{Min}$	I <sub>OH</sub> = -4.0 mA		2.4			٧

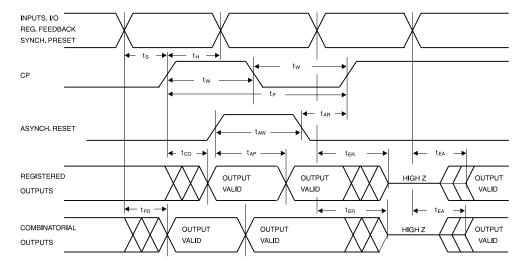
Notes: 1. Not more than one output at a time should be shorted. Duration of short circuit test should not exceed 30 sec.



<sup>2.</sup> The shaded devices are obsolete.



# **AC** Waveforms<sup>(1)</sup>



Note: 1. Timing measurement reference is 1.5V. Input AC driving levels are 0.0V and 3.0V, unless otherwise specified.

# **AC** Characteristics<sup>(1)</sup>

		-	7	-	10	-15		-20		-25		
Symbol	Parameter	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Units
t <sub>PD</sub>	Input or Feedback to Combinatorial Output	3	7.5	3	10	3	15	3	20	3	25	ns
t <sub>CO</sub>	Clock to Output	2	4.5 <sup>(2)</sup>	2	6.5	2	8	2	12	2	15	ns
$t_{CF}$	Clock to Feedback		2.5		2.5		2.5		8		13	ns
t <sub>S</sub>	Input or Feedback Setup Time	3.5		4.5		10			14	15		ns
t <sub>H</sub>	Hold Time	0		0		0		0		0		ns
	External Feedback 1/(t <sub>S</sub> + t <sub>CO</sub> )	125 <sup>(3)</sup>		90		55.5		38.5		33.3		MHz
$f_{MAX}$	Internal Feedback 1/(t <sub>S</sub> + t <sub>CF</sub> )	166		142		69		45.5		40		MHz
	No Feedback 1/(t <sub>WH</sub> + t <sub>WL</sub> )	166		142		83.3				38.5		MHz
$t_W$	Clock Width (t <sub>WL</sub> and t <sub>WH</sub> )	3		3.5		6		10		13		ns
t <sub>EA</sub>	Input or I/O to Output Enable	3	7.5	3	10	3	15	3	20	3	25	ns
t <sub>ER</sub>	Input or I/O to Output Disable	3	7.5	3	9	3	15	3	20	3	25	ns
t <sub>AP</sub>	Input or I/O to Asynchronous Reset of Register	3	10	3	12	3	20	3	22	3	25	ns
t <sub>AW</sub>	Asynchronous Reset Width	7		8		15		20		25		ns
t <sub>AR</sub>	Asynchronous Reset Recovery Time	5		6		10		20		25		ns
t <sub>SP</sub>	Setup Time, Synchronous Preset	4.5		6		10		14		15		ns
t <sub>SPR</sub>	Synchronous Preset to Clock Recovery Time	5		8		10		14		15		ns

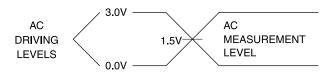
Notes: 1. See ordering information for valid part numbers.

- 2. 5.5 ns for DIP package devices.
- 3. 111 MHz for DIP package devices.
- 4. The shaded devices are obsolete.

# ATF22V10B(Q)(L)

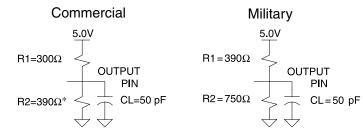
4

# Input Test Waveforms and Measurement Levels



 $t_R$ ,  $t_F < 3$  ns

#### **Output Test Loads**



<sup>\*</sup> All except -7 which is R2 =  $300\Omega$ 

#### Pin Capacitance

 $f = 1 \text{ MHz}, T = 25^{\circ}C^{(1)}$ 

	Тур	Max	Units	Conditions
C <sub>IN</sub>	5	8	pF	V <sub>IN</sub> = 0V
C <sub>OUT</sub>	6	8	pF	V <sub>OUT</sub> = 0V

Note: 1. Typical values for nominal supply voltage. This parameter is only sampled and is not 100% tested.

# **Power-up Reset**

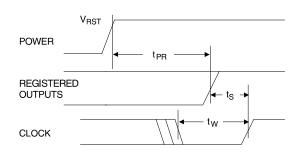
The registers in the ATF22V10Bs are designed to reset during power-up. At a point delayed slightly from  $V_{CC}$  crossing  $V_{RST}$ , all registers will be reset to the low state. The output state will depend on the polarity of the output buffer.

This feature is critical for state machine initialization. However, due to the asynchronous nature of reset and the uncertainty of how  $V_{\rm CC}$  actually rises in the system, the following conditions are required:

- 1. The V<sub>CC</sub> rise must be monotonic,
- 2. After reset occurs, all input and feedback setup times must be met before driving the clock pin high, and
- 3. The clock must remain stable during tpg.

# **Preload of Registered Outputs**

The ATF22V10B's registers are provided with circuitry to allow loading of each register with either a high or a low. This feature will simplify testing since any state can be forced into the registers to control test sequencing. A JEDEC file with preload is generated when a source file with vectors is compiled. Once downloaded, the JEDEC file preload sequence will be done automatically by most of the approved programmers after the programming.



Parameter	Description Typ		Max	Units
t <sub>PR</sub>	Power-up Reset Time	600	1,000	ns
V <sub>RST</sub>	Power-up Reset Voltage	3.8	4.5	V

# **Security Fuse Usage**

A single fuse is provided to prevent unauthorized copying of the ATF22V10B fuse patterns. Once programmed, fuse verify and preload are inhibited. However, the 64-bit User Signature remains accessible.

The security fuse should be programmed last, as its effect is immediate.





#### **Electronic Signature Word**

There are 64 bits of programmable memory that are always available to the user, even if the device is secured. These bits can be used for user-specific data.

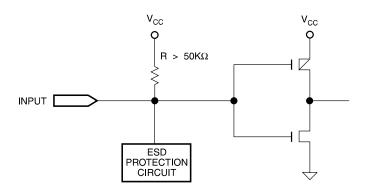
#### **Programming/Erasing**

Programming/erasing is performed using standard PLD programmers. See *CMOS PLD Programming Hardware and Software Support* for information on software/programming.

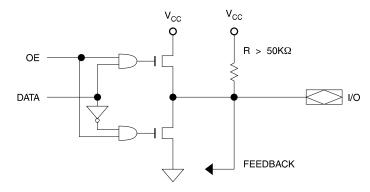
#### Input and I/O Pull-ups

All ATF22V10B family members have internal input and I/O pull-up resistors. Therefore, whenever inputs or I/Os are not being driven externally, they will float to  $V_{CC}$ . This ensures that all logic array inputs are at known states. These are relatively weak active pull-ups that can easily be overdriven by TTL-compatible drivers (see input and I/O diagrams below).

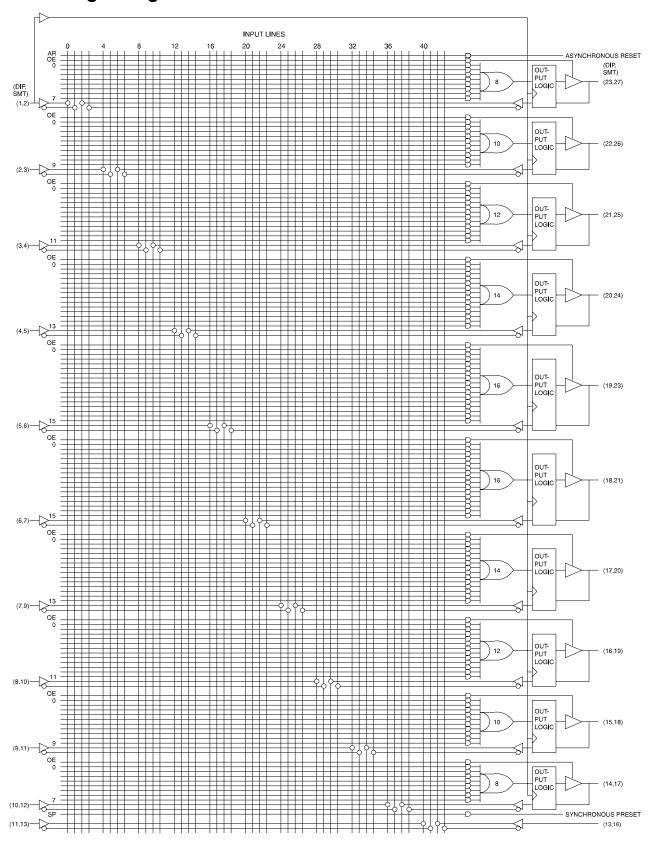
### **Input Diagram**



### I/O Diagram



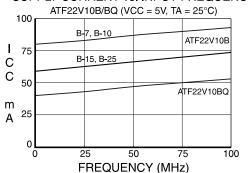
# **Functional Logic Diagram ATF22V10B**

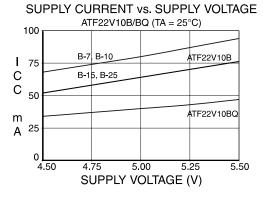




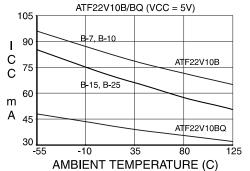


#### SUPPLY CURRENT vs. INPUT FREQUENCY

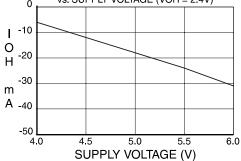




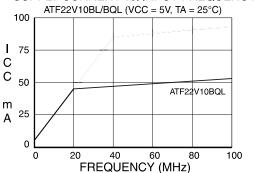
#### SUPPLY CURRENT vs. AMBIENT TEMPERATURE



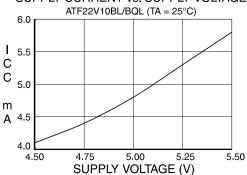
# OUTPUT SOURCE CURRENT vs. SUPPLY VOLTAGE (VOH = 2.4V)



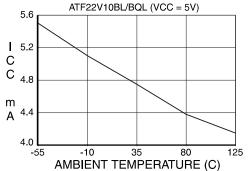
#### SUPPLY CURRENT vs. INPUT FREQUENCY



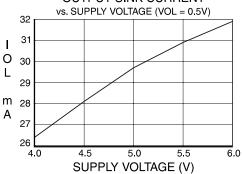
SUPPLY CURRENT vs. SUPPLY VOLTAGE

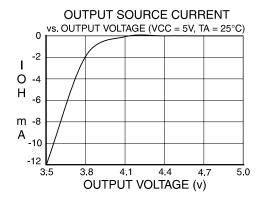


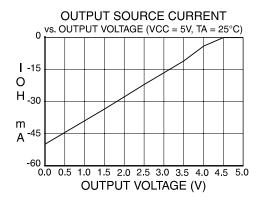
SUPPLY CURRENT vs. AMBIENT TEMPERATURE

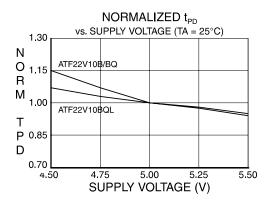


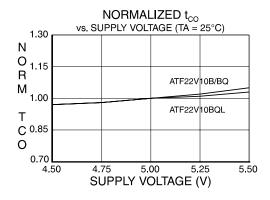
OUTPUT SINK CURRENT

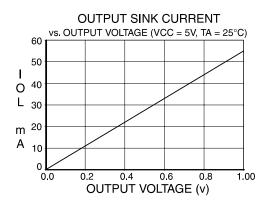


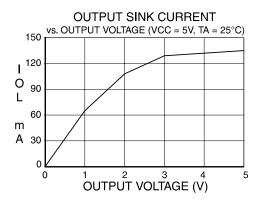


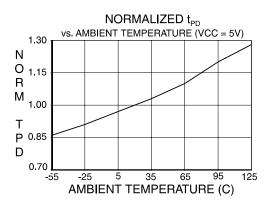


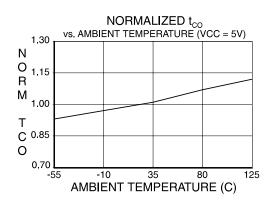






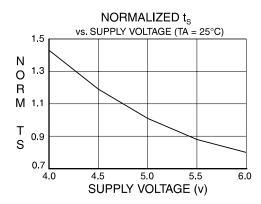


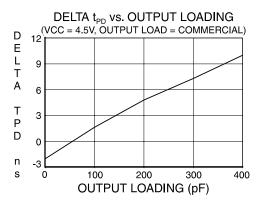


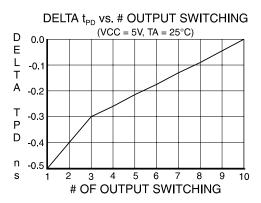


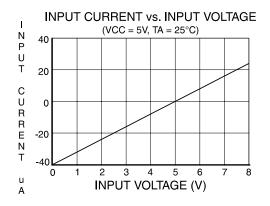


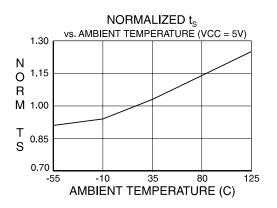
<u>AIMEL</u>

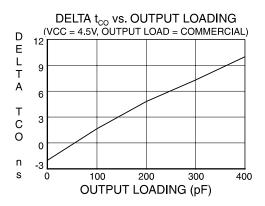


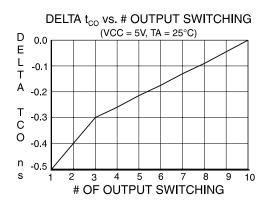


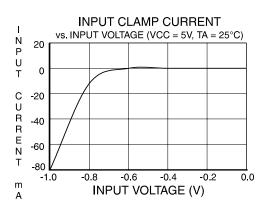












# **ATF22V10B Ordering Information**

7.5 3.5			Package	Operation Range
7.5 0.5	4.5	ATF22V10B-7JC <sup>(1)</sup> ATF22V10B-7PC <sup>(1)</sup> ATF22V10B-7SC <sup>(1)</sup> ATF22V10B-7XC <sup>(1)</sup>	28J 24P3 24S 24X	Commercial (0°C to 70°C)
10 4.5	6.5	ATF22V10B-10JC <sup>(1)</sup> ATF22V10B-10PC <sup>(1)</sup> ATF22V10B-10SC <sup>(1)</sup> ATF22V10B-10XC <sup>(1)</sup>	28J 24P3 24S 24X	Commercial (0°C to 70°C)
		ATF22V10B-10JI <sup>(1)</sup> ATF22V10B-10PI <sup>(1)</sup> ATF22V10B-10SI <sup>(1)</sup> ATF22V10B-10XI <sup>(1)</sup>	28J 24P3 24S 24X	Industrial (-40°C to 85°C)
		ATF22V10B-10GM/883 ATF22V10B-10NM/883	24D3 28L	Military/883C (-55°C to 125°C) Class B, Fully Compliant
		5962-89841 06LA 5962-89841 063X	24D3 28L	Military (-55°C to 125°C) Class B, Fully Compliant
15 10	8	ATF22V10B-15JC <sup>(1)</sup> ATF22V10B-15PC <sup>(1)</sup> ATF22V10B-15SC <sup>(1)</sup> ATF22V10B-15XC <sup>(1)</sup>	28J 24P3 24S 24X	Commercial (0°C to 70°C)
		ATF22V10B-15JI <sup>(1)</sup> ATF22V10B-15PI <sup>(1)</sup> ATF22V10B-15SI <sup>(1)</sup> ATF22V10B-15XI <sup>(1)</sup>	28J 24P3 24S 24X	Industrial (-40°C to 85°C)
		ATF22V10B-15GM/883 ATF22V10B-15NM/883	24D3 28L	Military/883C (-55°C to 125°C) Class B, Fully Compliant
		5962-89841 03LA 5962-89841 033X	24D3 28L	Military (-55°C to 125°C) Class B, Fully Compliant
25 15	15	ATF22V10B-25JC ATF22V10B-25PC ATF22V10B-25SC ATF22V10B-25XC	28J 24P3 24S 24X	Commercial (0°C to 70°C)
Notes: 1. Recommend		ATF22V10B-25JI ATF22V10B-25PI ATF22V10B-25SI ATF22V10B-25XI	28J 24P3 24S 24X	Industrial (-40°C to 85°C)

Notes: 1. Recommend ATF22V10C versions.



<sup>2.</sup> The shaded devices are obsolete.



# ATF22V10BQ(L) Ordering Information

t <sub>PD</sub> (ns)	t <sub>s</sub> (ns)	t <sub>co</sub> (ns)	Ordering Code	Package	Operation Range
15	10	8	ATF22V10BQ-15JC <sup>(1)</sup>	28J	Commercial
			ATF22V10BQ-15PC <sup>(1)</sup>	24P3	(0°C to 70°C)
			ATF22V10BQ-15SC <sup>(1)</sup>	24S	
			ATF22V10BQ-15XC <sup>(1)</sup>	24X	
20	14	12	ATF22V10BQL-20JC <sup>(1)</sup>	28J	Commercial
			ATF22V10BQL-20PC <sup>(1)</sup>	24P3	(0°C to 70°C)
			ATF22V10BQL-20SC <sup>(1)</sup>	24S	
			ATF22V10BQL-20XC <sup>(1)</sup>	24X	
			ATF22V10BQL-20JI <sup>(1)</sup>	28J	Industrial
			ATF22V10BQL-20PI <sup>(1)</sup>	24P3	(-40°C to 85°C)
			ATF22V10BQL-20SI <sup>(1)</sup>	24S	
			ATF22V10BQL-20XI <sup>(1)</sup>	24X	
			ATF22V10BQL-20GM/883	24D3	Military/883C
			ATF22V10BQL-20NM/883	28L	(-55°C to 125°C)
					Class B, Fully Compliant
			5962-89841 14 LA	24D3	Military
			5962-89841 14 3X	28L	(-55°C to 125°C)
					Class B, Fully Compliant
25	15	15	ATF22V10BQL-25JC	28J	Commercial
			ATF22V10BQL-25PC	24P3	(0°C to 70°C)
			ATF22V10BQL-25SC	24S	
			ATF22V10BQL-25XC	24X	
			ATF22V10BQL-25JI	28J	Industrial
			ATF22V10BQL-25PI	24P3	(-40°C to 85°C)
			ATF22V10BQL-25SI	24S	
			ATF22V10BQL-25XI	24X	
			ATF22V10BQL-25GM/883	24D3	Military/883C
			ATF22V10BQL-25NM/883	28L	(-55°C to 125°C)
					Class B, Fully Compliant
			5962-89841 13 LA	24D3	Military
			5962-89841 13 3X	28L	(-55°C to 125°C)
					Class B, Fully Compliant

Notes: 1. Recommend ATF22V10CQ and ATF2240CQZ

2. The shaded devices are obsolete.

# **Using "C" Product for Industrial**

To use commercial product for Industrial temperature ranges, down-grade one speed grade from the "I" to the "C" device (7 ns "C" = 10 ns "I") and de-rate power by 30%.

Package Type	
24D3	24-pin, 0.300" Wide, Ceramic Dual Inline Package (Cerdip)
28J	28-lead, Plastic J-leaded Chip Carrier (PLCC)
28L	28-pad, Ceramic Leadless Chip Carrier (LCC)
24P3	24-pin, 0.300" Wide, Plastic Dual Inline Package (PDIP)
24S	24-lead, 0.300" Wide, Plastic Gull Wing Small Outline (SOIC)
24X	24-lead, 4.4 mm Wide, Plastic Thin Shrink Small Outline (TSSOP)

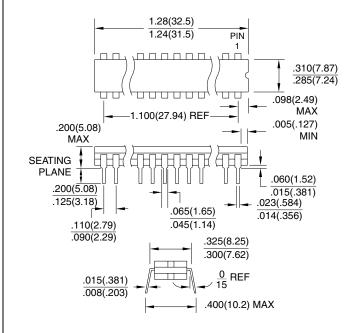




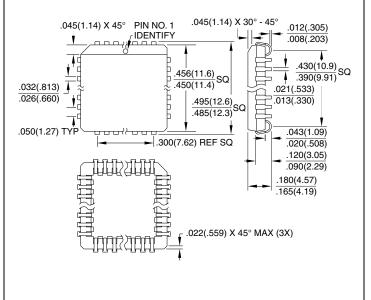
#### **Packaging Information**

24D3, 24-pin, 0.300" Wide, Non-windowed, Ceramic Dual Inline Package (Cerdip) Dimensions in Inches and (Millimeters)

MIL-STD-1835 D-9 CONFIG A



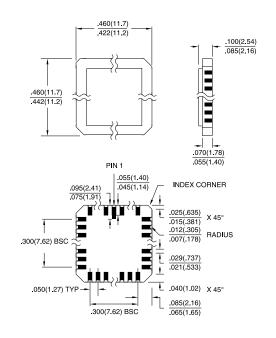
**28J**, 28-lead, Plastic J-leaded Chip Carrier (PLCC) Dimensions in Inches and (Millimeters) JEDEC STANDARD MS-018 AB



28L, 28-pad, Non-windowed, Ceramic Leadless Chip Carrier (LCC)

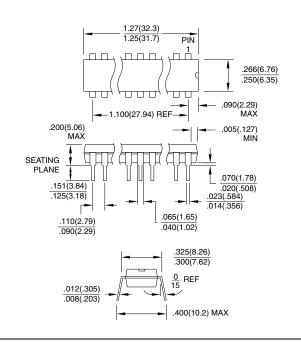
Dimensions in Inches and (Millimeters)

MIL-STD-1835 C-4



24P3, 24-pin, 0.300" Wide, Plastic Dual Inline Package (PDIP)

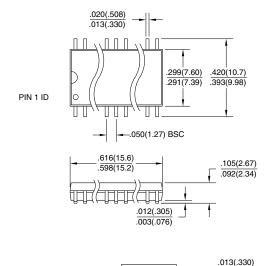
Dimensions in Inches and (Millimeters) JEDEC STANDARD MS-001 AF



### **Packaging Information**

**24S**, 24-lead, 0.300" Wide, Plastic Gull Wing Small Outline (SOIC)

Dimensions in Inches and (Millimeters)

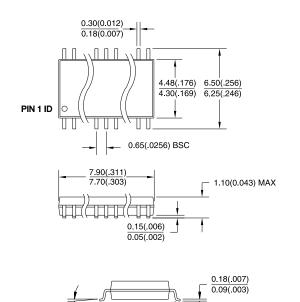


.050(1.27)

.015(.381)

.009(.229)

**24X**, 24-lead, 4.4 mm Wide, Plastic Thin Shrink Small Outline (TSSOP)
Dimensions in Millimeters and (Inches)



0.70(.028)

0.50(.020)

8 REF





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