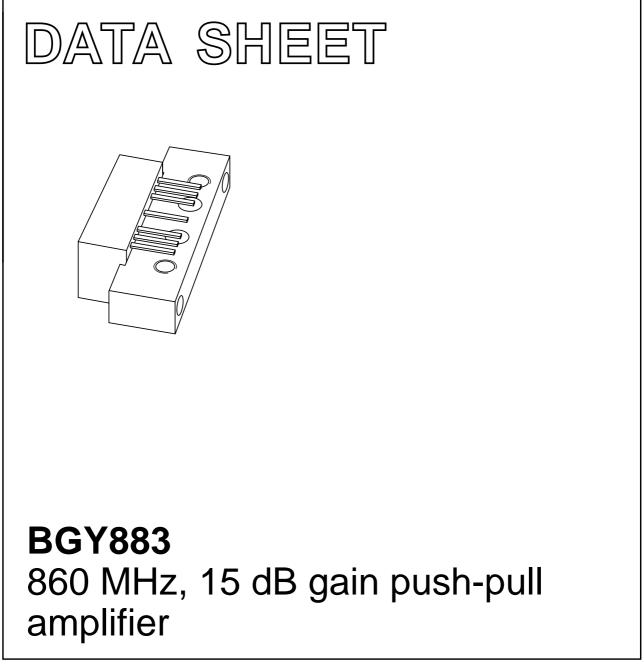
DISCRETE SEMICONDUCTORS



Product specification Supersedes data of 1997 Apr 14

2001 Oct 31



2001 Oct 31

860 MHz, 15 dB gain push-pull amplifier

BGY883

FEATURES

- Excellent linearity
- Extremely low noise
- Silicon nitride passivation
- Rugged construction
- Gold metallization ensures excellent reliability.

DESCRIPTION

Hybrid amplifier module designed for CATV systems operating over a frequency range of 40 to 860 MHz at a voltage supply of 24 V (DC).

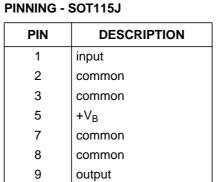
QUICK REFERENCE DATA

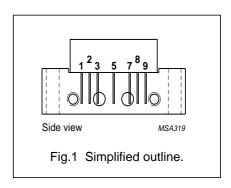
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
G _p	power gain	f = 50 MHz	14.5	15.5	dB
		f = 860 MHz	15	-	dB
I _{tot}	total current consumption (DC)	V _B = 24 V	_	235	mA

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
Vi	RF input voltage	-	65	dBmV
T _{stg}	storage temperature	-40	+100	°C
T _{mb}	operating mounting base temperature	-20	+100	°C





Product specification

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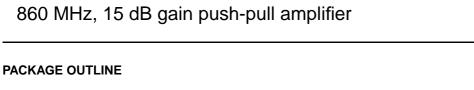
CHARACTERISTICS

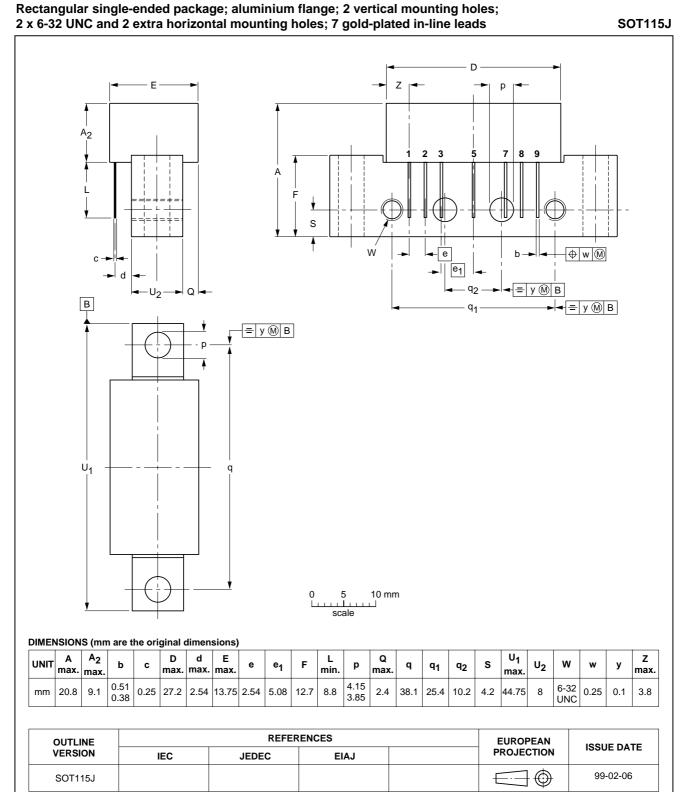
Table 1 Bandwidth 40 to 860 MHz; V_B = 24 V; T_{case} = 30 °C; Z_S = Z_L = 75 Ω

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
G _p	power gain	f = 50 MHz	14.5	_	15.5	dB
		f = 860 MHz	15	_	-	dB
SL	slope cable equivalent	f = 40 to 860 MHz	0	-	2	dB
FL	flatness of frequency response	f = 40 to 860 MHz	_	_	±0.3	dB
s ₁₁	input return losses	f = 40 to 80 MHz	20	-	-	dB
		f = 80 to 160 MHz	18.5	-	-	dB
		f = 160 to 320 MHz	17	-	-	dB
		f = 320 to 640 MHz	15.5	_	-	dB
		f = 640 to 860 MHz	14	-	-	dB
s ₂₂	output return losses	f = 40 to 80 MHz	20	-	-	dB
		f = 80 to 160 MHz	18.5	-	-	dB
		f = 160 to 320 MHz	17	-	-	dB
		f = 320 to 640 MHz	15.5	-	-	dB
		f = 640 to 860 MHz	14	-	-	dB
s ₂₁	phase response	f = 50 MHz	-45	-	+45	deg
СТВ	composite triple beat	49 channels flat; $V_o = 44 \text{ dBmV}$; measured at 859.25 MHz	-	-	-61	dB
X _{mod}	cross modulation	49 channels flat; $V_o = 44 \text{ dBmV}$; measured at 55.25 MHz	-	-	-61	dB
CSO	composite second order distortion	49 channels flat; $V_o = 44 \text{ dBmV}$; measured at 860.5 MHz	-	-	-61	dB
d ₂	second order distortion	note 1	_	_	-68	dB
Vo	output voltage	d _{im} = -60 dB; note 2	58.5	60	-	dBmV
F	noise figure	f = 50 MHz	_	_	6	dB
		f = 550 MHz	_	_	7	dB
		f = 650 MHz	_	_	7.5	dB
		f = 750 MHz	_	_	8	dB
		f = 860 MHz	_	-	8.5	dB
I _{tot}	total current consumption (DC)	note 3	-	-	235	mA

Notes

- 1. $f_p = 55.25 \text{ MHz}; V_p = 44 \text{ dBmV};$ $f_q = 805.25 \text{ MHz}; V_q = 44 \text{ dBmV};$ measured at $f_p + f_q = 860.5 \text{ MHz}.$
- 2. Measured according to DIN45004B:
 - $f_p = 851.25 \text{ MHz}; V_p = V_o;$
 - $f_q = 858.25 \text{ MHz}; V_q = V_o 6 \text{ dB};$
 - $f_r = 860.25 \text{ MHz}; V_r = V_o 6 \text{ dB};$
 - measured at $f_p + f_q f_r = 849.25$ MHz.
- 3. The module normally operates at V_B = 24 V, but is able to withstand supply transients up to 30 V.





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DATA SHEET STATUS

DATA SHEET STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
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Notes

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- 2. The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL http://www.semiconductors.philips.com.

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