TOSHIBA SOLID STATE AC RELAY

TSS2G48, TSS2J48

OPTICALLY ISOLATED, ZERO VOLTAGE TURN-ON, ZERO CURRENT TURN - OFF, NORMALLY OPEN SSR

COMPUTER PERIPHERALS
MACHINE TOOL CONTROLS
PROCESS CONTROL SYSTEMS
TRAFFIC CONTROL SYSTEMS

R.M.S On-State Current : I_{T (RMS)} = 2A
 Non-Repetitive Peak Off-State Voltage : V_{DSM} = 400, 600V

• TTL Compatible

• Isolation Voltage : 2000V AC (t=1min.)

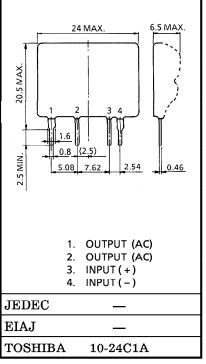
MAXIMUM RATINGS (Ta = 25°C) INPUT (CONTROL)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Control Input Voltage (DC) (Note 1)	V _{F (IN)}	5.5	v
Control Input Current (DC)	I _{F (IN)}	30	mA

OUTPUT (LOAD)

Non-Repetitive Peak	TSS2G48	VDGM	400	V	
Off-State Voltage	TSS2J48	VDSM	600		
Nominal AC Line	TSS2G48	V	120	v	
Voltage	TSS2J48	VAC	240	1 '	
R.M.S On-State Curren	I _T (RMS)	2	Α		
Peak One Cycle Surge	Iman	40 (50Hz)	Α		
Current (Non-Repetitive)		I _{TSM}	44 (60Hz)	A	
Operating Frequency R	f	45~65	Hz		
Isolation Voltage (t=1min., Input to Out	BVS/AC	2000	V		
Operating Temperature	$T_{ m opr}$	-20~80	°C		
Storage Temperature R	$T_{ m stg}$	-30~80	$^{\circ}\mathrm{C}$		

Unit in mm



Weight: 5g

- Note 1: Driving input rating: Insert an external resistance into SSR when the power supply over 5.5V is used.
- Note 2: Snubber nertork (C-R) is necessary to protect from surge voltage and dv/dt fire. Snubber network is to be connected between #1 #2 terminal.
- Note 3: Mounting: Soldering of printed wiring board should be used under 260°C and 10 second.

961001EBA2

[■] TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.

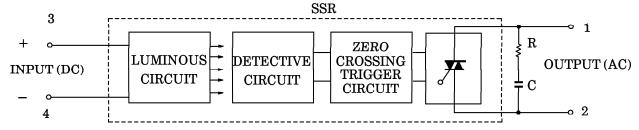
ELECTRICAL CHARACTERISTICS (Ta = 25°C) INPUT (CONTROL)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Pick Up Voltage	$V_{ ext{FT}}$		_	_	4.0	V
Drop Out Voltage	$ m v_{FD}$	$ m V_{AC} = 100 m V_{rms}$ Resistive Load	0.5	_	<u> </u>	V
Input Resistance	R (IN)	ivesistive Loau	_	160	_	Ω

OUTPUT (LOAD)

Off-State	TSS2G48	T	$V_{AC} = 100 V_{rms}, f = 50 Hz$	_	_	0.1	A	
Leakage Current	TSS2J48	$I_{ m OL}$	$V_{AC} = 200 V_{rms}$, f=50Hz		_	0.2	mA	
Peak On-State Vo	ltage	$V_{ extbf{TM}}$	$I_{T(RMS)}=2A$	_		1.5	V	
dv / dt (Off-State)		dv / dt	$V_{DSM} = 0.7 \times Rated$	50	1	_	V/μs	
Minimum Load Cu	ırrent	_		100			mA	
Turn-On Time		ton	$V_{AC} = 100V_{rms}$	_	1	1/2	Cycle	
Turn-Off Time		t_{off}	Resistive Load (Fig.1)			1/2	Cycle	
Isolation Resistance	e	$R_{\mathbf{S}}$	V=500V, R.H=40~60%	10^{10}	_	_	Ω	

EQUIVALEN CIRCUIT



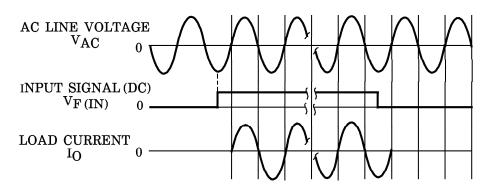


Fig.1 ZERO VOLTAGE SWITCHING WAVEFORM

961001EBA2'

The products described in this document are subject to foreign exchange and foreign trade control laws.

The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.

The information contained herein is subject to change without notice.

