TOSHIBA MT6P06T

TOSHIBA TRANSISTOR SILICON NPN EPITAXIAL PLANAR TYPE

1 T 6 P 0 6 T

VHF~UHF BAND LOW NOISE AMPLIFIER APPLICATIONS

TWO devices are built in to the super-thin and ultra super mini (6 pins) package: TU6

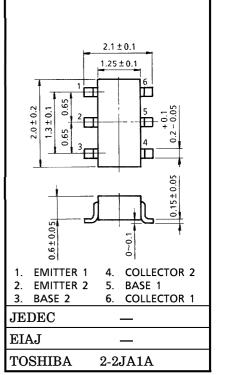
MOUNTED DEVICES

	Q1/Q2:SSM (TESM)
Three-pins (SSM/TESM) mold products are corresponded.	MT3S06S (MT3S06T)

MAXIMUM RATINGS (Ta = 25°C)

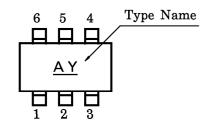
CHARACTERISTIC	SYMBOL	Q1 / Q2	UNIT
Collector-Base Voltage	v_{CBO}	10	V
Collector-Emitter Voltage	v_{CEO}	5	V
Emitter-Base Voltage	V_{EBO}	1.5	V
Collector Current	$I_{\mathbf{C}}$	15	mA
Base Current	$I_{\mathbf{B}}$	7	mA
Collector Power Dissipation	PC	120	mW
Junction Temperature	T_{j}	125	$^{\circ}\mathrm{C}$
Storage Temperature Range	$\mathrm{T_{stg}}$	-55~125	°C

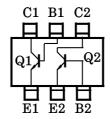
Unit in mm



MARKING

PIN ASSIGNMENT (TOP VIEW)





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ELECTRICAL CHARACTERISTICS Q1/Q2 (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector Cut-off Current	ICBO	$V_{CB} = 5 \text{ V}, I_{E} = 0$	_	_	0.1	μ A
Emitter Cut-off Current	$I_{ m EBO}$	$V_{EB} = 1 V, I_{C} = 0$	_	_	1	μ A
DC Current Gain	$h_{ extbf{FE}}$	$V_{ m CE}=1 m V,\ I_{ m C}=5 m mA$	70	_	140	_
Transition Frequency	$ m f_{T}$	$V_{ m CE}=3~{ m V},~{ m I}_{ m C}=5~{ m mA}$	7	10	_	GHz
Insertion Gain	$ S_{21e} ^2(1)$	$V_{\text{CE}} = 1 \text{ V}, \text{ I}_{\text{C}} = 5 \text{ mA},$ $f = 2 \text{ GHz}$	_	7.5	_	dB
	$ S_{21e} ^2$ (2)	$V_{ ext{CE}} = 3 \text{ V}, \text{ I}_{ ext{C}} = 7 \text{ mA},$ $f = 2 \text{ GHz}$	4.5	8	_	dB
Noise Figure	NF (1)	$V_{ ext{CE}} = 1 \text{ V}, \text{ I}_{ ext{C}} = 3 \text{ mA},$ $f = 2 \text{ GHz}$	_	1.7	3	dB
	NF (2)	$V_{ ext{CE}} = 3 \text{ V}, \text{ I}_{ ext{C}} = 3 \text{ mA},$ $f = 2 \text{ GHz}$	_	1.6	3	dB
Reverse Transfer Capacitance	C_{re}	$egin{aligned} V_{\mathrm{CB}} &= 1 \mathrm{V}, \ \mathrm{I_E} &= 0, \\ \mathrm{f} &= 1 \mathrm{MHz} \ \mathrm{(Note)} \end{aligned}$	_	0.35	0.75	pF

(Note): C_{re} is measured by 3 terminal method with capacitance bridge.

HANDLING PRECAUTION

When handling individual devices (which are not yet mounting on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.