TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

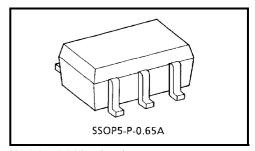
# TC7SB384FU

#### Single Bus Switch

The TC7SB384FU provides single bit of high-speed TTL-compatible switching. The low on resistance of the switch allows connections to be made with minimal propagation delay.

The device is organized as just 1-bit low-impedance switch with output-enable ( $\overline{OE}$ ) input. When  $\overline{OE}$  is low, the switch is on and data can flow from port A to port B, or vice versa. When  $\overline{OE}$  is high, the switch is open and a high-impedance state exists between the two ports.

All inputs are equipped with protection circuits against static discharge.

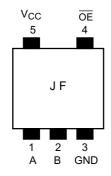


Weight: 0.006 g (typ.)

#### **Features**

- Operating voltage:  $VCC = 4.5 \sim 5.5 \text{ V}$
- High speed operation:  $t_{pd} = 0.25 \text{ ns (max)}$
- Low on resistance:  $RON = 5 \Omega$  (typ.)
- ESD performance: Machine model  $> \pm 200 \text{ V}$ Human body model  $> \pm 2000 \text{ V}$
- TTL level input (control input)
- Package: USV

#### Pin Assignment (top view)



980910EBA1

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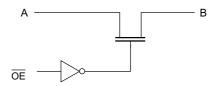
<sup>•</sup> The information contained herein is subject to change without notice.



### **Truth Table**

Input	Function	
ŌĒ	runction	
L	A port = B port	
Н	Disconnect	

### **System Diagram**



### **Maximum Ratings**

Characteristics	Symbol	Rating	Unit
Power supply range	V <sub>CC</sub>	-0.5~7.0	V
DC input voltage	V <sub>IN</sub>	-0.5~7.0	V
DC switch voltage	Vs	-0.5~7.0	V
Input diode current	I <sub>IK</sub>	-50	mA
Continuous channel current	IS	128	mA
Power dissipation	PD	200	mW
DC V <sub>CC</sub> /GND current	I <sub>CC</sub> /I <sub>GND</sub>	±100	mA
Storage temperature	T <sub>stg</sub>	-65~150	°C

# **Recommended Operating Conditions**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	4.5~5.5	V
Input voltage	V <sub>IN</sub>	0~5.5	V
Switch voltage	Vs	0~5.5	V
Operating temperature	T <sub>opr</sub>	-40~85	°C
Input rise and fall time	dt/dv	0~10	ns/V



#### **Electrical Characteristics**

#### DC Characteristics ( $Ta = -40 \sim 85$ °C)

Characte	arietice	Symbol	Test Condition V <sub>CC</sub> (V)		Min	Тур.	Max	Unit	
Charact	en istics	Symbol			V <sub>CC</sub> (V)	IVIIII	(Note1)	IVIAX	Offic
Input voltage	"H" level	V <sub>IH</sub>	-	_		2.0	_		V
input voltage	"L" level	$V_{IL}$	-	_	4.5~5.5		_	0.8	٧
Input leakage cu	urrent	I <sub>IN</sub>	V <sub>IN</sub> = 0~5.5 V		5.5		_	±1.0	μΑ
Off-state leakag (switch off)	e current	I <sub>SZ</sub>	A, B = 0~5.5 V, $\overline{OE} = V_{CC}$		5.5	_	_	±1.0	μΑ
ON resistance			Vis = 0 V	I <sub>IS</sub> = 30 mA	4.5		5	7	
ON resistance	(Note2)	R <sub>ON</sub>	VIS – U V	I <sub>IS</sub> = 64 mA	4.5		5	7	Ω
(NOTEZ)		$V_{IS} = 2.4 \text{ V}, I_{IS} = 15 \text{ mA}$		4.5		10	15		
Quiescent sunn	uiescent supply current ICC VIN = VCC or GND, IOU		), I <sub>OUT</sub> = 0	5.5		_	10	μΑ	
Quiocociit supp	iy odiront	$\Delta I_{CC}$	V <sub>IN</sub> = 3.4 V (one input)		5.5		_	2.5	mA

Note1: Typical values are at  $V_{CC} = 5 \text{ V}$  and  $Ta = 25^{\circ}\text{C}$ .

Note2: Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the two (A or B) pins.

#### AC Characteristics ( $Ta = -40 \sim 85$ °C)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit
Propagation delay time (bus to bus)	t <sub>pLH</sub> t <sub>pHL</sub>	Figure 1, Figure 2 (Note3)	4.5	_	0.25	ns
Output enable time	t <sub>pZL</sub> t <sub>pZH</sub>	Figure 1, Figure 3	4.5		4.0	ns
Output disable time	t <sub>pLZ</sub> t <sub>pHZ</sub>	Figure 1, Figure 3	4.5		4.5	ns

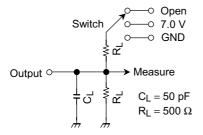
Note3: This parameter is guaranteed by design but is not tested. The bus switch contributes no propagation delay other than the RC delay of the typical on resistance of the switch and the 50 pF load capacitance, when driven by an ideal voltage the source (zero output impedance).

#### **Capacitive Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Тур.	Unit
Control pin input capacitance	C <sub>IN</sub>	(N	lote4)	5.0	3	pF
Switch terminal capacitance	C <sub>I/O</sub>	$\overline{OE} = V_{CC}$ (N	lote4)	5.0	10	pF

Note4: This item is guaranteed by design.

#### **AC Test Circuit**



Parameter	Switch
t <sub>pLH</sub> , t <sub>pHL</sub>	Open
$t_{pLZ}$ , $t_{pZL}$	7.0 V
t <sub>pHZ</sub> , t <sub>pZH</sub>	Open

Figure 1

#### **AC Waveform**

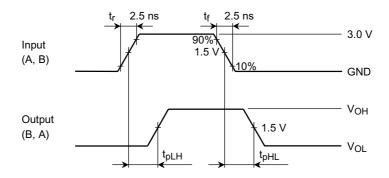


Figure 2  $t_{pLH}$ ,  $t_{pHL}$ 

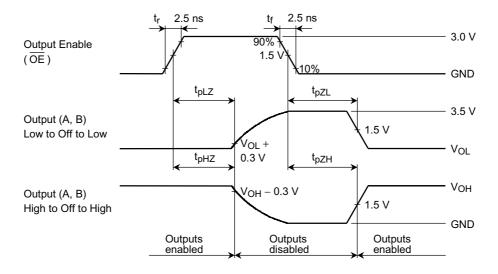
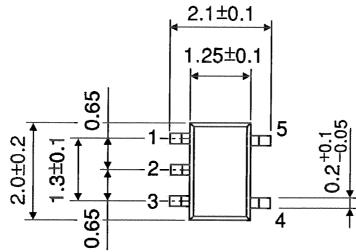
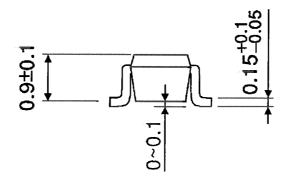


Figure 3  $t_{pLZ}$ ,  $t_{pHZ}$ ,  $t_{pZL}$ ,  $t_{pZH}$ 

# **Package Dimensions**

SSOP5-P-0.65A Unit: mm





Weight: 0.006 g (typ.)