

TOSHIBA Transistor  
Silicon NPN Epitaxial Type (PCT process) Silicon PNP Epitaxial Type (PCT process)

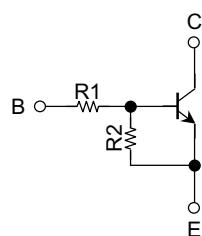
# R N 4 7 A 3

Switching, Inverter Circuit, Interface Circuit  
and Driver Circuit Applications.

- Including two devices in USV (ultra super mini type with 5 leads)
- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process

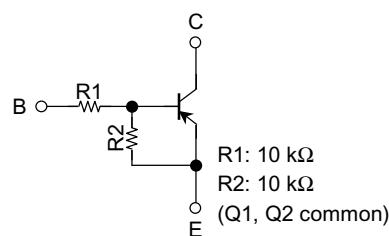
## Equivalent Circuit and Bias Resistor Values

Q1

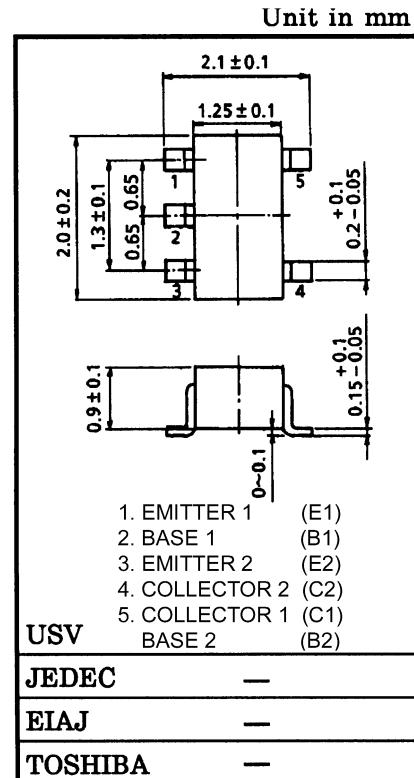


Q1: RN1102F

Q2

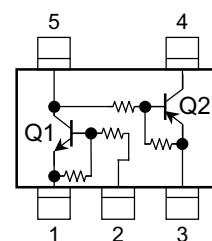
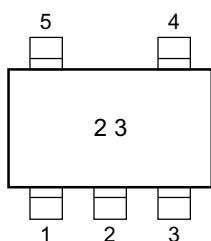


Q2: RN2102F



## Marking

## Equivalent Circuit (top view)



- 961001EA1
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**Q1 Maximum Ratings ( $T_a = 25^\circ\text{C}$ )**

Characteristics	Symbol	Rating	Unit
Collector-base voltage	$V_{CBO}$	50	V
Collector-emitter voltage	$V_{CEO}$	50	V
Emitter-base voltage	$V_{EBO}$	10	V
Collector current	$I_C$	100	mA

**Q2 Maximum Ratings ( $T_a = 25^\circ\text{C}$ )**

Characteristics	Symbol	Rating	Unit
Collector-base voltage	$V_{CBO}$	-50	V
Collector-emitter voltage	$V_{CEO}$	-50	V
Emitter-base voltage	$V_{EBO}$	-10	V
Collector current	$I_C$	-100	mA

**Q1, Q2 Common Maximum Ratings ( $T_a = 25^\circ\text{C}$ )**

Characteristics	Symbol	Rating	Unit
Collector power dissipation	$P_C$ (Note)	200	mW
Junction temperature	$T_j$	150	°C
Storage temperature range	$T_{stg}$	-55 to 150	°C

Note: Total rating

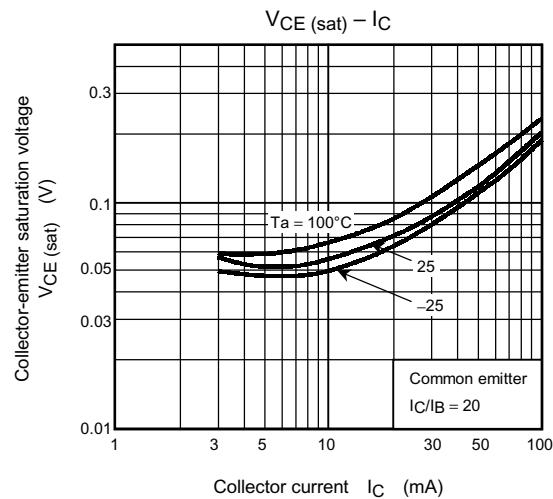
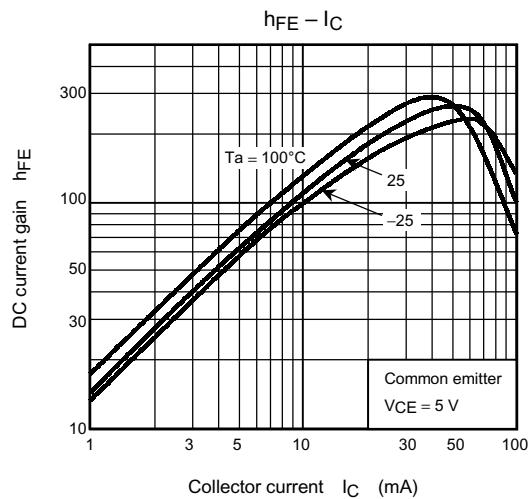
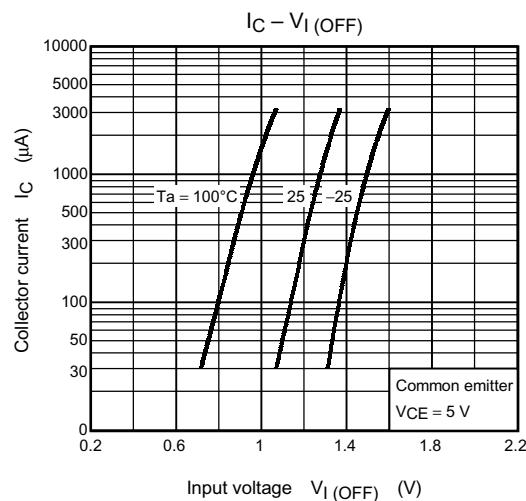
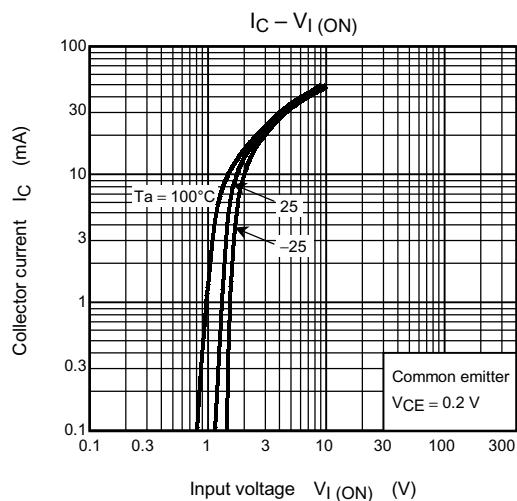
**Q1 Electrical Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	V <sub>CB</sub> = 50 V, I <sub>E</sub> = 0	—	—	100	nA
	I <sub>CEO</sub>	V <sub>CE</sub> = 50 V, I <sub>B</sub> = 0	—	—	500	
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> = 10 V, I <sub>C</sub> = 0	0.38	—	0.71	mA
DC current gain	h <sub>FE</sub>	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 10 mA	50	—	—	
Collector-emitter saturation voltage	V <sub>CE</sub> (sat)	I <sub>C</sub> = 5 mA, I <sub>B</sub> = 0.25 mA	—	0.1	0.3	V
Input voltage (ON)	V <sub>I</sub> (ON)	V <sub>CE</sub> = 0.2 V, I <sub>C</sub> = 5 mA	1.2	—	2.4	V
Input voltage (OFF)	V <sub>I</sub> (OFF)	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 0.1 mA	1.0	—	1.5	V
Transition frequency	f <sub>T</sub>	V <sub>CE</sub> = 10 V, I <sub>C</sub> = 5 mA	—	250	—	MHz
Collector output capacitance	C <sub>ob</sub>	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1 MHz	—	3	6	pF
Input resistor	R <sub>1</sub>	—	7	10	13	kΩ
Resistor ratio	R <sub>1</sub> /R <sub>2</sub>	—	0.8	1.0	1.2	

**Q2 Electrical Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	V <sub>CB</sub> = -50 V, I <sub>E</sub> = 0	—	—	-100	nA
	I <sub>CEO</sub>	V <sub>CE</sub> = -50 V, I <sub>B</sub> = 0	—	—	-500	
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> = -10 V, I <sub>C</sub> = 0	-0.38	—	-0.71	mA
DC current gain	h <sub>FE</sub>	V <sub>CE</sub> = -5 V, I <sub>C</sub> = -10 mA	50	—	—	
Collector-emitter saturation voltage	V <sub>CE</sub> (sat)	I <sub>C</sub> = -5 mA, I <sub>B</sub> = -0.25 mA	—	-0.1	-0.3	V
Input voltage (ON)	V <sub>I</sub> (ON)	V <sub>CE</sub> = -0.2 V, I <sub>C</sub> = -5 mA	-1.2	—	-2.4	V
Input voltage (OFF)	V <sub>I</sub> (OFF)	V <sub>CE</sub> = -5 V, I <sub>C</sub> = -0.1 mA	-1.0	—	-1.5	V
Transition frequency	f <sub>T</sub>	V <sub>CE</sub> = -10 V, I <sub>C</sub> = -5 mA	—	200	—	MHz
Collector output capacitance	C <sub>ob</sub>	V <sub>CB</sub> = -10 V, I <sub>E</sub> = 0, f = 1 MHz	—	3	6	pF
Input resistor	R <sub>1</sub>	—	7	10	13	kΩ
Resistor ratio	R <sub>1</sub> /R <sub>2</sub>	—	0.8	1.0	1.2	

Q1



Q2

