

SANYO Semiconductors DATA SHEET

2SD2712 — NPN Triple Diffused Planar Silicon Darlington Transistor Driver Applications

Applications

• Suitable for use in control motor drivers, printer hammer drivers, relay drivers, audio output and constant-voltage regulators.

Features

- · High DC current gain.
- · Wide ASO.
- · Low saturation voltage.
- · Adoption of MBIT process.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		180	V
Collector-to-Emitter Voltage	VCEO		160	V
Emitter-to-Base Voltage	VEBO		6	V
Collector Current	IC		10	Α
Collector Current (Pulse)	ICP		16	Α
Collector Dissipation	Do.		2.5	W
	PC	Tc=25°C	110	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offile
Collector Cutoff Current	ICBO	V _{CB} =180V, I _E =0A			0.1	mA
Emitter Cutoff Current	IEBO	VEB=6V, IC=0A			10	mA
DC Current Gain	hFE	V _{CE} =5V, I _C =6.5A	5000			
Gain-Bandwidth Product	fT	V _{CE} =5V, I _C =6.5A		15		MHz
Collector-to-Emitter Saturation Voltage	VCE(sat)	IC=5.5A, IB=11mA			1.5	V
Base-to-Emitter Saturation Voltage	V _{BE} (sat)	I _C =5.5A, I _B =11mA			2.3	٧
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =1mA, I _E =0A	180			٧
Collector Sustain Voltage	VCEO(SUS)	I _C =100mA, I _B =0A	160			V

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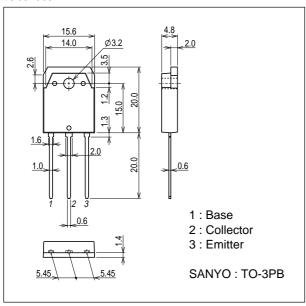
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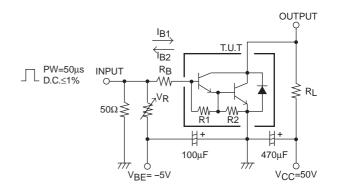
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Onit
Turn-ON Time	ton	See specified Test Circuit.		0.9		μs
Storage Time	tstg	See specified Test Circuit.		8.0		μs
Fall Time	tf	See specified Test Circuit.		3.0		μs

Package Dimensions

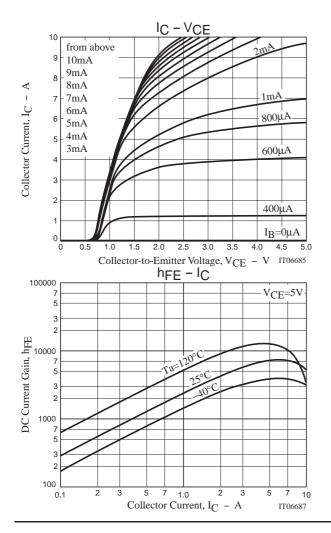
unit : mm 7503-003

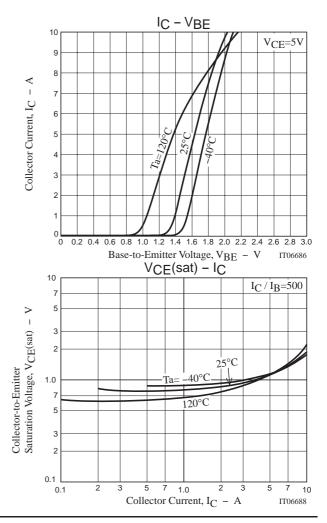


Switching Time Test Circuit

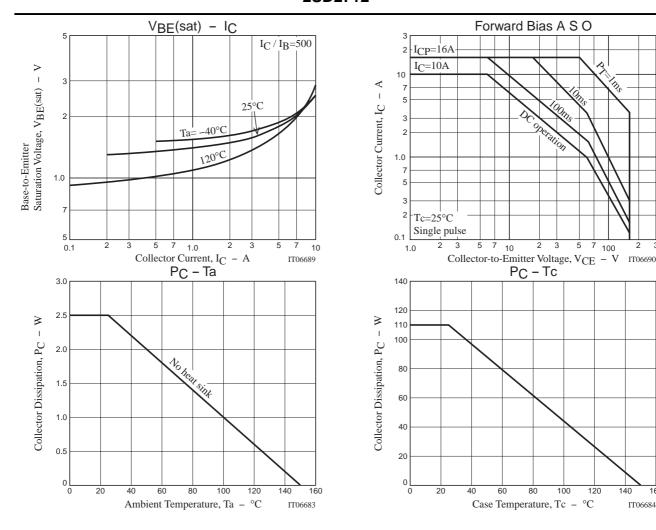


 $I_{C}=500I_{B1}=500I_{B2}=6.5A$





2SD2712



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