

# Oki, Network Solutions for a Global Society

## **OKI Semiconductor**

# **FEDW9045BC-03** Issue Date: Dec. 16. 2002

## **MBF9045BC**

SAW Antenna Duplexer (700 to 1000 MHz)

#### **GENERAL DESCRIPTION**

The MBF9045BC is the SAW antenna duplexer for the frequency range of 700 to 1000 MHz.

This SAW Duplexer integrates RF filters at Tx and Rx side, and matching circuit into PKG. This helps to save the space and weight greatly in the target application such as mobile telephone.

This SAW Duplexer has very low insertion loss by using high quality package.

Due to high harmonics characteristics, total number of components at RF circuit can be minimized.

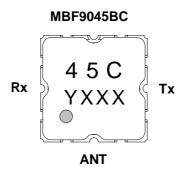
Thanks to high isolation performance, high sensitivity can be expected. Low insertion loss at Tx saves the power consumption of mobile telephone which prolong the battery life.

#### **FEATURES**

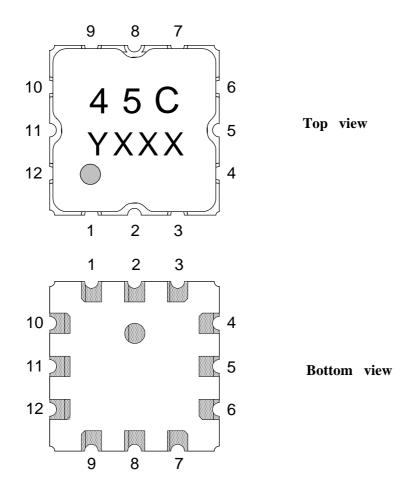
- Complying Standard AMPS, IS-95, IS-136
- Small package: 5mm x 5mm & less than 1.8 mm in height
- PKG I/O Impedance:  $50 \Omega$

#### PRODUCT DESCRIPTION

#### Package Type



#### PIN ASSIGNMENT & DESCRIPTION



#### CONNECTION

- 2: ANT (Antenna Pin)
- 5: Tx (Transmitting Terminal Pin)
  11: Rx (Receiving Terminal Pin)
- 13: INDEX Mark (should not be soldered )

Others: GND (Ground Pin)

#### ABSOLUTE MAXIMUM RATINGS

Parameter	Cymhal	Rat	Lloit	
	Symbol	Min.	Max.	Unit
Operating Temperature	Та	-30	+85	°C
Storage Temperature	T <sub>STG</sub>	-40	+85	°C
Maximum Input Power	P <sub>IN</sub>	_	1.2	W

#### RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Rat	Unit	
		Min.	Max.	Offic
Operating Temperature	Та	-30	+85	°C

#### **ELECTRICAL CHARACTERISTICS**

 $(Ta = -30 \text{ to } +85^{\circ}C)$ 

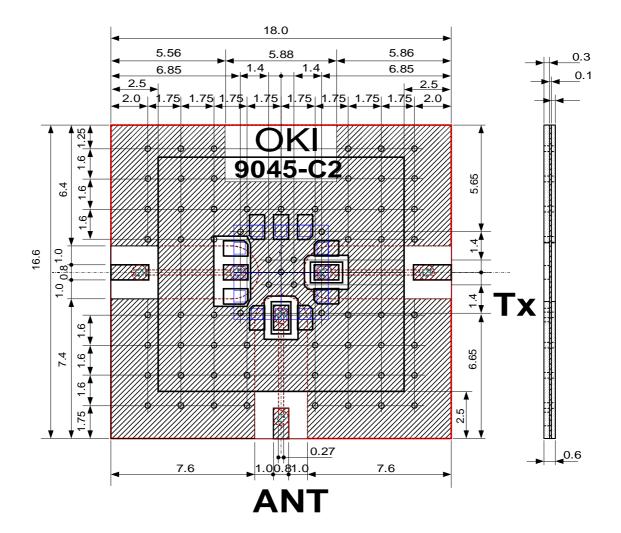
Parameter		Condition	Condition Mini.		Max.	Unit/Notes		
T	Tx → Antenna							
a)	Insertion loss		_	2.0	2.5	dB		
b)	Passband ripple	824 to 849 MHz	_	0.7	1.5			
	VSWR ANT Port	024 (0 049 (1)172		1.8	2.2			
c)	VSWR Tx Port		_	1.8	2.5			
		500 to 800 MHz	12	14				
		869 to 894 MHz	40	45	_			
d)	Absolute attenuation	900 to 1600MHz	13	17	_	dB		
		1648 to 1698 MHz	10	15	_			
		2472 to 2547 MHz	2	3	_			
Α	Antenna → Rx							
a)	Insertion loss		_	3.3	4.0	dB		
b)	Passband ripple	869 to 894 MHz	_	1.0	2.0	ав		
۵)	VSWR ANT Port	009 to 694 Wil 12	_	2.0	2.5			
c)	VSWR Rx Port		_	1.8	2.1			
	Absolute attenuation	824 to 849 MHz	50	60	_			
d)		960 to 990 MHz	35	42	_			
		1050 to 1080 MHz	35	43	_	dB		
		1140 to 1170 MHz	35	46	_			
		1230 to 1270 MHz	35	45	_			
Isolation TX → RX								
3)	Absolute attenuation	824 to 849 MHz	55	60	_	dB		
a)		869 to 894 MHz	43	49	_	dB		

Note: Electrical characteristics described above is guaranteed by the following measurement and equipment condition.

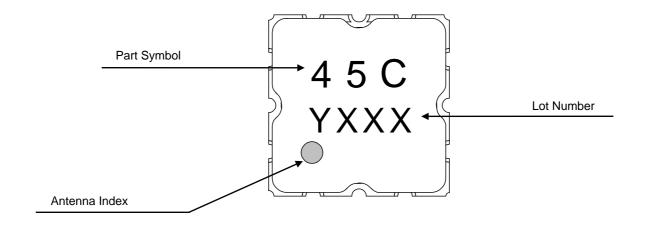
1) Test board: See next page

2) Measurement machine: Network analyzer

#### **Test Board**



#### **MARKING**



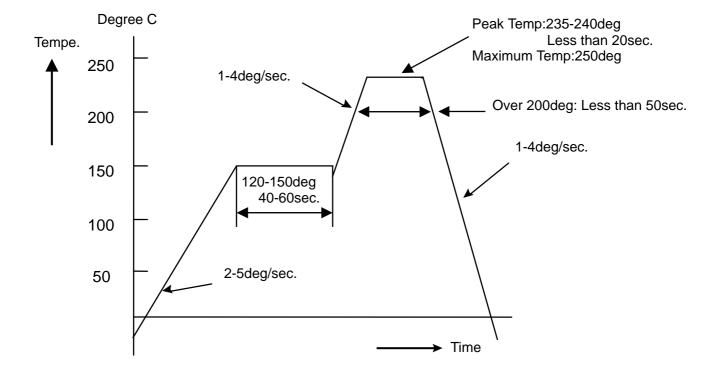
Note) Lot Number

Y: Last number of year XXX: Serial number

#### REFLOW TEMPERATURE PROFILE

The figure below shows recommended temperature profile of infrared reflow and air reflow. Other type of reflow is not recommended.

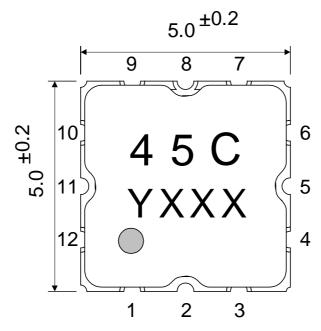
The maximum reflow count is 2 times. Washing of this device after reflow process is prohibited.

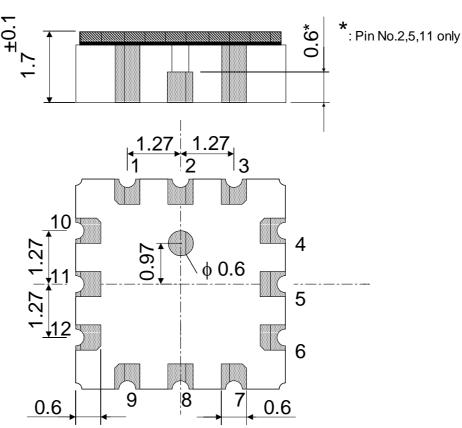


#### PACKAGE DIMENTION

Unit: mm

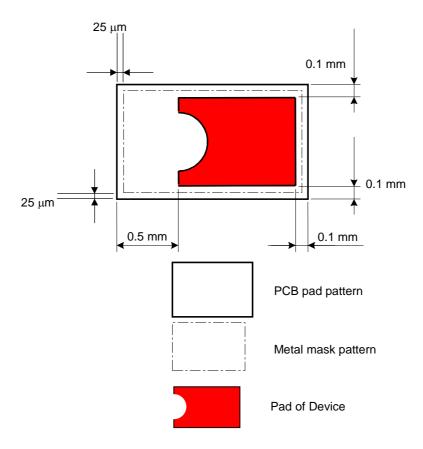
General tolerance: ±0.15





#### RECOMMENDATION FOR SOLDER PAD PATTERN

The solder pad pattern should be designed by customers because it depends on the electrical performance of the customers' system. Following is an example of solder pad pattern which is used in OKI's package evaluation board. Please be noted that this is for reference purpose only.



Please pay attention to the following items to maintain electrical performance.

- (1) Metal mask pattern for cream solder should be 25  $\mu m$  smaller on each side. Metal mask is 0.15 mm in thickness.
- (2) As the impedance of Tx, Rx, ANT is designed for  $50\Omega$ , please consider this for the design of mother board.

#### **REVISION HISTORY**

Document	Date	Page			
No.		Previous Edition	Current Edition	Description	
FEDW9045BC-01	Dec. 2001	_	ı	Final edition 1	
FEDW9045BC-02	Apr.1, 2002	_	-	Final edition 2	
		1	1	Partially changed the content of "PRODUCT DESCRIPTION".	
FEDW9045BC-03	Dec. 16, 2002	_	-	Final edition 3	
			4	Partially changed the content of "ELECTRICAL CHARACTERISTICS".	
			5	Partially changed the content of "Test Board".	
			7	Partially changed the content of "REFLOW TEMPERATURE PROFILE".	

#### **NOTICE**

1. The information contained herein can change without notice owing to product and/or technical improvements. Before using the product, please make sure that the information being referred to is up-to-date.

- 2. The outline of action and examples for application circuits described herein have been chosen as an explanation for the standard action and performance of the product. When planning to use the product, please ensure that the external conditions are reflected in the actual circuit, assembly, and program designs.
- 3. When designing your product, please use our product below the specified maximum ratings and within the specified operating ranges including, but not limited to, operating voltage, power dissipation, and operating temperature.
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