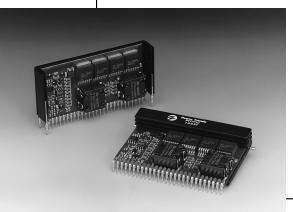
32 AMP HIGH-PERFORMANCE "SLEDGE HAMMER" PROGRAMMABLE ISR

**SLTS078** (Revised 5/31/2000)



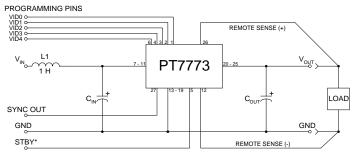
### **Description**

The PT7773 is one of a series of high-performance, 32 Amp Integrated Switching Regulators (ISRs) housed in a 27-pin SIP package. The 32A capability allows easy integration of the latest high-speed, low-voltage µPs, ASICs, DSPs, and bus drivers into existing 5V systems.

The output voltage of the PT7773 is programmable over the low voltage range, 0.8V to 3.1V via a 5-bit input. A differential remote sense is also provided, which automatically compensates for any voltage drop between the ISR and load.

An output capacitance of 2400µF is required for proper operation.

# **Standard Application**



 $\begin{array}{ll} C_{in} &= Required\ 2400\mu F\ electrolytic \\ C_{out} &= Required\ 2400\mu F\ electrolytic \\ L1 &= Optional\ l\mu H\ input\ choke \end{array}$ 

#### Pin-Out Information

Pin	Function
1	VID0
2	VID1
3	VID2
4	VID3
5	STBY*- Stand-by
6	VID4
7	$V_{in}$
8	$V_{in}$
9	$V_{in}$
10	$V_{in}$
11	$V_{in}$
12	Remote Sense Gnd
13	GND

•		
	Pin	Function
	14	GND
	15	GND
	16	GND
	17	GND
	18	GND
	19	GND
	20	V <sub>out</sub>
	21	V <sub>out</sub>
	22	V <sub>out</sub>
	23	V <sub>out</sub>
	24	V <sub>out</sub>
	25	V <sub>out</sub>
	26	Remote Sense $V_{out}$
	27	Sync Out

For STBY\* pin; open = output enabled; ground = output disabled.

### **Specifications**

Characteristics			P	PT7773 SERIES		
(T <sub>a</sub> = 25°C unless noted)	Symbols	Conditions	Min	Тур	Max	Units
Output Current	$I_{o}$	$T_a$ = +60°C, 200 LFM, pkg N $T_a$ = +25°C, natural convection	0.1(1) 0.1(1)		32 26	A
Input Voltage Range	$V_{in}$	$0.1A \le I_o \le 32A$	4.5	_	5.5	V
Output Voltage Tolerance	$\Delta V_{o}$	$V_{in} = +5V$ , $I_{o} = 32A$ $0^{\circ}C \le T_{a} \le +55^{\circ}C$	Vo-0.03	_	Vo+0.03	V
Line Regulation	Reg <sub>line</sub>	$4.5V \le V_{\rm in} \le 5.5V$ , $I_{\rm o} = 32A$	_	±10	_	mV
Load Regulation	Reg <sub>load</sub>	$V_{\rm in} = +5V$ , $0.1 \le I_{\rm o} \le 32A$	_	±10	_	mV
Vo Ripple/Noise pk-pk	$V_n$	$V_{in} = +5V, I_o = 32A$	_	50	_	mV
Transient Response with C <sub>out</sub> = 2400μF	$egin{array}{c} t_{tr} \ V_{os} \end{array}$	$I_o$ step between 16A and 32A $V_o$ over/undershoot	_	100 200	_	μSec mV
Efficiency	η	$V_{\rm in}$ = +5V, $I_{\rm o}$ = 20A, $V_{\rm o}$ = 2.5V	_	86	_	%
Switching Frequency	$f_{o}$	$4.5V \le V_{in} \le 5.5V$ $0.1A \le I_o \le 32A$	650	700	750	kHz
Absolute Maximum Operating Temperature Range	$T_a$	Over V <sub>in</sub> Range	0		+85 (2)	°C
Storage Temperature	$T_s$	_	-40		+125	°C
Weight	_	Vertical/Horizontal	_	53/66	_	grams

Notes: (1) ISR-will operate down to no load with reduced specifications. Please note that this product is not short-circuit protected.

(2) See SOA curves or consult the factory for the appropriate derating.

Output Capacitors: The PT7773 series requires a minimum output capacitance of  $2400\mu F$  for proper operation. Do not use Oscon type capacitors. The maximum allowable output capacitance is  $30,000\mu F$ .

Input Filter: An input filter is optional for most applications. The input inductor must be sized to bandle 32ADC with a typical value of 1µH. The input capacitance must be rated for a minimum of 2.6Årms of ripple current. For transient or dynamic load applications, additional capacitance may be required.

#### **Features**

- +5V input
- 5-bit Programmable: 0.8V to 3.1V @32A
- High Efficiency
- Input Voltage Range: 4.5V to 5.5V
- Differential Remote Sense
- 27-pin SIP Package

### **Programming Information**

VID	3 VID2	VID1	VIDO	VID4=1 Vout	VID4=0 Vout
1	1	1	1	1.6V	0.80V
1	1	1	0	1.7V	0.85V
1	1	0	1	1.8V	0.90V
1	1	0	0	1.9V	0.95V
1	0	1	1	2.0V	1.00V
1	0	1	0	2.1V	1.05V
1	0	0	1	2.2V	1.10V
1	0	0	0	2.3V	1.15V
0	1	1	1	2.4V	1.20V
0	1	1	0	2.5V	1.25V
0	1	0	1	2.6V	1.30V
0	1	0	0	2.7V	1.35V
0	0	1	1	2.8V	1.40V
0	0	1	0	2.9V	1.45V
0	0	0	1	3.0V	1.50V
0	0	0	0	3.1V	1.55V

Logic 0 = Pin 12 potential (remote sense gnd) Logic 1 = Open circuit (no pull-up resistors) VID3 and VID4 may not be changed while the unit

# **Ordering Information**

**PT7773**  $\Box$  = 0.8 to 3.1 Volts

For dimensions and PC board layout, see Package Style 1020 and 1030

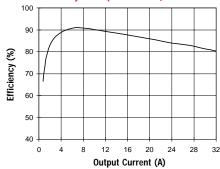
## PT Series Suffix (PT1234X)

Case/Pin
Configuration

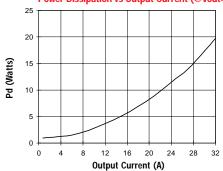
Comiguration	
Vertical Through-Hole	N
Horizontal Through-Hole	Α
Horizontal Surface Mount	С

# CHARACTERISTIC DATA

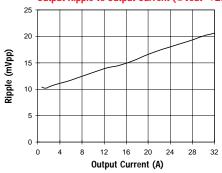




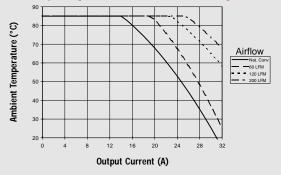
### Power Dissipation vs Output Current (@Vout=+2.5V)



### Output Ripple vs Output Current (@Vout=+2.5V)



### Safe Operating Area (@Vin=+5V, Vout=+2.5V, Pkg N) (See Note B)



Note A: Characteristic data in the above graphs has been developed from actual products tested at 25°C. This data is considered typical for the ISR. Note B: SOA curves represent operating conditions at which internal components are at or below manufacturer's maximum rated operating temperatures.

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