Preliminary

- **CMOS Logic**
- **Operating Voltage Range 2V to 5.5V**
- **Input Voltage Range 0V to 5.5V**
- **5V to 3V Level Change**
- **High Speed Operations**
- **Low Power Consumption**
- **MSOP-8 Package**

Applications

- Palmtops
- Digital Equipment

Series Description

The XC74WL series is manufactured using silicon gate CMOS processes. The small quiescent current, which is one of the features of the CMOS logic, gives way to high speed operations which enables LS-TTL. To be surpassed. Since it is possible to input a voltage higher than the reference voltage, it is also possible to achieve a level change from 5V to 3V. With wave forming buffers connected internally, stabilized output can be achieved as the series offers high noise immunity. As the series is integrated into a mini molded, MSOP-8 package, high density mounting is possible.

Product List

SERIES	FUNCTION
XC74WL00AAS	Dual 2-input NAND Gate
XC74WL02AAS	Dual 2-input NOR Gate
XC74WL04AAS	Triple Inverter
XC74WL08AAS	Dual 2-input AND Gate
XC74WL14AAS	Triple Schmitt Inverter
XC74WL32AAS	Dual 2-input OR Gate
XC74WL34AAS	Triple Buffer
XC74WL74AAS	D-Type Flip Flop

SERIES	FUNCTION
XC74WL86AAS	Dual 2-input exclusive OR Gate
XC74WL125AS	Dual Bus Buffer
XC74WL126AS	Dual Bus Buffer
XC74WL157AS	2-channel Multiplexer
XC74WL240AS	Dual Bus Buffer (inverted 3-state outputs)
XC74WL241AS	Dual Bus Buffer (non-inverted 3-state outputs)
XC74WL4053S	2-channel Analog Multiplexer/Demultiplexer
XC74WL4066S	Dual Analog Switch/Multiplexer
XC74WLU04AS	Triple Inverter (Unbuffered)