

### HIGH RELIABILITY HYBRID DC-DC CONVERTERS WITH INTEGRAL EMI FILTER

#### **DESCRIPTION**

The DVETR series of high reliability DC-DC converters is operable over the full military (-55 °C to +125 °C) temperature range with no power derating. Unique to the DVETR series is a fault tolerant magnetic feedback circuit. Operating at a nominal fixed frequency of 500 kHz per stage, these regulated, isolated units utilize well-controlled undervoltage lockout circuitry to eliminate slow start-up problems.

These converters are designed and manufactured in a facility qualified to ISO9001 and certified to MIL-PRF-38534 and MIL-STD-883.

This product may incorporate one or more of the following U.S. patents:

5,784,266 5,790,389 5,963,438 5,999,433 6,005,780 6,084,792 6,118,673

#### **FEATURES**

- High Reliability
- Very Low Output Noise
- Wide Input Voltage Range: 15 to 50 Volts per MIL-STD-704
- Up to 40 Watts Output Power
- Fault Tolerant Magnetic Feedback Circuit
- NO Use of Optoisolators
- Undervoltage Lockout
- Indefinite Short Circuit Protection
- Current Limit Protection
- Industry Standard Pinout
- High Input Transient Voltage: 80 Volts for 1 sec per MIL-STD-704A
- Precision Solder Seal Hermetic Package
- High Power Density: > 30 W/in<sup>3</sup>
- Custom Versions Available
- Additional Environmental Screening Available
- No External EMI Filter Required
- Meets MIL-STD-461C and MIL-STD-461D EMC Requirements
- Protects Against Conducted Susceptibility Specified in MIL-STD-461C, CS01 and CS02
- Flanged and Non-flanged Versions Available
- MIL-PRF-38534 Element Evaluated Components

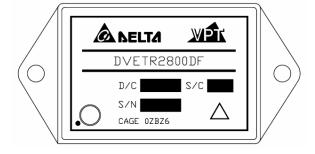


Figure 1 – DVETR2800D / DVETR2800DF DC-DC Converter (Not To Scale)



SPECIFICATIONS ( $T_{CASE} = -55^{\circ}C$  to +125°C,  $V_{IN} = +28V \pm 5\%$ , Full Load<sup>5</sup>, Unless Otherwise Specified)

**ABSOLUTE MAXIMUM RATINGS** 

 $\begin{array}{ll} \mbox{Input Voltage (Continuous)} & 50 \ \mbox{V}_{DC} \\ \mbox{Input Voltage (Transient, 1 second)} & 80 \ \mbox{Volts} \\ \mbox{Output Power}^1 & 40 \ \mbox{Watts} \\ \end{array}$ 

Power Dissipation (Full Load, T<sub>CASE</sub> = +125°C) 14 Watts

Junction Temperature Rise to Case

Storage Temperature

+15°C -65°C to +150°C

Lead Solder Temperature (10 seconds)

270°C

Weight (Maximum) (Un-Flanged / Flanged)

(51 / 55) Grams

Parameter		Conditions	D	VETR2805	D	D	VETR2812	D	Units
		Conditions	Min	Тур	Max	Min	Тур	Max	Units
STATIC									
INPUT		Continuous	15	28	50	15	28	50	V
Voltage <sup>4</sup>		Transient, 1 sec	-	-	80	-	-	80	V
Current		Inhibited	-	-	6	-	-	6	mA
Ourient		No Load	-	-	90	-	-	90	mA
Inhibit Pin Input <sup>4</sup>			0	-	1.5	0	-	1.5	V
Inhibit Pin Open Circuit	Voltage⁴		9.0	11.0	13.0	9.0	11.0	13.0	V
UVLO Turn On			13.5	-	14.8	13.5	-	14.8	V
UVLO Turn Off⁴			11.0	-	14.5	11.0	-	14.5	V
	+V <sub>OUT</sub>	T <sub>CASE</sub> = 25°C	4.95	5.0	5.05	11.88	12.0	12.12	V
OUTPUT	$+V_{OUT}$	$T_{CASE}$ = -55°C to +125°C	4.925	5.0	5.075	11.82	12.0	12.18	V
Voltage⁵	$-V_{OUT}$	T <sub>CASE</sub> = 25°C	4.80	5.0	5.20	11.80	12.0	12.20	V
	$-V_{OUT}$	$T_{CASE}$ = -55°C to +125°C	4.70	5.0	5.30	11.64	12.0	12.36	V
Power <sup>3,6</sup>	Total		0	-	30	0	-	40	W
FOWEI	$\pm V_{\text{OUT}}$	Either Output	0	-	21	0	-	28	W
Current <sup>3,6</sup>	±V <sub>OUT</sub>	Either Output	0	-	4.2	0	-	2.33	Α
Ripple Voltage	±V <sub>OUT</sub>	Full Load <sup>5</sup> , 20Hz to 10MHz	-	-	60	-	-	50	mV <sub>p-p</sub>
Line Degulation	+V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-	-	20	-	-	20	mV
Line Regulation -V <sub>OU</sub>		V <sub>IN</sub> = 16V to 40V	-	-	200	-	-	200	mV
Load Regulation	+V <sub>OUT</sub>	No Load to Full Load <sup>5</sup>	-	-	50	-	-	50	mV
Load Regulation	-V <sub>OUT</sub>	No Load to Full Load <sup>5</sup>	-	-	200	-	-	200	mV
Cross Regulation	-V <sub>OUT</sub>	+Load 70%, -Load 30% +Load 30%, -Load 70%	-	-	650	-	-	650	mV
EFFICIENCY		Full Load⁵	70	-	-	74	-	-	%
LOAD FALL T DOWED DIO	CIDATION	Overload <sup>4</sup>	-	-	16	-	-	14	W
LOAD FAULT POWER DIS	SIPATION	Short Circuit	-	-	16	-	-	14	W
CAPACITIVE LOAD <sup>4</sup>		Either Output	-	-	500	-	-	500	μF
SWITCHING FREQUENCY			400	500	550	400	500	550	kHz
ISOLATION		500 V <sub>DC</sub> , T <sub>CASE</sub> = 25°C	100	-	-	100	-	-	ΜΩ
MTBF (MIL-HDBK-217F)		AIF @ T <sub>C</sub> = 55°C	-	413	-	-	413	-	kHrs

See notes next page.



SPECIFICATIONS (T<sub>CASE</sub> = -55°C to +125°C, V<sub>IN</sub> = +28V ± 5%, Full Load<sup>5</sup>, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS							
Input Voltage (Continuous)	50 V <sub>DC</sub>	Junction Temperature Rise to Case	+15°C				
Input Voltage (Transient, 1 second)	80 Volts	Storage Temperature	-65°C to +150°C				
Output Power <sup>1</sup>	40 Watts	Lead Solder Temperature (10 seconds)	270°C				
Power Dissipation (Full Load, T <sub>CASE</sub> = +125°C)	14 Watts	Weight (Maximum) (Un-Flanged / Flanged)	(51 / 55) Grams				

Parameter		Conditions	DVETR2805D		DVETR2812D			Units	
		Conditions	Min	Min Typ Max			Тур	Max	Offics
DYNAMIC									
Load Step Output Transient	$\pm V_{\text{OUT}}$	Half Load to Full Load	-	-	500	-	-	600	$mV_{PK}$
Load Step Recovery <sup>2</sup>		Tiali Load to Full Load	-	-	350	-	-	400	μSec
Line Step Output Transient4	±V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-	150	600	-	850	1200	$mV_{PK}$
Line Step Recovery <sup>2, 4</sup>	_	V <sub>IN</sub> = 16V (0 40V	-	150	500	-	300	500	μSec
Turn On Delay	±V <sub>OUT</sub>	\/ = 0\/ to 29\/	-	-	20	-	-	20	mSec
Turn On Overshoot	ershoot $V_{IN} = 0V \text{ to } 28V$		-	-	50	-	-	50	$mV_{PK}$

Notes: 1. Dependant on output voltage.

2. Time for output voltage to settle within 1% of its nominal value.

Derate linearly to 0 at 135°C.
 Verified by qualification testing.
 Half load at +V<sub>OUT</sub> and half load at -V<sub>OUT</sub>.
 Up to 70% of the total power or current can be drawn from any one of the two outputs.



SPECIFICATIONS ( $T_{CASE} = -55^{\circ}C$  to +125°C,  $V_{IN} = +28V \pm 5\%$ , Full Load<sup>5</sup>, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS

 $\begin{array}{lll} \mbox{Input Voltage (Continuous)} & 50 \ \mbox{V}_{DC} \\ \mbox{Input Voltage (Transient, 1 second)} & 80 \ \mbox{Volts} \\ \mbox{Output Power}^1 & 40 \ \mbox{Watts} \\ \mbox{Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}$C)} & 14 \ \mbox{Watts} \\ \end{array}$ 

Junction Temperature Rise to Case +15°C
Storage Temperature -65°C to +150°C

Lead Solder Temperature (10 seconds) 270°C

Weight (Maximum) (Un-Flanged / Flanged) (51 / 55) Grams

Parameter		Conditions	D	VETR2815	D	Units
		Conditions	Min	Тур	Max	Units
STATIC						
INPUT		Continuous	15	28	50	V
Voltage⁴		Transient, 1 sec	-	-	80	V
Current		Inhibited	-	-	6	mA
Current		No Load	-	-	90	mA
Inhibit Pin Input <sup>4</sup>			0	-	1.5	V
Inhibit Pin Open Circuit	Voltage <sup>4</sup>		9.0	11.0	13.0	V
UVLO Turn On			13.5	-	14.8	V
UVLO Turn Off⁴			11.0	-	14.5	V
	+V <sub>OUT</sub>	T <sub>CASE</sub> = 25°C	14.85	15.0	15.15	V
OUTPUT	+V <sub>OUT</sub>	T <sub>CASE</sub> = -55°C to +125°C	14.70	15.0	15.30	V
Voltage⁵	-V <sub>OUT</sub>	T <sub>CASE</sub> = 25°C	14.70	15.0	15.30	V
	-V <sub>OUT</sub>	T <sub>CASE</sub> = -55°C to +125°C	14.55	15.0	15.45	V
Power <sup>3,6</sup>	Total		-	-	40	W
	$\pm V_{OUT}$	Either Output	-	-	28	W
Current <sup>3,6</sup>	±V <sub>OUT</sub>	Either Output	-	-	1.87	Α
Ripple Voltage	±V <sub>OUT</sub>	Full Load <sup>5</sup> , 20Hz to 10MHz	-	-	50	mV <sub>p-p</sub>
	+V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-	-	20	mV
Line Regulation	-V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-	-	200	mV
	+V <sub>OUT</sub>	No Load to Full Load⁵	-	-	50	mV
Load Regulation	-V <sub>OUT</sub>	No Load to Full Load⁵	-	-	200	mV
Cross Regulation	-V <sub>OUT</sub>	+Load 70%, -Load 30% +Load 30%, -Load 70%	-	-	650	mV
EFFICIENCY		Full Load <sup>5</sup>	75	-	-	%
LOAD FALL T DOWNER DIO	OIDATION	Overload <sup>4</sup>	-	-	14	W
LOAD FAULT POWER DIS	SIPATION	Short Circuit	-	-	14	W
CAPACITIVE LOAD <sup>4</sup>		Either Output	-	-	500	μF
SWITCHING FREQUENCY	,		400	500	550	kHz
ISOLATION		500 V <sub>DC</sub> , T <sub>CASE</sub> = 25°C	100	-	-	ΜΩ
MTBF (MIL-HDBK-217F)		AIF @ T <sub>C</sub> = 55°C	-	413	-	kHrs

See notes next page.



SPECIFICATIONS (T<sub>CASE</sub> = -55°C to +125°C, V<sub>IN</sub> = +28V ± 5%, Full Load<sup>5</sup>, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS							
Input Voltage (Continuous)	50 V <sub>DC</sub>	Junction Temperature Rise to Case	+15°C				
Input Voltage (Transient, 1 second)	80 Volts	Storage Temperature	-65°C to +150°C				
Output Power <sup>1</sup>	40 Watts	Lead Solder Temperature (10 seconds)	270°C				
Power Dissipation (Full Load, T <sub>CASE</sub> = +125°C)	14 Watts	Weight (Maximum) (Un-Flanged / Flanged)	(51 / 55) Grams				

Parameter		Conditions	D	VETR2815	Units	
		Conditions	Min	Тур	Max	Offics
DYNAMIC						
Load Step Output Transient	$\pm V_{\text{OUT}}$	Half Load to Full Load	-	-	600	$mV_{PK}$
Load Step Recovery <sup>2</sup>		Hall Load to Full Load	-	-	300	μSec
Line Step Output Transient <sup>4</sup>	±V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-	850	1200	$mV_{PK}$
Line Step Recovery <sup>2, 4</sup>		V <sub>IN</sub> = 16V to 40V	-	300	500	μSec
Turn On Delay	±V <sub>OUT</sub>	V <sub>IN</sub> = 0V to 28V	-	-	20	mSec
Turn On Overshoot		V <sub>IN</sub> = UV (U ZOV	-	-	50	$mV_{PK}$

Notes: 1. Dependant on output voltage.

2. Time for output voltage to settle within 1% of its nominal value.

3. Derate linearly to 0 at 135°C.

4. Verified by qualification testing.

5. Half load at +V<sub>OUT</sub> and half load at -V<sub>OUT</sub>.
6. Up to 70% of the total power or current can be drawn from any one of the two outputs.



### **BLOCK DIAGRAM**

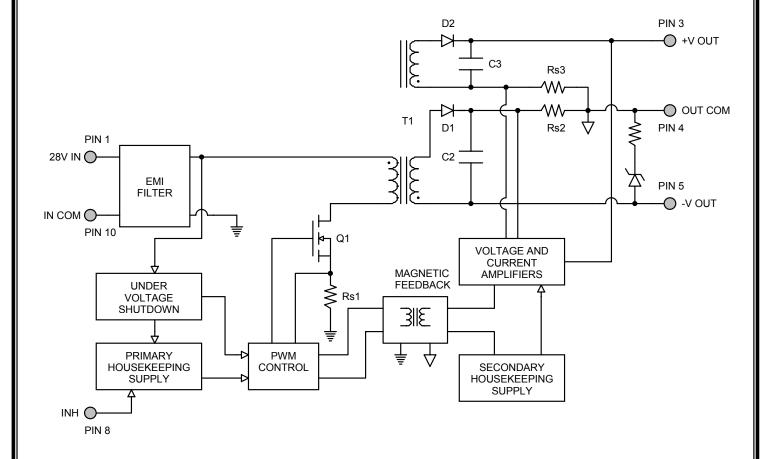


Figure 2



### **CONNECTION DIAGRAM**

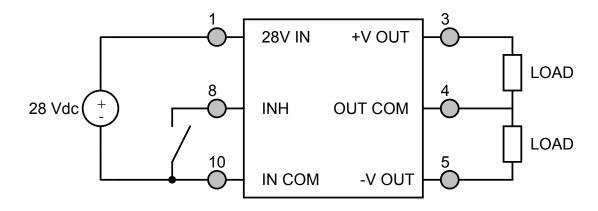
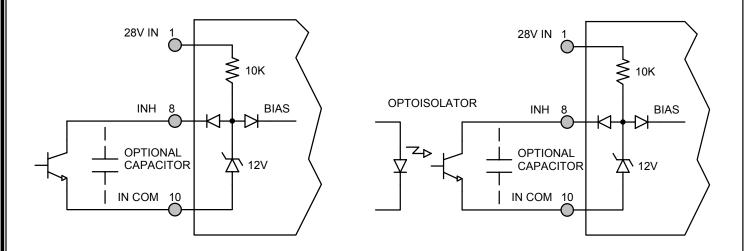


Figure 3

### INHIBIT DRIVE CONNECTION DIAGRAMS



**Figure 4** – Internal Inhibit Circuit and Recommended Drive (Shown with optional capacitor for turn-on delay)

Figure 5 – Isolated Inhibit Drive (Shown with optional capacitor for turn-on delay)



EFFICIENCY PERFORMANCE CURVES (T<sub>CASE</sub> = 25°C, Full Load, Unless Otherwise Specified)

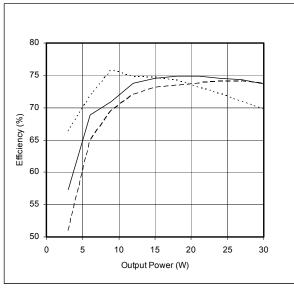


Figure 6 – DVETR2805D Efficiency (%) vs. Output Power (W)

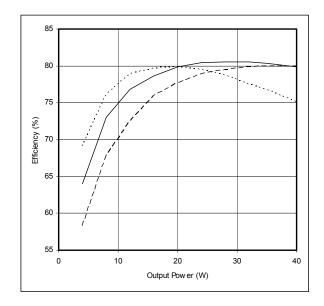


Figure 7 – DVETR2812D Efficiency (%) vs. Output Power (W)

8

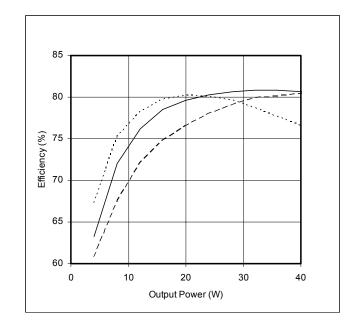


Figure 8 – DVETR2815D Efficiency (%) vs. Output Power (W)



### EMI PERFORMANCE CURVES

 $(T_{CASE} = 25^{\circ}C, V_{IN} = +28V \pm 5\%, Full Load, Unless Otherwise Specified)$ 

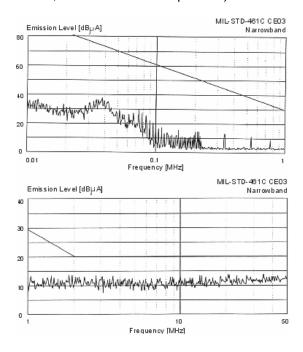


Figure 9 – MIL-STD-461C DVETR2800D

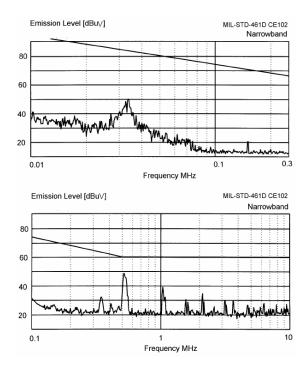
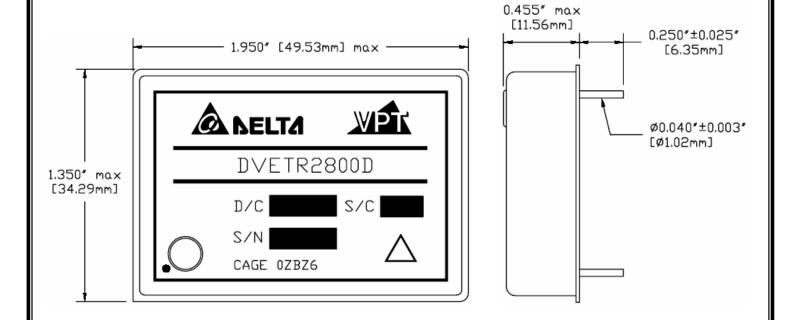


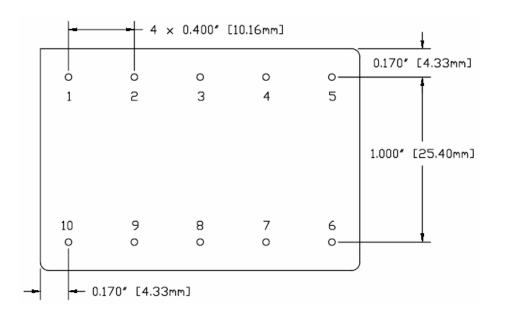
Figure 10 – MIL-STD-461D DVETR2800D



PACKAGE SPECIFICATIONS (NON-FLANGED, SOLDER SEAL)



TOP VIEW SIDE VIEW



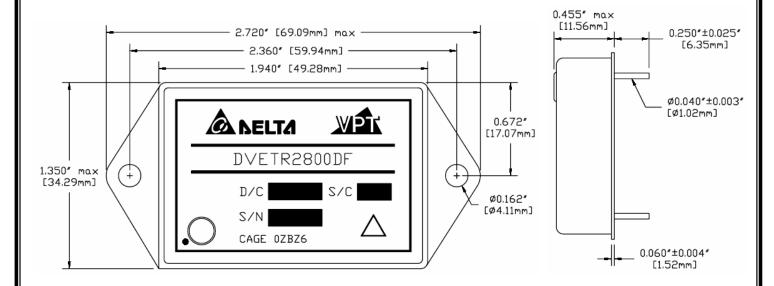
PIN	FUNCTION
1	28V IN
2	N/C
3	+V OUT
4	OUT COM
5	-V OUT
6	N/C
7	CASE
8	INHIBIT
9	N/C
10	IN COM

#### **BOTTOM VIEW**

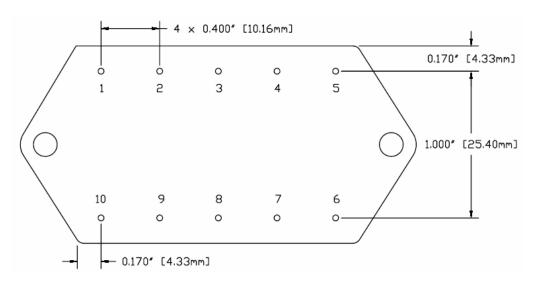
Figure 11 – Non-Flanged, Solder Seal Tin Plated Package and Pinout (Dimensional Limits are ±0.005" Unless Otherwise Stated)



### PACKAGE SPECIFICATIONS (FLANGED, SOLDER SEAL)



### TOP VIEW



#### **SIDE VIEW**

PIN	FUNCTION
1	28V IN
2	N/C
3	+V OUT
4	OUT COM
5	-V OUT
6	N/C
7	CASE
8	INHIBIT
9	N/C
10	IN COM

**BOTTOM VIEW** 

**Figure 12** – Flanged, Solder Seal Tin Plated Package and Pinout (Dimensional Limits are ±0.005" Unless Otherwise Stated)

### PACKAGE PIN DESCRIPTION

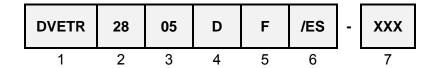
Pin	Function	Description			
1	28V IN	Positive Input Voltage Connection			
2	N/C	No Connection			
3	+V OUT	Positive Output Voltage Connection			
4	OUT COM	Output Common Connection			
5	-V OUT	Negative Output Voltage Connection			
6	N/C	No Connection			
7	CASE	Case Connection			
8	INHIBIT	Logic Low = Disabled Output. Connecting the inhibit pin to input common causes converter shutdown.  Logic High = Enabled Output. Unconnected or open collector TTL.			
9	N/C	No Connection			
10	IN COM	Input Common Connection			



ENVIRONMENTAL SCREENING (100% Tested Per MIL-STD-883 as referenced to MIL-PRF-38534)

Screening	MIL-STD-883	Standard (No Suffix)	Extended /ES
Non- Destructive Bond Pull	Method 2023	•	•
Internal Visual	Method 2017, 2032 Internal Procedure	•	•
Temperature Cycling	Method 1010, Condition C Method 1010, -55°C to 125°C		•
Constant Acceleration	Method 2001, 3000g, Y1 Direction Method 2001, 500g, Y1 Direction		•
PIND	Method 2020, Condition A <sup>2</sup>		
Pre Burn-In Electrical	100% at 25°C		
Burn-In	Method 1015, 320 hours at +125°C Method 1015, 160 hours at +125°C 96 hours at +125°C 24 hours at +125°C	•	•
Final Electrical	MIL-PRF-38534, Group A <sup>1</sup> 100% at 25°C	•	•
Hermeticity	Method 1014, Fine Leak, Condition A Method 1014, Gross Leak, Condition C Dip (1 x 10 <sup>-3</sup> )	•	•
Radiography	Method 2012 <sup>3</sup>		
External Visual	Method 2009	•	•

### ORDERING INFORMATION



(1) (2) (3)

Product Series		al Input tage	Output	Voltage	Number of Outputs	
DVETR	28	28 Volts	05 12 15	± 5 Volts ± 12 Volts ± 15 Volts	D	Dual

(5) (6)

Package Option		Screenir	ng Code <sup>1</sup>	Additional Screening Code	
None	Non-Flanged	None	Standard	Contact Sales	
F	Flanged	/ES	Extended		

Notes: 1. VPT Inc. reserves the right to ship higher screened or SMD products to meet lower screened orders at our sole discretion unless specifically forbidden by customer contract.

Please contact your sales representative or the VPT Inc. Sales Department for more information concerning additional environmental screening and testing, different input voltage, output voltage, power requirement, source inspection, and/or special element evaluation for space or other higher quality applications.



### **CONTACT INFORMATION**

To request a quotation or place orders please contact your sales representative or the VPT Inc. Sales Department at:

**Phone**: (425) 353-3010 **Fax**: (425) 353-4030

**E-mail**: vptsales@vpt-inc.com

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