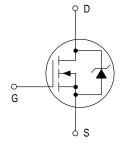
Product Preview WaveFET™ Power Surface Mount Products HDTMOS Single N-Channel Field Effect Transistor



WaveFET[™] devices are an advanced series of power MOSFETs which utilize Motorola's latest MOSFET technology process to achieve the lowest possible on–resistance per silicon area. They are capable of withstanding high energy in the avalanche and commutation modes and the drain–to–source diode has a very low reverse recovery time. WaveFET[™] devices are designed for use in low voltage, high speed switching applications where power efficiency is important. Typical applications are dc–dc converters, and power management in portable and battery powered products such as computers, printers, cellular and cordless phones. They can also be used for low voltage motor controls in mass storage products such as disk drives and tape drives. The avalanche energy is specified to eliminate the guesswork in designs where inductive loads are switched and offer additional safety margin against unexpected voltage transients.

- Characterized Over a Wide Range of Power Ratings
- Ultralow R_{DS(on)} Provides Higher Efficiency and Extends Battery Life in Portable Applications
- Logic Level Gate Drive Can Be Driven by Logic ICs
- Diode Is Characterized for Use In Bridge Circuits
- Diode Exhibits High Speed, With Soft Recovery
- IDSS Specified at Elevated Temperature
- Avalanche Energy Specified
- Industry Standard DPAK Surface Mount Package





SINGLE TMOS POWER MOSFET 30 VOLTS RDS(on) = 10 mΩ



CASE 369A-13, Style 2 DPAK



MAXIMUM RATINGS (T_J = 25° C unless otherwise specified)

Parameter		Value	Unit
Drain-to-Source Voltage	VDSS	30	Vdc
Drain-to-Gate Voltage	VDGR	30	Vdc
Gate-to-Source Voltage	VGS	±20	Vdc
Operating and Storage Temperature Range	TJ, Tstg	-55 to 150	°C
Single Pulse Drain–to–Source Avalanche Energy — Starting T _J = 25°C (V _{DD} = 25 Vdc, V _{GS} = 10 Vdc, L = 126 mH, I _{L(pk)} = 3.0 A, V _{DS} = 30 Vdc)	E _{AS}	500	mJ

DEVICE MARKING

ORDERING INFORMATION

D3303	Device	Device Reel Size Tape V		Quantity
D3302	D3302 MTD3302T4		12 mm embossed tape	2500

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MTD3302

POWER RATINGS (T_J = 25° C unless otherwise specified)

Parameter		Symbol	Value	Unit
Drain Current — Continuous @ $T_A = 25^{\circ}C$ — Continuous @ $T_A = 100^{\circ}C$ — Single Pulse (tp $\leq 10 \ \mu s$)	Mounted on heat sink T _{case} = 25°C	I _D ID IDM	30 30 70	Adc Adc Adc
Total Power Dissipation @ T _A = 25°C Linear Derating Factor	V _{GS} = 10 Vdc	PD	96 769	Watts mW/°C
Thermal Resistance — Junction-to-Case	Steady State	R _θ JC	1.3	°C/W
Continuous Source Current (Diode Conduction)		IS	2.0	Adc

Parameter		Symbol	Value	Unit
Drain Current — Continuous @ $T_A = 25^{\circ}C$ — Continuous @ $T_A = 100^{\circ}C$ — Single Pulse (tp $\leq 10 \ \mu$ s)	Mounted on 1 inch square FR–4 or G10 board	I _D I _D I _{DM}	10.8 6.6 70	Adc Adc Adc
Total Power Dissipation @ T _A = 25°C Linear Derating Factor	V _{GS} = 10 Vdc	PD	1.8 14	Watts mW/°C
Thermal Resistance — Junction-to-Ambient	Steady State	R _{θJA}	71.4	°C/W
Continuous Source Current (Diode Conduction)		١s	2.0	Adc

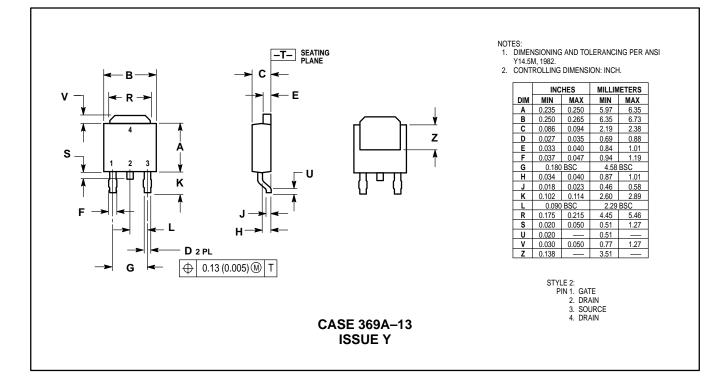
Parameter		Symbol	Value	Unit
Drain Current — Continuous @ $T_A = 25^{\circ}C$ — Continuous @ $T_A = 100^{\circ}C$ — Single Pulse (tp $\leq 10 \ \mu s$)	Mounted on minimum recommended FR–4 or G10 board	I _D I _D I _{DM}	8.3 5.2 60	Adc Adc Adc
Total Power Dissipation @ T _A = 25°C Linear Derating Factor	V _{GS} = 10 Vdc	PD	1.0 8.3	Watts mW/°C
Thermal Resistance — Junction-to-Ambient	Steady State	$R_{\theta JA}$	120	°C/W
Continuous Source Current (Diode Conduction)		١s	2.0	Adc

ELECTRICAL CHARACTERISTICS (T_J = 25° C unless otherwise specified)

Characteristic		Symbol	Min	Тур	Мах	Unit
OFF CHARACTERISTICS				•	•	
Drain-to-Source Breakdown Volta	ge	V(BR)DSS				Vdc
(V _{GS} = 0 Vdc, I _D = 250 μAdc) Temperature Coefficient (Positive)			30	33 23	_	mV/°C
				20		μAdc
Zero Gate Voltage Drain Current ($V_{DS} = 30 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}$) ($V_{DS} = 30 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, T_J = 125^{\circ}\text{C}$)		IDSS	_	0.02	1.0	μπου
			—	0.5	10	
Gate–Body Leakage Current (V_GS = \pm 20 Vdc, V _{DS} = 0 Vdc)		IGSS	_	-	±100	nAdc
ON CHARACTERISTICS ⁽¹⁾						
Gate Threshold Voltage		VGS(th)				Vdc
$(V_{DS} = V_{GS}, I_D = 250 \mu Adc)$ Threshold Temperature Coefficie	nt (Negative)		1.0	1.9 4.7	_	mV/°C
			_	4.7	_	
Static Drain-to-Source On-Resist (V _{GS} = 10 Vdc, I _D = 10 Adc)	ance	R _{DS(on)}	_	8.9	10	mΩ
$(V_{GS} = 4.5 \text{ Vdc}, I_D = 5.0 \text{ Adc})$			_	13	16	
Forward Transconductance (V_{DS} = 15 Vdc, I_D = 10 Adc)		9FS	5	13	—	Mhos
YNAMIC CHARACTERISTICS						
Input Capacitance		C _{iss}	_	1810	—	pF
Output Capacitance	$(V_{DS} = 24 \text{ Vdc}, V_{GS} = 0 \text{ Vdc},$	C _{OSS}	_	165	_	
Transfer Capacitance	f = 1.0 MHz)	C _{rss}	_	595	_	
WITCHING CHARACTERISTICS	2)	100				
Turn–On Delay Time		^t d(on)	_	9	_	ns
Rise Time	(V _{DD} = 25 Vdc, I _D = 1.0 Adc,	t _r	_	10		-
Turn–Off Delay Time		td(off)	_	60		
Fall Time	RG = 0.0.22	t _f		43		
Turn–On Delay Time			_	18		ns
Rise Time	(V _{DD} = 25 Vdc, I _D = 1.0 Adc,	^t d(on)	_	32		113
	$V_{GS} = 4.5 \text{ Vdc},$	t _r		-	—	
Turn–Off Delay Time	$R_{G} = 6.0 \Omega$)	^t d(off)	_	42	—	
Fall Time		tf	_	44	—	
Gate Charge	(V _{DS} = 15 Vdc, I _D = 2.0 Adc,	QT		46	60	nC
		Q ₁	_	5.3	—	-
	$V_{GS} = 10 \text{ Vdc})$	Q ₂	—	10.7	—	
		Q ₃	—	10.3	—	
OURCE-DRAIN DIODE CHARAC	TERISTICS					
Forward On–Voltage (1)		V _{SD}				Vdc
	(I _S = 2.3 Adc, V _{GS} = 0 Vdc) (I _S = 2.3 Adc, V _{GS} = 0 Vdc, T _J = 125°C)		_	0.75 0.58	1.1	
Reverse Recovery Time		+				
Reverse Recovery Time	(I _S = 2.3 Adc, V _{GS} = 0 Vdc, dI _S /dt = 100 A/μs)	t _{rr}	_	36	—	ns
		^t a	_	21	—	-
		tb	—	15	—	
Reverse Recovery Stored Charge		Q _{RR}	_	0.041	-	μC

Pulse Test: Pulse Width ≤ 300 µs, Duty Cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperatures.

PACKAGE DIMENSIONS



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