

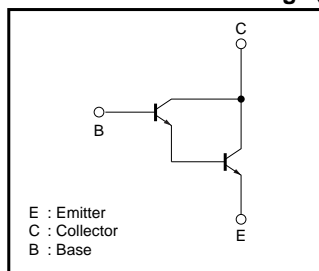
High-gain Amplifier Transistor (32V, 0.3A)

2SC2062S

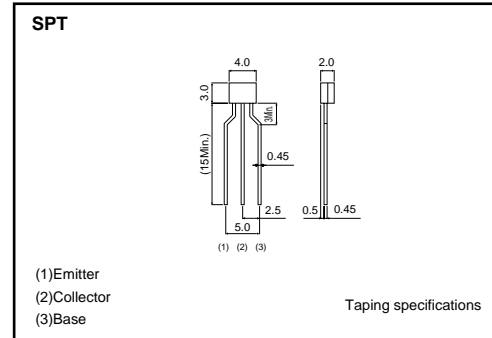
●Features

- 1) Darlington connection for a high h_{FE} .
(DC current gain = 5000 (Min.) at $V_{CE} = 3V$, $I_c = 0.1A$.)
- 2) High input impedance.

●Absolute maximum ratings (Ta=25°C)



●External dimensions (Unit : mm)



●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-base voltage	V_{CBO}	40	V
Collector-emitter voltage	V_{CES}	32	V
Emitter-base voltage	V_{EBO}	12	V
Collector current	I_c	0.3	A
Collector power dissipation	P_c	0.3	W
Junction temperature	T_j	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-base breakdown voltage	BV_{CBO}	40	-	-	V	$I_c = 100\mu A$
Collector-emitter breakdown voltage	BV_{CES}	32	-	-	V	$I_c = 10mA$
Emitter-base breakdown voltage	BV_{EBO}	12	-	-	V	$I_E = 100\mu A$
Collector cutoff current	I_{CBO}	-	-	0.1	μA	$V_{CB} = 30V$
Emitter cutoff current	I_{EBO}	-	-	0.1	μA	$V_{EB} = 12V$
DC current transfer ratio	h_{FE}	10000	-	-	-	$V_{CE}/I_c = 3V/0.1A$
Collector-emitter saturation voltage	$V_{CE(sat)}$	-	-	1.4	V	$I_c/I_B = 200mA/0.2mA$
Transition frequency	f_t	-	200	-	MHz	$V_{CE} = 5V, I_E = -10mA, f = 100MHz$
Output capacitance	C_{ob}	-	2.5	-	pF	$V_{CB} = 10V, I_E = 0A, f = 1MHz$

* Transition frequency of the device.

●Packaging specifications and h_{FE}

Type	2SC2062S
Package	SPT
h_{FE}	C
Code	TP
Basic ordering unit (pieces)	5000

Transistors

●Electrical characteristics curves

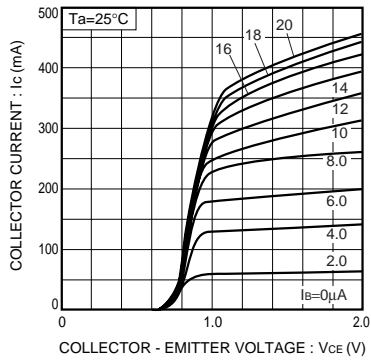


Fig.1 Typical output characteristics (I)

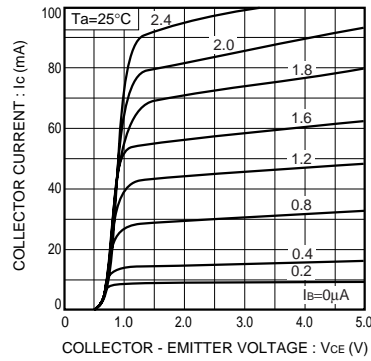


Fig.2 Typical output characteristics (II)

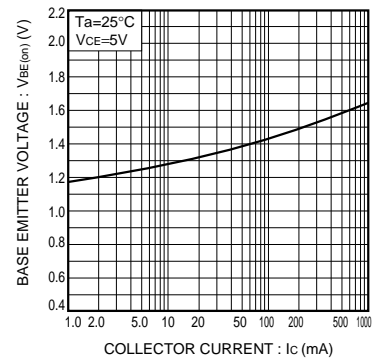


Fig.3 Base emitter 'ON' voltage vs. collector current

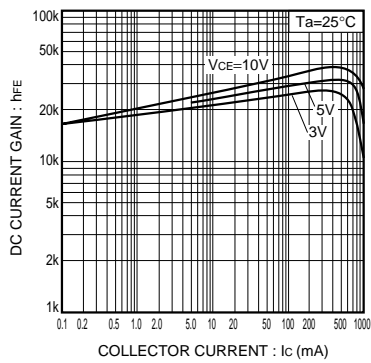


Fig.4 DC current gain vs. collector current (I)

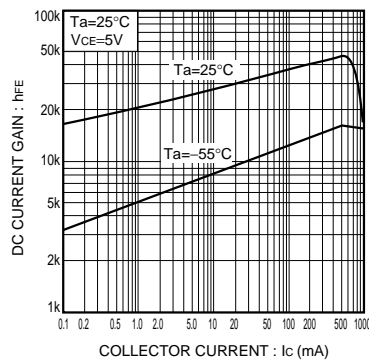


Fig.5 DC current gain vs. collector current (II)

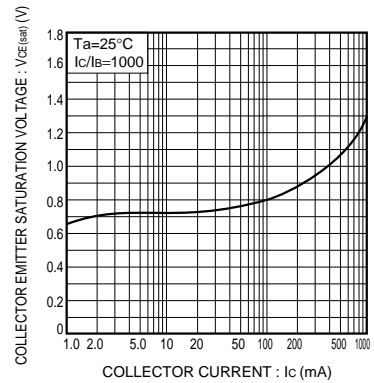


Fig.6 Collector emitter saturation voltage vs. collector current

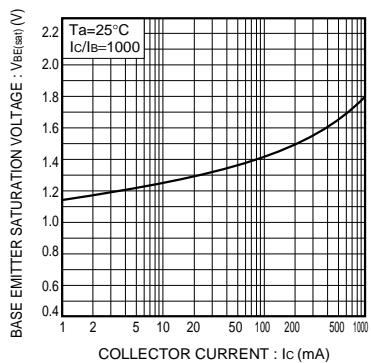


Fig.7 Base emitter saturation voltage vs. collector current

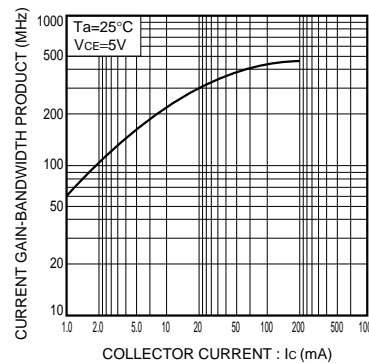


Fig.8 Current gain-bandwidth product vs. collector current

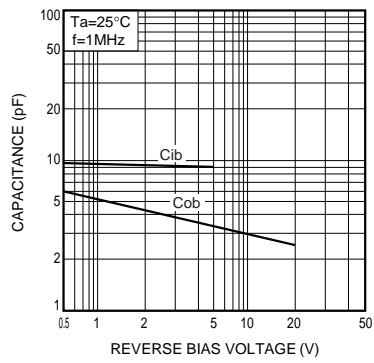


Fig.9 Capacitance vs. reverse bias voltage

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